

**512Kx8 Static RAM
CMOS, Monolithic**

ADVANCE INFORMATION

Features

The EDI88512C is a 4 megabit Monolithic CMOS Static RAM.

The 32 pin DIP pinout adheres to the JEDEC standard for the four megabit device and is a pin for pin replacement for the 512Kx8 module, EDI8M8512C. All three 32 pin packages are pin for pin upgrades for the single chip enable 128K x 8, the EDI88128C. Pins 1 and 30 become the higher order addresses.

A Low Power version with Data Retention (EDI88512LP) is also available for battery backed applications.

Ceramic military product compliant to MIL-STD-883, paragraph 1.2.1 is available.

A non-compliant device is available in a plastic dual-in-line package which is tested over the full military temperature range, -55°C to +125°C, and undergoes 160 hours of burn-in at 125°C or the equivalent.

512Kx8 bit CMOS Static Random Access Memory

- Access Times: 55*, 70, 85 and 100ns
- Data Retention Function (LP version)
- TTL Compatible Inputs and Outputs
- Fully Static, No Clocks

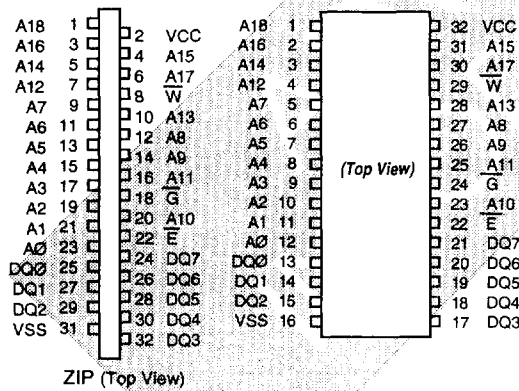
32 lead, JEDEC Approved Pinout

- Ceramic Sidebrazed DIP, No. 9
- Ceramic ZIP, No. 100
- Ceramic SOJ, No. 140
- Plastic DIP, No. 11

Single +5V (±10%) Supply Operation

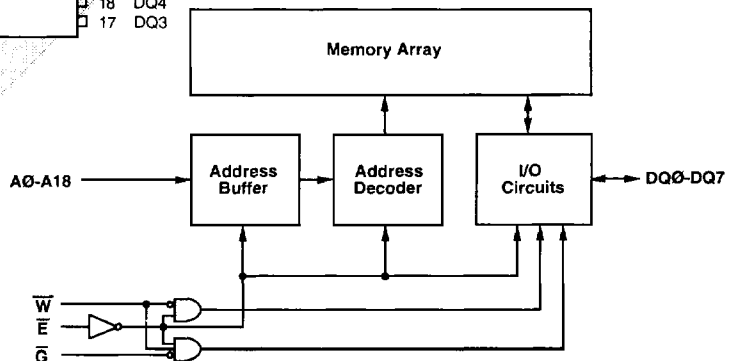
*Advance Information

Pin Configuration and Block Diagram



Pin Names

- A0-A18 Address Inputs
- E Chip Enable
- W Write Enable
- G Output Enable
- DQ0-DQ7 Common Data Input/Output
- VCC Power (+5V±10%)
- VSS Ground



Ordering Information
Military

Part No. Standard Power	Speed (ns)	Leads	Package Style	No.
EDI88512C55CB	55	32	Dip	9
EDI88512C70CB	70	32	DIP	9
EDI88512C85CB	85	32	DIP	9
EDI88512C100CB	100	32	DIP	9
EDI88512C55NB	55	32	CSOJ	140
EDI88512C70NB	70	32	CSOJ	140
EDI88512C85NB	85	32	CSOJ	140
EDI88512C100NB	100	32	CSOJ	140
EDI88512C55ZB	55	32	Zip	100
EDI88512C70ZB	70	32	ZIP	100
EDI88512C85ZB	85	32	ZIP	100
EDI88512C100ZB	100	32	ZIP	100

For Commercial or Industrial grade product use C or I, respectively to replace B in the suffix of the part number, eg EDI88512C100CB becomes EDI88512C100CI (Industrial Temp range)

Low Power with Data Retention	Speed (ns)	Leads	Package Style	No.
EDI88512LP55CB	55	32	DIP	9
EDI88512LP70CB	70	32	DIP	9
EDI88512LP85CB	85	32	DIP	9
EDI88512LP100CB	100	32	DIP	9
EDI88512LP55NB	55	32	CSOJ	140
EDI88512LP70NB	70	32	CSOJ	140
EDI88512LP85NB	85	32	CSOJ	140
EDI88512LP100NB	100	32	CSOJ	140
EDI88512LP55ZB	55	32	ZIP	100
EDI88512LP70ZB	70	32	ZIP	100
EDI88512LP85ZB	85	32	ZIP	100
EDI88512LP100ZB	100	32	ZIP	100

Military (Temperature Range Only)

Part No. Standard Power	Speed (ns)	Leads	Package Style	No.
EDI88512C55PM	55	32	DIP	11
EDI88512C70PM	70	32	DIP	11
EDI88512C85PM	85	32	DIP	11
EDI88512C100PM	100	32	DIP	11

Military (Temperature Range Only)

Low Power with Data Retention	Speed (ns)	Leads	Package Style	No.
EDI88512LP55PM	55	32	DIP	11
EDI88512LP70PM	70	32	DIP	11
EDI88512LP85PM	85	32	DIP	11
EDI88512LP100PM	100	32	DIP	11

Industrial

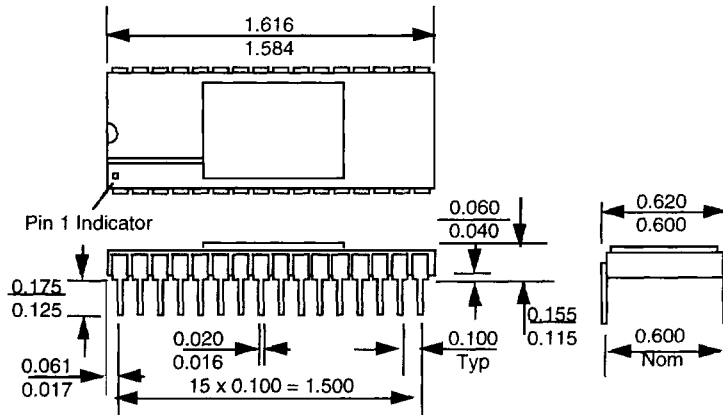
Part No. Standard Power	Speed (ns)	Leads	Package Style	No.
EDI88512C55PI	55	32	Dip	11
EDI88512C70PI	70	32	DIP	11
EDI88512C85PI	85	32	DIP	11
EDI88512C100PI	100	32	DIP	11

Industrial

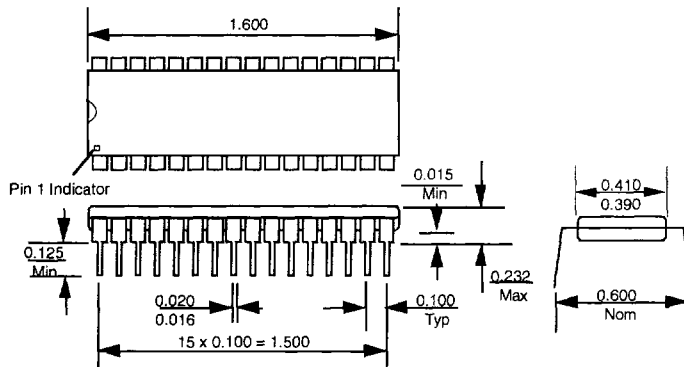
Low Power with Data Retention	Speed (ns)	Leads	Package Style	No.
EDI88512LP55PI	55	32	DIP	11
EDI88512LP70PI	70	32	DIP	11
EDI88512LP85PI	85	32	DIP	11
EDI88512LP100PI	100	32	DIP	11

Package Description

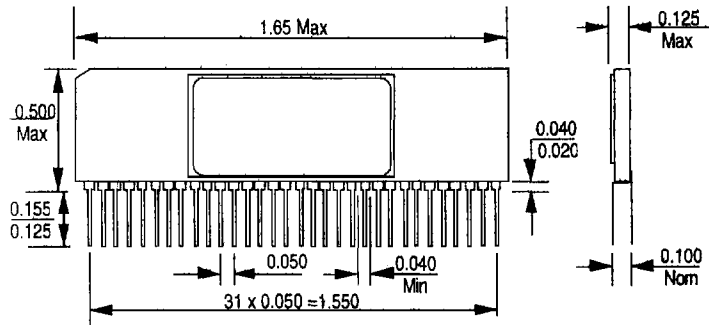
Package No. 9
32 Pin Sidebrazed Ceramic
Dual-in-line Package



Package No. 11
32 Pin Plastic
Dual-in-line Package



Package No. 100
32 Pin Ceramic
ZIP Package



Package No. 140
32 Lead Ceramic
SOJ Package

