

## PNP Silicon Transistors (Plastic Package TO-236)

Type	Marking Code	$-V_{CE0}$	$h_{FE}$	$-V_{CEsat}$		$-I_{CES}$		$f_T$	$C_{ob}$			
				at $-V_{CE}/-I_C$	at $-I_C/-I_B$	at $-V_{CE}$	at $-V_{CE}/-I_C$		at $-V_{CB}$			
		Volts		V/mA	max. V	mA/mA	max. nA	V	MHz	V/mA	max. pF	V
BC807-16	5A	45	100-250	1/100	0.7	500/50	100	25	100	5/10	12	10
BC807-25	5B	45	160-400	1/100	0.7	500/50	100	25	100	5/10	12	10
BC807-40	5C	45	250-600	1/100	0.7	500/50	100	25	100	5/10	12	10
BC808-16	5E	25	100-250	1/100	0.7	500/50	100	25	100	5/10	12	10
BC808-25	5F	45	160-400	1/100	0.7	500/50	100	25	100	5/10	12	10
BC808-40	5G	45	250-600	1/100	0.7	500/50	100	25	100	5/10	12	10
BC856A	3A	65	110-220	5/2	0.65	100/5	15	80	150	5/10	6	10
BC856B	3B	65	200-450	5/2	0.65	100/5	15	80	150	5/10	6	10
BC857A	3E	45	110-220	5/2	0.65	100/5	15	50	150	5/10	6	10
BC857B	3F	45	200-450	5/2	0.65	100/5	15	50	150	5/10	6	10
BC857C	3G	45	420-800	5/2	0.65	100/5	15	50	150	5/10	6	10
BC858A	3J	30	110-220	5/2	0.65	100/5	15	30	150	5/10	6	10
BC858B	3K	30	200-450	5/2	0.65	100/5	15	30	150	5/10	6	10
BC858C	3L	30	420-800	5/2	0.65	100/5	15	30	150	5/10	6	10
BC859A <sup>1)</sup>	4A	30	110-220	5/2	0.65	100/5	15	30	150	5/10	6	10
BC859B <sup>1)</sup>	4B	30	200-450	5/2	0.65	100/5	15	30	150	5/10	6	10
BC859C <sup>1)</sup>	4C	30	420-800	5/2	0.65	100/5	15	30	150	5/10	6	10
BC860A <sup>2)</sup>	4E	45	110-220	5/2	0.65	100/5	15	50	150	5/10	6	10
BC860B <sup>2)</sup>	4F	45	200-450	5/2	0.65	100/5	15	50	150	5/10	6	10
BC860C <sup>2)</sup>	4G	45	420-800	5/2	0.65	100/5	15	50	150	5/10	6	10
BCW61A	BA	32	120-220	5/2	0.55	50/1.25	20	32	180	5/10	6	10
BCW61B	BB	32	180-310	5/2	0.55	50/1.25	20	32	180	5/10	6	10
BCW61C	BC	32	250-460	5/2	0.55	50/1.25	20	32	180	5/10	6	10
BCW61D	BD	32	380-630	5/2	0.55	50/1.25	20	32	180	5/10	6	10
BCX71G	BG	45	120-220	5/2	0.55	50/1.25	20	45	180	5/10	6	10
BCX71H	BH	45	180-310	5/2	0.55	50/1.25	20	45	180	5/10	6	10
BCX71J	BJ	45	250-460	5/2	0.55	50/1.25	20	45	180	5/10	6	10
BCX71K	BK	45	380-630	5/2	0.55	50/1.25	20	45	180	5/10	6	10

Normally, the transistors BC807, BC808 and BC856 to BC860 have the following pin configuration: 1 = collector, 2 = base, 3 = emitter. They are also available with inverted configuration: 1 = collector, 2 = emitter, 3 = base, this being indicated by the suffix "R" next to the type designation and the marking code. Example: type BC856AR is indicated as 3AR.

Normally, the transistors BCW61 to BCX71 have the following pin configuration: 1 = collector, 2 = base, 3 = emitter. These types are also available with inverted configuration: 1 = collector, 2 = emitter, 3 = base. The type designation and marking is the following.

Type	Marking
BCW61RA	BO
BCW61RB	BP
BCW61RC	BR
BCW61RD	BS
BCX71RG	BU
BCX71RH	BW
BCX71RJ	BX
BCX71RK	BY

<sup>1)</sup> Low Noise Type

<sup>2)</sup> Extremely Low Noise Type