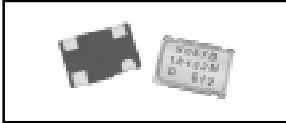




Clock Oscillators

Surface Mount
1.5MHz to 70MHz



FEATURES

- HCMOS/TTL compatible.
- Industrial Temperature range optional.

STANDARD ELECTRICAL SPECIFICATIONS

FREQUENCY RANGE (MHz)	INPUT CURRENT	WAVEFORM SYMMETRY @ 50% Vdd	RISE & FALL TIME (nS) (Typ./Max.)	"ZERO" LEVEL (Vdd)	"ONE" LEVEL (Vdd)
1.5 - 7.999	15 Max.	48/52	5/8	10%	90%
8.0 - 40.0	25 Max.	45/55	5/8	10%	90%
40.001 - 70.0	50 Max.	40/60	5/8	10%	90%

ELECTRICAL SPECIFICATIONS

Operating Temperature Range: 0°C to + 70°C (- 40°C to + 85°C optional).

Frequency Stability: ± .01% standard (± .005% optional).

Input Voltage: + 5.0 VDC, ± 0.5V (3.3V optional 50MHz & below).

Enable Input Voltage: 3.5V minimum.

Disable Input Voltage: 0.5V maximum.

Output Load: 50pF/10 TTL.

ENVIRONMENTAL SPECIFICATIONS

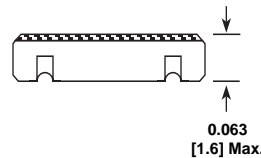
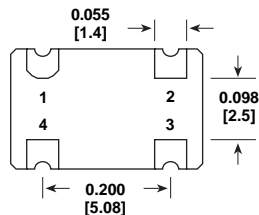
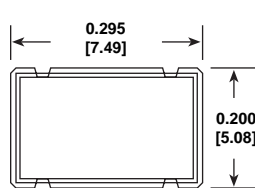
Temperature Cycle: - 55°C to + 85°C, 3 cycles.

Shock: 1000g, 0.35 millisecond, 1/2 sine wave, 3 shocks each plane.

Vibration: .06 D.A., 10 - 55Hz, 20g, 55 - 3000Hz.

Humidity: 85% relative humidity at + 85°C, 240 hours.

DIMENSIONAL CONFIGURATIONS [Numbers in brackets indicate millimeters]



Tolerance is ± .008" [± .2] unless otherwise specified.

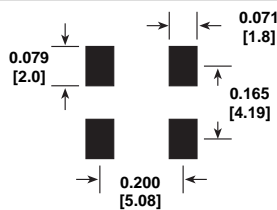
*An internal pull-up resistor is connected to Pin 1 allowing active output if Pin 1 is left open.

PIN	CONNECTION
1	E/D
2	Ground
3	Output
4	+ 5 VDC

ENABLE/DISABLE FUNCTION	
*Pin 1 E/D	Pin 3 output
Open	Active
High (1)	Active
Low (0)	High Z

RECOMMENDED SOLDER PAD LAYOUT

[Numbers in brackets indicate millimeters]



A 0.01µF bypass capacitor should be placed between Vdd and GND to minimize power supply line noise.

PACKAGING

16mm wide tape and reel packaging for automatic assembly

1000 pieces per reel

HOW TO ORDER

XOSM-57
MODEL

- 57 = 5V
- 573 = 3.3V

B
FREQUENCY STABILITY

A = .005% (50PPM)
B = .01% Std. (100PPM)

R
OTR

Blank = Standard
R = - 40°C to + 85°C

E
ENABLE/DISABLE

50M
FREQUENCY/MHz