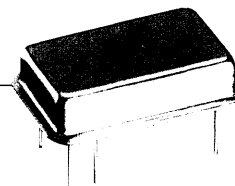


VF960/VF961

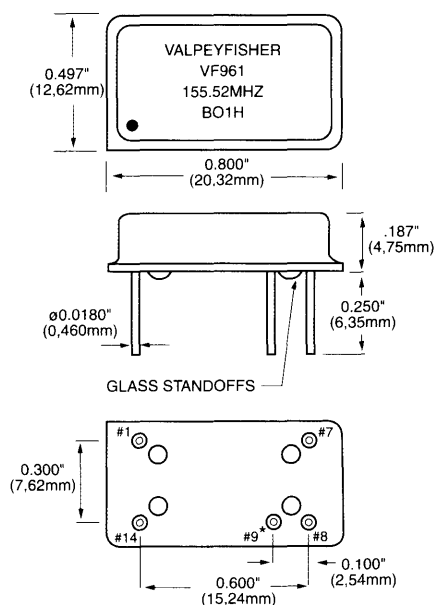


ECL/PECL Compatible Hybrid VCXO

Standard 14 Pin DIP Package

FEATURES

- No Frequency Multiplication is Used
- Very Low Phase Jitter
- Low Component Count
- Pullability (± 500 ppm available)
- Temperature Range
(-40°C to $+85^{\circ}\text{C}$) Available
- Complementary Output Available
- Wide Frequency Range



All dimensions are typical unless otherwise specified.
* Pin 9 optional

Creating a Part Number

VF960/961 ---**FREQ.**

FREQUENCY STABILITY	
Code	Specification
5	± 20 ppm
	± 25 ppm (std.)

DUTY CYCLE	
Code	Specification
H	$\pm 5\%$
	$\pm 10\%$ (std.)

INPUT VOLTAGE	
Code	Specification
L	3.3 Volt $\pm 5\%$ *
	5.0 Volt $\pm 5\%$ (std.)

(*VF961 Only)

ABSOLUTE PULL RANGE (ppm)	
Code	Specification
	±50 ppm MIN. (std.)
XXX	up to 500 ppm (customer specified)

LEAD CONFIGURATION	
Code	Specification
G	Gull Wing
	Through Hole (std.)

OUTPUT	
Code	Specification
C	Complementary 4 pin pkg Single (std.)

OPERATIONAL TEMP. RANGE	
Code	Specification
	0°C to +70°C (std.)
1	-40°C to +85°C

Example: VF960SHL-1CG-100-106.25MHz: Frequency Stability ± 20 ppm, Duty Cycle $\pm 5\%$, Input Voltage 3.3 Volt $\pm 5\%$, Operating Temperature -40°C to $+85^{\circ}\text{C}$, Complementary Output (5-pin package), Gull Wing, APR ± 100 ppm, Frequency 106.25MHz.

[illegible]

Notes:

1. Tighter duty cycle available.
2. 0V to 5V control voltage available for V_{CC} 3.3V. Nominal control voltage is 2.5V and setability is $\pm 0.5V$ in this case.
3. Wider pullability available.
4. 10% and 5% available.