

Frequency Discriminators SDLD & MDLD

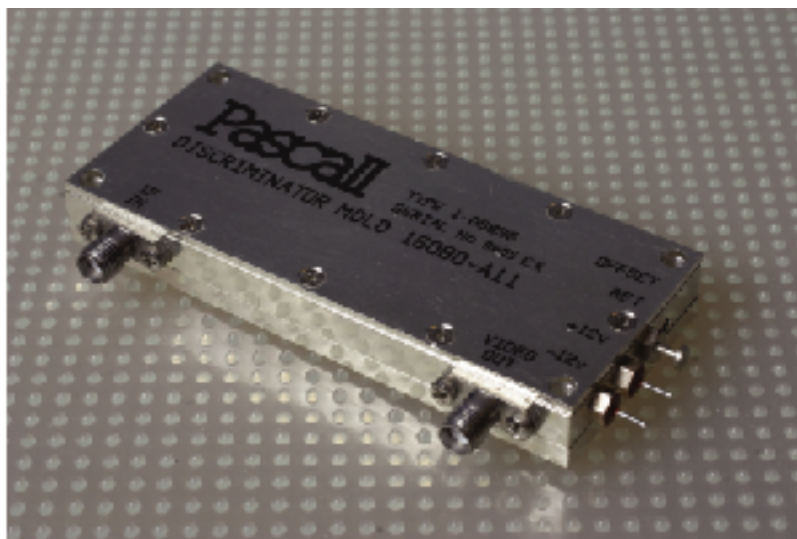
Centre frequencies

10 to 750 MHz

Frequency accuracy 5%

Linear Bandwidth 60%

Fast Rise time



The Pascall SDLD & MDLD frequency discriminators employ a discrete component design using SMD technology.

These discriminators use delay line techniques to accomplish FM demodulation; this form of demodulation is optimised to handle pulsed signals with rise time less than 50 nsecs.

The design incorporates input video amplification, supply conditioning and temperature compensation circuitry to provide good frequency accuracy over temperature range.

The units will operate from the limited IF output of our range of logarithmic amplifiers.

Frequency Discriminators SDLD & MDLD

SDLD/MDLD			DATA
Rated Input			0dBm (useable to -10 dBm)
Video Output			DC coupled into 93 ohms
Output VSWR			1.5 : 1 max
Frequency accuracy			5% max. at + 25°C 10% max -45 to +85°C
Video offset adjustment			± 200 mV min
Crossover Frequency Adjustment (SDLD only)			±5 mHz
Power	SDLD series		+ve 60mA, -ve 120mA
	MDLD series		+ve 40mA, -ve 120mA
(Above 500MHz)	MDLD series		+ve 50mA, -ve 190mA
Weight	SDLD series		105g max
	MDLD series		85g max
Size	SDLD series		117.5 x 38.1 x 11.0 mm
	MDLD series		89.7 x 38.1 x 11.7 mm

Model No. SDLD	Model No. MDLD	Centre Freq. MHz	Linear Bandwidth MHz	Peak to Peak Band Width MHz	Video O/P (mV/MHz) Nominal	Rise-Time nsec
SDLD/MDLD Series						
SDLD-1008	MDLD-1008	10	08	10	200	500
SDLD-2105	MDLD-2105	21	05	10	200	300
SDLD-3010	MDLD-3010	30	10	20	100	150
SDLD-6015	MDLD-6015	60	15	30	100	70
SDLD-7036	MDLD-7036	70	36	40	100	70
SDLD-12040	MDLD-12040	120	40	50	30	50
SDLD-16050	MDLD-16050	160	50	80	20	30
SDLD-25025	MDLD-25025	250	25	40	50	25
	MDLD-750100	750	100	450	10	20
	MDLD-750250	750	250	450	10	20

Ordering Information

