

MVLL/MVLP SERIES – Ceramic J-Lead PECL/LVPECL VCXO



- Hermetically Sealed Ceramic SMD Package
- Wide Frequency Range
- Available to -55°C to +125°C Operating
- Jitter as Low as 3 pSec



RoHS
COMPLIANT

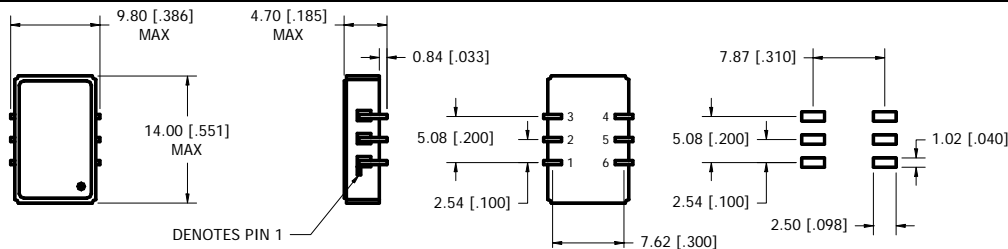
ELECTRICAL SPECIFICATIONS:

Frequency Range	70.000 MHz to 622.080 MHz	
Frequency Stability (Inclusive of Temp., Load, Voltage and Aging)	(See Part Number Guide for Options)	
Aging	±5 ppm / yr max	
Operating Temp. Range	(See Part Number Guide for Options)	
Storage Temp. Range	-55°C to +125°C	
Supply Voltage (Vdd)	+3.3 VDC ±5% (LVPECL)	+5.0 VDC ±5% (PECL)
Supply Current	60mA	85mA
Control Voltage	+1.65VDC ±1.50VDC	+2.50VDC ±2.00VDC
Logic "0"	Vdd – 1.620 VDC max	
Logic "1"	Vdd- 1.025 vdc min	
Symmetry (50% of waveform)	(See Part Number Guide for Options)	
Load	50 Ohms into Vdd-2.00 VDC	
Rise / Fall Time (20% to 80%)	5nSec max	
Start Time	5mSec max	
Jitter (RMS)	Option 3 = Less than 3 pSec / Option P = Less than 18 pSec	
Linearity	±10% max	
Pullability	(See Part Number Guide for Options)	

ENVIRONMENTAL/MECHANICAL SPECIFICATIONS:

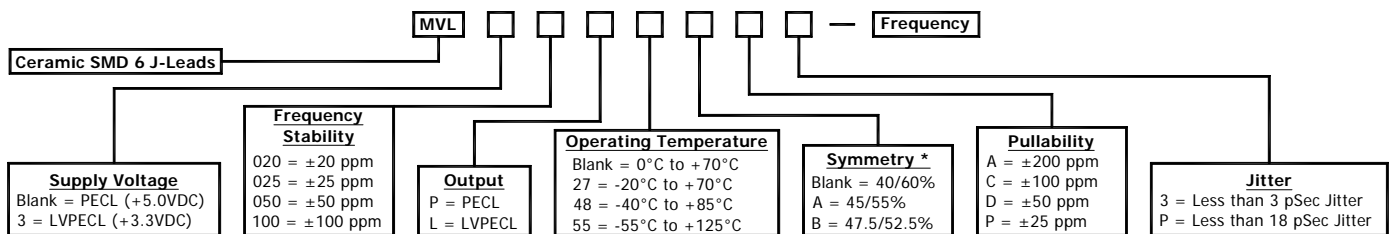
Humidity	85% RH, 85°C, 48 Hours
Hermetic Seal	Leak Rate 2×10^{-8} ATM-cm ³ /sec max
Solderability	MIL-STD-202G, Method 208
Reflow Solderability	260°C for 10 seconds
Vibration	MIL-STD-202G, Method 204 35G, 50 to 2000 Hz
Shock	MIL-STD-202G, Method 203 Test Cond E, 1000G's, ½ Sinewave
MIL-STD-883	Available with Level B Screening

MECHANICAL DIMENSIONS:



PIN CONNECTIONS	
PIN 1	VOLTAGE CONTROL
PIN 2	ENABLE/DISABLE
PIN 3	GROUND
PIN 4	OUTPUT
PIN 5	COMP. OUTPUT
PIN 6	SUPPLY VOLTAGE

PART NUMBERING GUIDE:



*Option B (47.5/52.5%) Available up to MHz only
Please Consult with MMD sales department for any other parameters or options.

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