

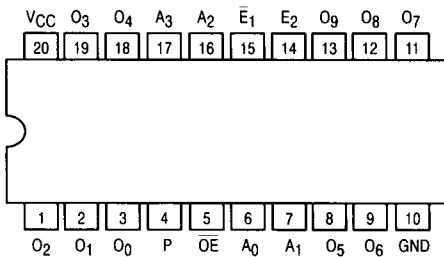


# 1-OF-10 DECODER WITH 3-STATE OUTPUTS

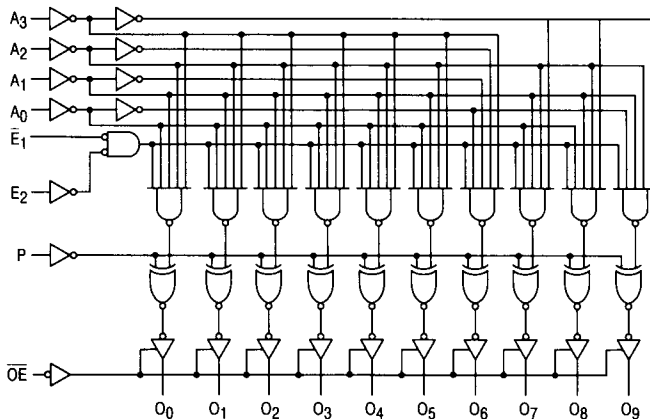
The MC54/74F537 is a one-of-ten decoder/demultiplexer with four active HIGH BCD inputs and ten mutually exclusive outputs. A polarity control input determines whether the outputs are active LOW or active HIGH. The MC54/74F537 has 3-state outputs, and a HIGH signal on the Output Enable ( $\overline{OE}$ ) input forces all outputs to the high impedance state. Two input enables, active HIGH  $E_2$  and active LOW  $\overline{E}_1$ , are available for demultiplexing data to the selected output in either non-inverted or inverted form. Input codes greater than BCD nine cause all outputs to go to the inactive state (i.e., same polarity as the P input).

- Demultiplexing Capability
- 3-State Outputs
- Multiple Input Enable for Expansion
- Polarity Control Input
- ESD Protection > 4000 Volts

CONNECTION DIAGRAM DIP (TOP VIEW)



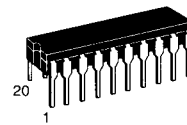
LOGIC DIAGRAM



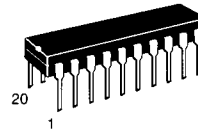
Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

## MC54/74F537

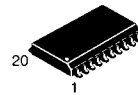
1-OF-10 DECODER  
WITH 3-STATE OUTPUTS  
FAST™ SCHOTTKY TTL



J SUFFIX  
CERAMIC  
CASE 732-03



N SUFFIX  
PLASTIC  
CASE 738-03

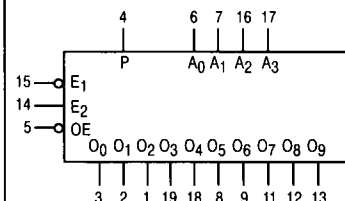


DW SUFFIX  
SOIC  
CASE 751D-03

ORDERING INFORMATION

MC54FXXXJ Ceramic  
MC74FXXXN Plastic  
MC74FXXXDW SOIC

LOGIC SYMBOL



VCC = PIN 20  
GND = PIN 10

# MC54/74F537

## GUARANTEED OPERATING RANGES

Symbol	Parameter	Min	Typ	Max	Unit	
V <sub>CC</sub>	Supply Voltage	54, 74	4.5	5.0	5.5	V
T <sub>A</sub>	Operating Ambient Temperature Range	54	-55	25	125	°C
		74	0	25	70	
I <sub>OH</sub>	Output Current — High	54, 74			-3.0	mA
I <sub>OL</sub>	Output Current — Low	54, 74			24	mA

## DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

Symbol	Parameter	Limits			Unit	Test Conditions
		Min	Typ	Max		
V <sub>IH</sub>	Input HIGH Voltage	2.0			V	Guaranteed Input HIGH Voltage
V <sub>IL</sub>	Input LOW Voltage			0.8	V	Guaranteed Input LOW Voltage
V <sub>IK</sub>	Input Clamp Diode Voltage			-1.2	V	I <sub>IN</sub> = -18 mA, V <sub>CC</sub> = MIN
V <sub>OH</sub>	Output HIGH Voltage	54, 74	2.4		V	I <sub>OH</sub> = -3.0 mA, V <sub>CC</sub> = 4.5 V
		74	2.7		V	I <sub>OH</sub> = -3.0 mA, V <sub>CC</sub> = 4.75 V
V <sub>OL</sub>	Output LOW Voltage			0.5	V	I <sub>OL</sub> = 24 mA, V <sub>CC</sub> = MIN
I <sub>OZH</sub>	Output OFF Current — HIGH			50	μA	V <sub>OUT</sub> = 2.7 V, V <sub>CC</sub> = MAX
I <sub>OZL</sub>	Output OFF Current — LOW			-50	μA	V <sub>OUT</sub> = 0.5 V, V <sub>CC</sub> = MAX
I <sub>IH</sub>	Input HIGH Current			20	μA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 2.7 V
				0.1	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 7.0 V
I <sub>IL</sub>	Input LOW Current			-0.6	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 0.5 V
I <sub>OS</sub>	Output Short Circuit Current (Note 2)	-60		-150	mA	V <sub>CC</sub> = MAX, V <sub>OUT</sub> = 0 V
I <sub>CCZ</sub>	Power Supply Current		44	66	mA	V <sub>CC</sub> = MAX: A <sub>0</sub> -A <sub>3</sub> , E <sub>1</sub> = GND OE, E <sub>2</sub> , P = HIGH

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## AC CHARACTERISTICS

Symbol	Parameter	54/74F			54F		74F		Unit
		T <sub>A</sub> = +25°C V <sub>CC</sub> = +5.0 V C <sub>L</sub> = 50 pF			T <sub>A</sub> = -55 to +125°C V <sub>CC</sub> = 5.0 V ± 10% C <sub>L</sub> = 50 pF		T <sub>A</sub> = 0 to 70°C V <sub>CC</sub> = 5.0 V ± 10% C <sub>L</sub> = 50 pF		
		Min	Typ	Max	Min	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay A <sub>n</sub> to O <sub>n</sub>	4.0		14	3.5	19	3.5	16	ns
t <sub>PHL</sub>	Propagation Delay E <sub>1</sub> to O <sub>n</sub>	2.5		11	2.0	15	2.0	12	
t <sub>PLH</sub>	Propagation Delay E <sub>2</sub> to O <sub>n</sub>	4.0		11	4.0	14	4.0	12	ns
t <sub>PHL</sub>	Propagation Delay P to O <sub>n</sub>	3.0		11	3.0	14	3.0	12	
t <sub>PLH</sub>	Propagation Delay E <sub>2</sub> to O <sub>n</sub>	6.0		11.5	5.0	15	5.0	13	ns
t <sub>PHL</sub>	Propagation Delay P to O <sub>n</sub>	4.0		11.5	4.0	14.5	4.0	12.5	
t <sub>PLH</sub>	Propagation Delay P to O <sub>n</sub>	5.0		16	5.0	21	4.5	17	ns
t <sub>PHL</sub>	Propagation Delay P to O <sub>n</sub>	3.5		11.5	3.5	13	3.5	12	
t <sub>PZH</sub>	Output Enable Time OE to O <sub>n</sub>	2.5		7.0	2.5	11	2.5	8.0	ns
t <sub>PZL</sub>	Output Disable Time OE to O <sub>n</sub>	4.0		8.0	4.0	10	4.0	9.0	
t <sub>PHZ</sub>	Output Disable Time OE to O <sub>n</sub>	1.5		6.0	1.0	8	1.0	7.0	ns
t <sub>PLZ</sub>	Output Enable Time OE to O <sub>n</sub>	1.5		6.5	1.0	8	1.0	7.0	

# MC54/74F537

**TRUTH TABLE**

FUNCTION	INPUTS							OUTPUTS										
	OE	E <sub>1</sub>	E <sub>2</sub>	A <sub>3</sub>	A <sub>2</sub>	A <sub>1</sub>	A <sub>0</sub>	O <sub>0</sub>	O <sub>1</sub>	O <sub>2</sub>	O <sub>3</sub>	O <sub>4</sub>	O <sub>5</sub>	O <sub>6</sub>	O <sub>7</sub>	O <sub>8</sub>	O <sub>9</sub>	
HIGH Impedance	H	X	X	X	X	X	X	Z	Z	Z	Z	Z	Z	Z	Z	Z	Z	
Disable	L	H	X	X	X	X	X	Outputs Equal P Input										
	L	X	L	X	X	X	X											
Active HIGH Output (P = L)	L	L	H	L	L	L	L	H	L	L	L	L	L	L	L	L	L	
	L	L	H	L	L	L	H	L	H	L	L	L	L	L	L	L	L	
	L	L	H	L	L	H	L	L	L	H	L	L	L	L	L	L	L	
	L	L	H	L	L	H	H	L	L	L	H	L	L	L	L	L	L	
	L	L	H	L	H	L	L	L	L	L	L	H	L	L	L	L	L	
	L	L	H	L	H	L	H	L	L	L	L	L	H	L	L	L	L	
	L	L	H	L	H	H	L	L	L	L	L	L	L	L	H	L	L	
	L	L	H	H	L	L	L	L	L	L	L	L	L	L	L	L	H	
	L	L	H	H	L	L	H	L	L	L	L	L	L	L	L	L	H	
	L	L	H	H	X	H	X	L	L	L	L	L	L	L	L	L	L	
	L	L	H	H	H	X	X	L	L	L	L	L	L	L	L	L	L	
	Active LOW Output (P = H)	L	L	H	L	L	L	L	L	H	H	H	H	H	H	H	H	H
		L	L	H	L	L	L	H	H	L	H	H	H	H	H	H	H	H
		L	L	H	L	L	H	L	H	H	L	H	H	H	H	H	H	H
		L	L	H	L	L	H	H	H	H	H	L	H	H	H	H	H	H
		L	L	H	L	H	L	L	H	H	H	H	L	H	H	H	H	H
L		L	H	L	H	L	H	H	H	H	H	H	L	H	H	H	H	
L		L	H	L	H	H	L	H	H	H	H	H	H	L	H	H	H	
L		L	H	L	H	H	H	H	H	H	H	H	H	H	L	H	H	
L		L	H	H	L	L	L	H	H	H	H	H	H	H	H	L	H	
L		L	H	H	L	L	H	H	H	H	H	H	H	H	H	H	L	
L		L	H	H	X	H	X	H	H	H	H	H	H	H	H	H	H	
L		L	H	H	H	X	X	H	H	H	H	H	H	H	H	H	H	

H = HIGH Voltage Level  
 L = LOW Voltage Level  
 X = Don't Care  
 Z = High Impedance

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