



SS-5-8211 ✓
 SS-6-8211 ✓
 SS-7-2016 ✓

S 34

ORIG

002649

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Dual 16-Bit Static Shift Registers

F-2649

61

FEATURES

- TTL/DTL Compatible Clock Input
- TTL/DTL Compatible Data—
No external interfacing components required on data inputs or outputs.
- DC-2MHz Operation
- Full Static Operation—
Data is stored independently of the clock logic level.
- Two Temperature Ranges—
SS-5 & SS-7: 0°C to +70°C
SS-6: -55°C to +125°C
- Zener Protected Inputs
- Glass Passivation Protection

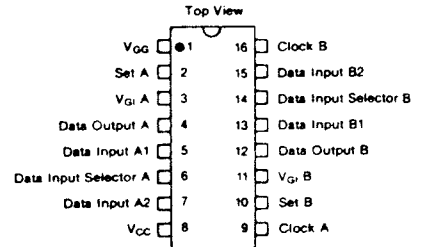
DESCRIPTION

The SS-5-8211/SS-6-8211/SS-7-2016 are dual 16-bit static shift registers with clock and data inputs and outputs that interface directly with TTL/DTL logic arrays without the use of any special interface components. These devices contain two independent 16-bit DC to 2MHz shift registers constructed on a single monolithic chip utilizing MTNS P-Channel enhancement mode transistors. Each shift register bit is implemented with a cross coupled flip-flop, so that data is stored indefinitely regardless of the logic level of the clock. Independent single phase clock inputs are provided for both registers. Data on the input is sampled while the clock is at a "0" level and the register shifts on a "0" to "1" transition. Separate input data selector controls are provided on each shift register. They determine which of the two inputs shall be shifted into the register. Each shift register also has its own set input which forces all stages of the register to a "1" level.

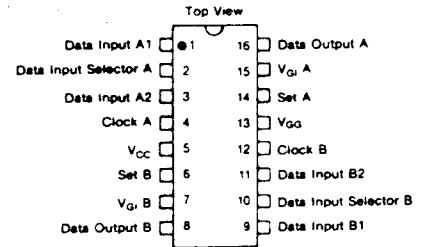
*Available only in Europe (16 Lead Plastic DIP)

PIN CONFIGURATION

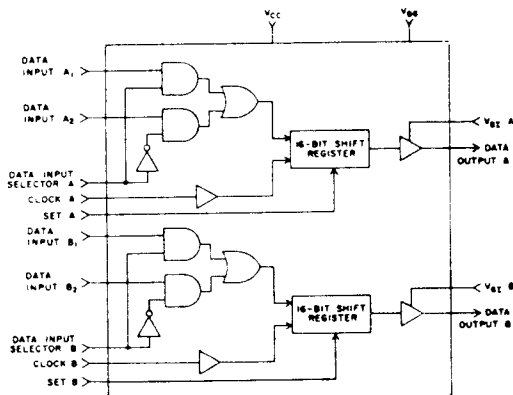
16 LEAD DUAL IN-LINE
 SS-5-8211, SS-6-8211



SS-7-2016*



BLOCK DIAGRAM



TIMING DIAGRAM

