

JEIDA Ver. 4 ELECTRICAL ERASABLE PROGRAMMABLE ROM

VARIATION

Part Number	Memory Size	Description
QWB017SD*0	16K Bytes	8K × 16 bits JEIDA Ver.4 CMOS EEPROM CARD
QWB033SD*0	32K Bytes	16K × 16 bits JEIDA Ver.4 CMOS EEPROM CARD

Note: * : Attribute memory type
X : 2K Bytes EEPROM (can be read/written)
Y : Without attribute memory (output “FF”) (read only)

OUTLINE OF FUNCTIONS AND FEATURES

- (1) This memory card conforms to JEIDA Ver.4.
- (2) Size of the card
 - Width : 54.0 mm
 - Length : 85.6 mm
 - Thickness : 3.3 mm
- (3) Includes exclusive IC's for the control of I/O.
- (4) Support 2 type attribute memory.
 - 4-1) With 2K Bytes EEPROM which can be read/written.
 - 4-2) Without attribute memory which can be read only. (output “FF”)
- (5) Card Type: 68 pin Two-piece Type.

MAXIMUM RAITING

Symbol	Description	Note	Min	Max	Unit
VCC	Supply voltage		-0.3	7.0	V
VIN	Input signal voltage		-0.3	7.0	V
VOUT	Output signal voltage		-0.3	7.0	V
TOPR	Operating temperature		0	60	°C
TSTR	Storage temperature		-20	60	°C
HUM	Humidity	1	10	90	%
PD	Power dissipation			1	W

Note: 1. No dew condition

CAPACITANCE (Ta=25°C, VIN/VOUT = 0 V, f = 1 MHz)

Symbol	Description	Min	Typ	Max	Unit
C1	Input capacitance		10	14	pF
C2	Input/output capacitance		10	14	pF

Note: The above figures are for reference only

RECOMMENDED DC OPERATING CONDITIONS

Symbol	Description	Min	Typ	Max	Unit
VCC	Supply voltage at read	4.75	5.0	5.25	V
VIH	High level input voltage	3.5	—	VCC +0.3	V
VIL	Low level input voltage	-0.1	—	1.0	V

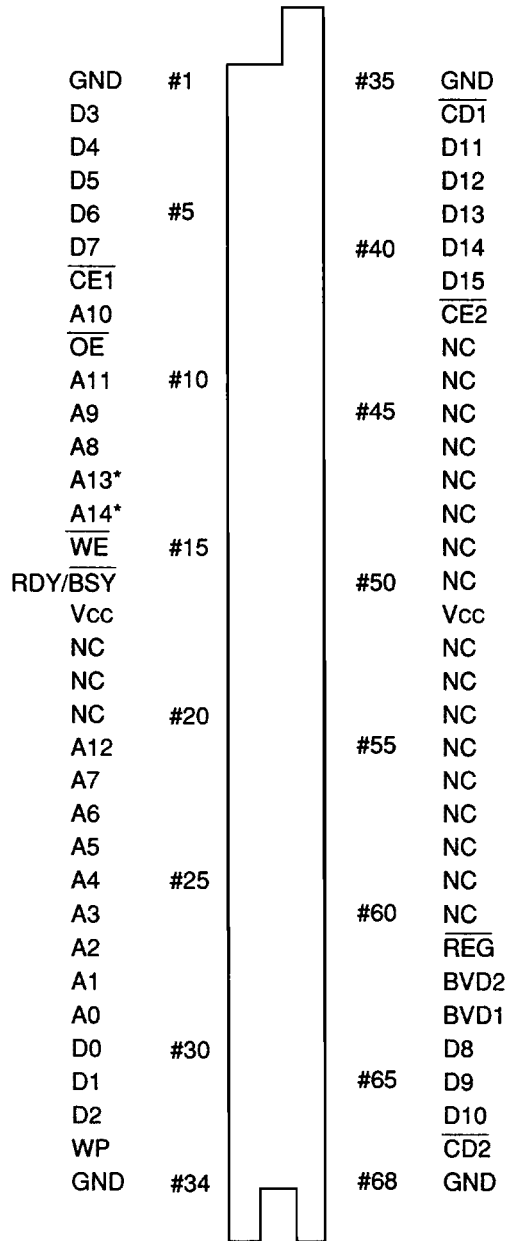
DC CHARACTERISTICS (VCC = 5 V, Ta = 25°C)

Symbol	Description	Object	Condition	Min	Typ	Max	Unit
ILI	Low level input current	1, 3	VIN = 0 V	-10	—	10	μA
		2		-53	—	-48	μA
IHI	High level input current	1, 2	VIN = 5 V	-10	—	10	μA
		3		10	—	50	
VOH	High level output voltage	3, 4, 5	IOH = -2.0 mA	VCC -0.4	—	—	V
VOL	Low level output voltage	3, 4, 5	IOL = 6.0 mA	—	—	VSS +0.4	V

- Object : 1. A0 to A14
 2. $\overline{CE1}$, $\overline{CE2}$, \overline{WE} , \overline{OE} , \overline{REG}
 3. D0 to D15
 4. WP
 5. $\overline{RDY/BSY}$

- Notes : $\overline{CE1}$, $\overline{CE2}$, \overline{WE} , \overline{OE} , \overline{REG} = Pull-up to VCC through 100K ohm
 D0 to D15 = Pull-down to GND through 100K ohm

PIN ASSIGNMENT



Notes: *A13 : QWB017, QWB033

*A14 : QWB033

Unused address lines should be no connect

NC : No connect