

DATA BUFFER BUILT-IN MEMORY (CONTINUED)

Function	Type No.	Outline	Process	Features	Usage/field
8KB FIFO	M66282FP ★	24P2Q-A	CMOS	<ul style="list-style-type: none"> ● 8192-word X 8-bit configuration ● Cycle : 25ns (min) ● Access : 18ns (max) ● Permits completely asynchronous write and read ● Vcc = 2.7 ~ 3.6V ● Data hold time 20ms (max) 	<ul style="list-style-type: none"> ● Digital PPC, scanner, FAX ● Image processing
SRAM FIFO	M66850J/FP ★★	32P0/32P6B	CMOS	<ul style="list-style-type: none"> ● 64-word X 9-bit configuration ● Cycle : 25ns (min) ● Access : 15ns (max) ● Built-in flag function ● Permits completely asynchronous write and read 	<ul style="list-style-type: none"> ● Information communications ● Communications data buffer
	M66851J/FP ★★	32P0/32P6B	CMOS	<ul style="list-style-type: none"> ● 256-word X 9-bit configuration ● Cycle : 25ns (min) ● Access : 15ns (max) ● Built-in flag function ● Permits completely asynchronous write and read 	
	M66852J/FP ★★	32P0/32P6B	CMOS	<ul style="list-style-type: none"> ● 512-word X 9-bit configuration ● Cycle : 25ns (min) ● Access : 15ns (max) ● Built-in flag function ● Permits completely asynchronous write and read 	
	M66853J/FP ★★	32P0/32P6B	CMOS	<ul style="list-style-type: none"> ● 1024-word X 9-bit configuration ● Cycle : 25ns (min) ● Access : 15ns (max) ● Built-in flag function ● Permits completely asynchronous write and read 	
Toggle line buffer	M66305AP/AFP	20P4/24P2W-A	CMOS	<ul style="list-style-type: none"> ● A3, 400dpi compatible ● 5120-bit FIFO toggled ● Serial in/serial out ● High-speed transmission-10Mbps ● Permits expansion of memory capacity by cascade connection 	<ul style="list-style-type: none"> ● Page printers, FAX ● Image processing
Parallel-in/serial-out scan line buffer	M66307SP/FP	32P4B/32P2W-A	CMOS	<ul style="list-style-type: none"> ● Includes 5120-bit RAM for A3, 400dpi ● Permits direct connection to 16-bit MPU bus ● Permits cascade connection (Toggle configuration, 32-bit bus) 	<ul style="list-style-type: none"> ● Page printers, FAX

LINE INTERFACE IC

EIA standard	Function	Number of circuits	Type No.	Outline
RS-232C	Transceiver	1 pair	M751701P ×	8P4
	Driver	4	M5A26LS31P/FP	16P4/16P2N-A
RS-422A	Receiver	4	M5A26LS32AP/AFP	16P4/16P2N-A
	Transceiver	2 pairs	M5M34050P/FP	16P4/16P2N-A
		2 pairs	M5M34051P/FP	16P4/16P2N-A
	RS-485	Transceiver	1 pair	M75176P
M75177P ×				8P4
M75178P ×				8P4
M75179P				8P4
FDD/HDD interface	Driver	7	M751270P/FP ×	16P4/16P2N-A
		7	M751271P/FP ×	16P4/16P2N-A
		6	M751273P/FP ×	16P4/16P2N-A
Photo-coupler interface	Transceiver	1 pair	M753114P	8P4

× : Plan for production discontinue