



## L7591 Subscriber Line Interface Circuit Protector

### Features

- Shunts lightning pulses to ground
  - Positive or negative 30 A, 10 x 1000  $\mu$ s
  - Positive or negative 40 A, 5 x 320  $\mu$ s
  - Positive or negative 80 A, 2 x 10  $\mu$ s
- Power-cross protection
  - 3.5 A<sub>PEAK</sub>, 50 Hz to 60 Hz, 1 s
  - 5.0 A<sub>PEAK</sub>, 50 Hz to 60 Hz, 10 ms
- Gate trigger current, 15 mA max
- Up to -80 V capability
- Holding current, 150 mA min

### Pin Information

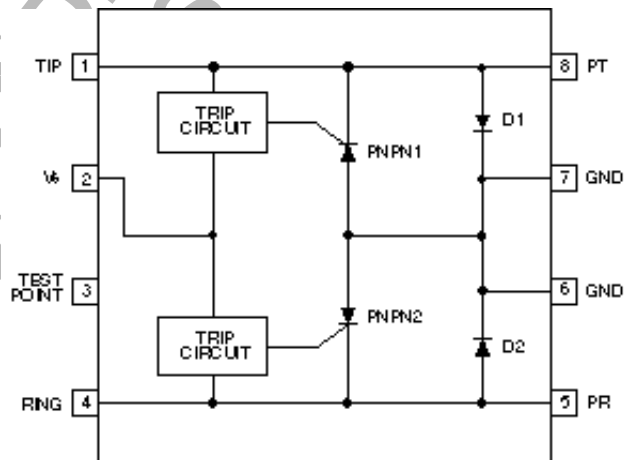
**Table 1. Pin Description**  
(Applies to DIP and SONB packages)

Pin	Symbol	Name/Function
1	TIP	Tip signal from customer.
2	V <sub>S</sub>	Supply voltage to gate (-20 V to -80 V).
3	—	Test Point, do not use.
4	RING	Ring signal from customer.
5	PR	Protected ring signal to line-feed circuitry.
6	GND	Device ground/fault current return.
7	GND	Device ground/fault current return.
8	PT	Protected tip signal to line-feed circuitry.

Note: Pins 1 and 4 must always be connected to the protection resistors shown in Figures 4 and 5 (Line Feed Circuitry). The SLIC can be connected either to the protected outputs (pins 5 and 8) or to the inputs (pins 1 and 4).

### Description

The L7591 Subscriber Line Interface Circuit (SLIC) Protector is designed to protect line-feed circuitry from fault-induced lightning and power-cross surge pulses. If a fault current forces TIP and/or RING to a more negative voltage than V<sub>S</sub>, current is conducted through the trip circuit. When the specified trip current level is reached, transistors PNP1 and/or PNP2 will turn on and "crowbar" the majority of the current to ground. If similar pulses force TIP and/or RING to a more positive state than ground, diodes D1 and/or D2 will conduct the pulse to ground. The L7591 SLIC Protector is available in an 8-pin, plastic DIP (L7591AB) and in an 8-pin, plastic SONB package (L7591AS).



12-1590 (C)

**Figure 1. Functional and Pin Diagram**

**Absolute Maximum Ratings** (At 25 °C)

Stresses in excess of the Absolute Maximum Ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of the data sheet. Exposure to Absolute Maximum Ratings for extended periods can adversely affect the device reliability.

Parameter	Symbol	Value	Unit
Ambient Operating Temperature	T <sub>A</sub>	-40 to +85	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to +125	°C
Pin Soldering Temperature (t = 15 s max)	—	300	°C
Supply Voltage	V <sub>S</sub>	-83	V
Peak Pulse Current: * (See Figure 2.)			
10 x 1000 μs	—	30	A
5 x 320 μs	—	40	A
2 x 10 μs	—	80	A
Nonrepetitive Peak ON-state Current:			
t = 1 s, f = 50 Hz to 60 Hz	—	3.5	A
t = 10 ms, f = 50 Hz to 60 Hz	—	5	A
Maximum Gate Current (Half Sine Wave 10 ms)	—	2	A
Maximum Voltage:			
TIP or RING to Gnd	—	-100	V
V <sub>S</sub> to Gnd	—	-80	V

\*Pulse Waveform Data:

10 x 1000 μs	tr = 10 μs	tp = 1000 μs
5 x 320 μs	tr = 5 μs	tp = 320 μs
2 x 10 μs	tr = 2 μs	tp = 10 μs

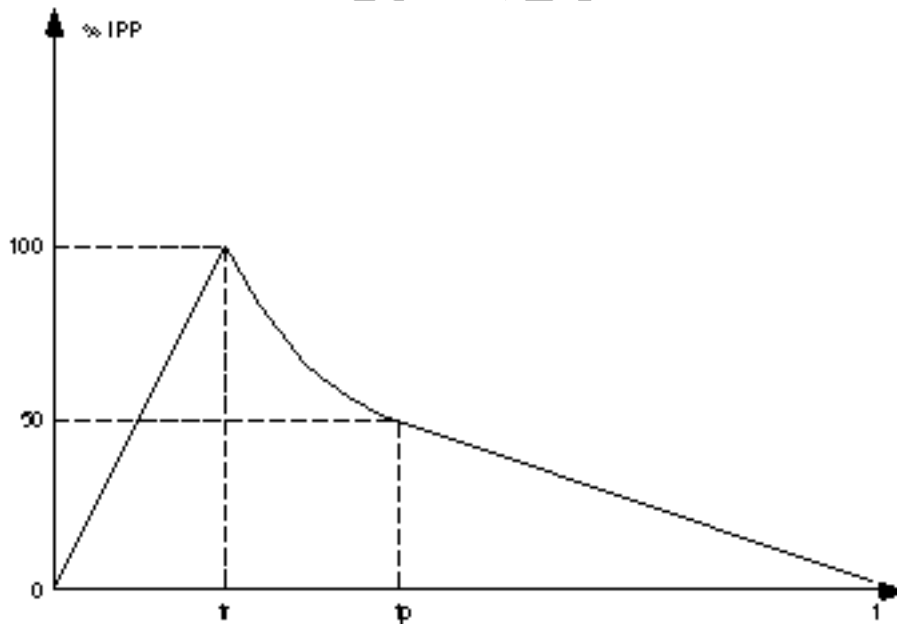


Figure 2. Pulse Waveform

NOT RECOMMENDED FOR NEW DESIGNS

NOT RECOMMENDED FOR NEW DESIGNS

## Electrical Specifications (TA = 25 °C)

The minimum and/or maximum limits specified for the parameters are based on the absolute system. The algebraic sign only applies to the direction of the parameter. These requirements apply to either the TIP or RING terminal; however, the device is capable of simultaneous Tip and Ring surges and continuous current, as noted below in Tip and Ring leads. See Figure 3 and Table 5 for symbol and test condition definition.

**Table 2. Electrical Characteristics D1 and D2**

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Forward Voltage	V <sub>F</sub>	I <sub>P</sub> = 5 A, t <sub>P</sub> = 1 ms	—	1.6	3	V
Peak Forward Voltage	V <sub>FP</sub>	I <sub>PP</sub> = 30 A, 10 x 1000 μs	—	5.2	15	V

**Table 3. Electrical Characteristics, PNP Circuitry**

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Gate Trigger Current	I <sub>GT</sub>	V <sub>S</sub> = 48 V	0.2	—	15	mA
Holding Current	I <sub>H</sub>	t <sub>P</sub> = 10 ms, V <sub>S</sub> = 48 V	-150	-250	—	mA
Trip Voltage	V <sub>T</sub>	dc	—	V <sub>S</sub> -2.0	V <sub>S</sub> -2.8	V
Dynamic Trip Voltage	V <sub>SGL</sub>	I <sub>PP</sub> = 30 A, V <sub>S</sub> = -48 V, 10 x 1000 μs	—	-51	-63	V
Reverse Leakage Current: V <sub>S</sub> to Tip or Ring	I <sub>RG</sub>	V <sub>S</sub> = -75 V				
TA = 25 °C	—	—	—	0.12	5	μA
TA = 70 °C	—	—	—	—	50	μA
dv/dt Sensitivity	—	Tip or Ring Lead	±1000	—	—	V/μs
On-state Voltage:	V <sub>ON</sub>	t <sub>P</sub> = 1 ms				
I <sub>T</sub> = 0.5 A	—	—	—	-1.3	—	V
I <sub>T</sub> = 3.0 A	—	—	—	-2.3	—	V

**Table 4. Electrical Characteristics, Diode and PNP Circuitry**

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
Reverse Leakage Current: (Gate Open)	I <sub>R</sub>	V <sub>R</sub> = -85 V				
TA = 25 °C	—	—	—	0.14	5	μA
TA = 70 °C	—	—	—	—	50	μA
Off-state Capacitance:	C <sub>off</sub>	f = 1 MHz				
V <sub>R</sub> = -3 V	—	—	—	50	—	pF
V <sub>R</sub> = -48 V	—	—	—	40	—	pF

Characteristic Curves

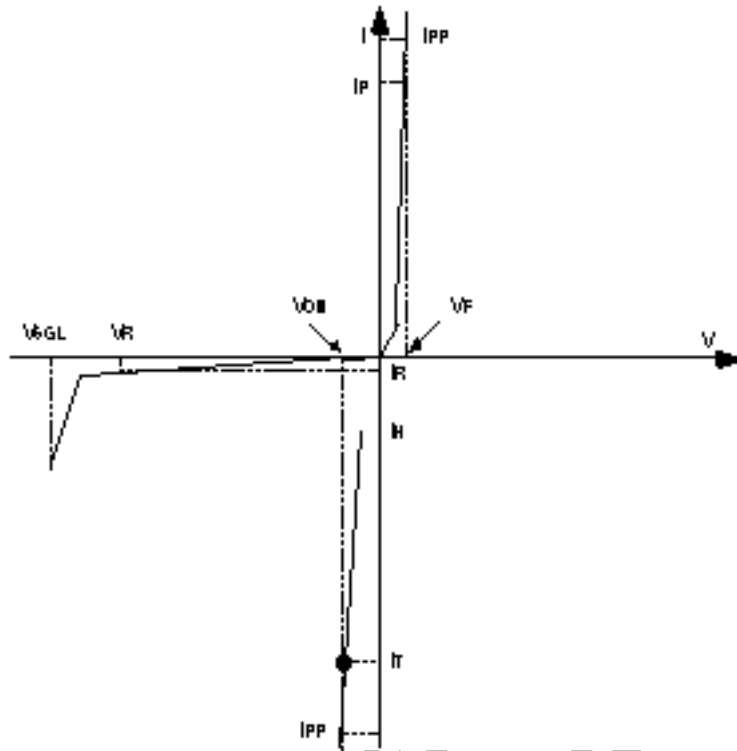


Figure 3. Typical Trip Characteristics of the SLIC Protector

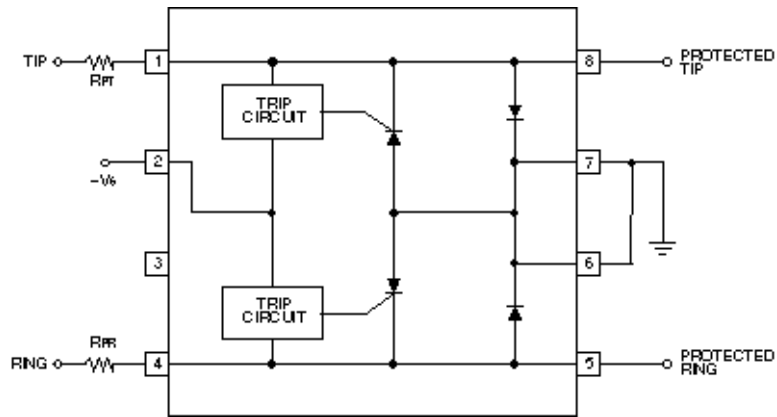
Table 5. Symbols Definition

Symbol	Parameter
I <sub>H</sub>	PNPN holding current
I <sub>GT</sub>	Gate trigger current out of pin VS
I <sub>P</sub>	Pulse current
I <sub>PP</sub>	Peak pulse current
I <sub>RG</sub>	Reverse leakage current VS to Tip or Ring
I <sub>T</sub>	Tip or Ring current when PNPN is on
I <sub>R</sub>	Reverse leakage current, Tip or Ring to Ground
V <sub>F</sub>	Forward voltage, Tip or Ring to Ground
V <sub>FP</sub>	Peak forward voltage, Tip or Ring to Ground
V <sub>T</sub>	Trip voltage, Tip or Ring to VS
V <sub>SGL</sub>	Dynamic trip voltage, Tip or Ring to VS
V <sub>ON</sub>	PNPN on voltage at I <sub>T</sub>
V <sub>R</sub>	Tip or Ring voltage when PNPN is off
C <sub>off</sub>	Off-state capacitance, Tip or Ring to Ground

NOT RECOMMENDED FOR NEW DESIGNS NOT RECOMMENDED FOR NEW DESIGNS

NOT RECOMMENDED FOR NEW DESIGNS NOT RECOMMENDED FOR NEW DESIGNS

Applications



RPT and RPR must be properly selected for proper operation and/or response.

12-1632 (C)

Figure 4. Standard Configuration

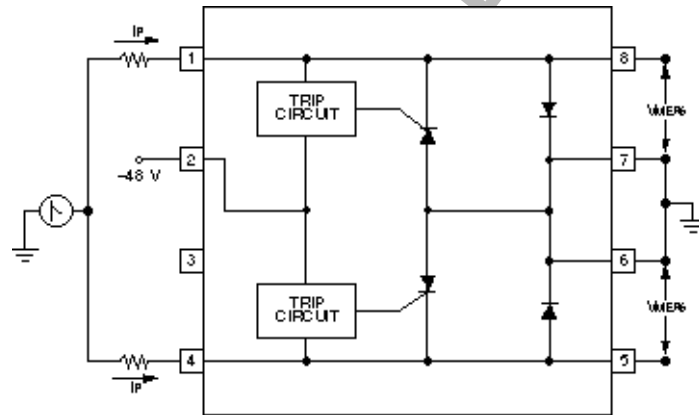
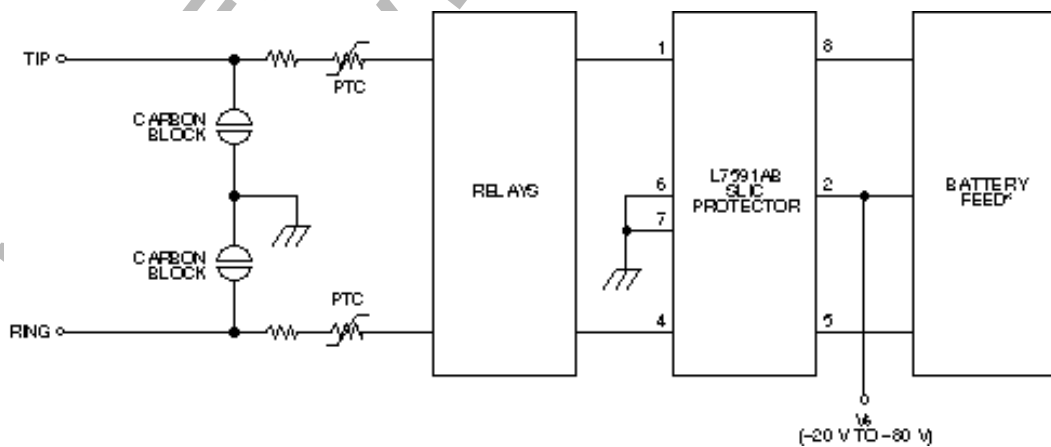


Figure 5. Test Circuit

12-1596 (C)



\*An example device is Legerity's LB1276 High Balance SLIC.

12-1598 (C)

Figure 6. Fully Protected Electronic TIP-RING Interface (Not all devices needed in all applications)

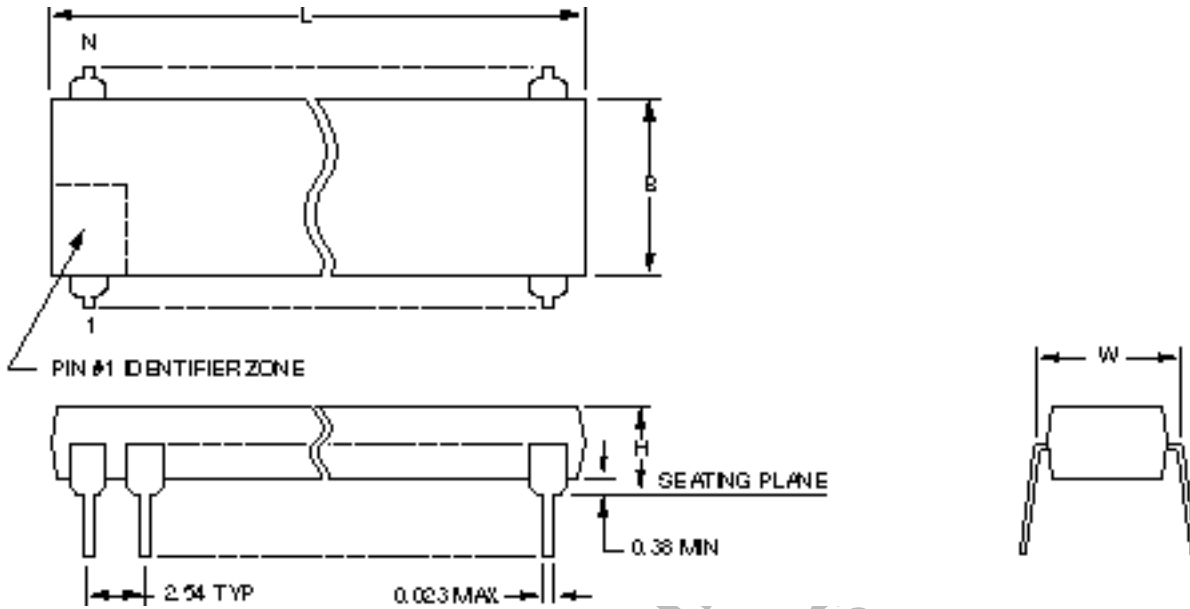
NOT RECOMMENDED FOR NEW DESIGNS NOT RECOMMENDED FOR NEW DESIGNS

NOT RECOMMENDED FOR NEW DESIGNS NOT RECOMMENDED FOR NEW DESIGNS

Outline Drawings

8-Pin DIP (L7591AB)

Dimensions are in millimeters.



5-4410r.1

Package Description	Number of Pins (N)	Package Dimensions			
		Maximum Length (L)	Maximum Width Without Leads (B)	Maximum Width Including Leads (W)	Maximum Height Above Board (H)
PDIP3 (Plastic Dual-In-Line Package)	8	10.16	6.48	7.87	5.46

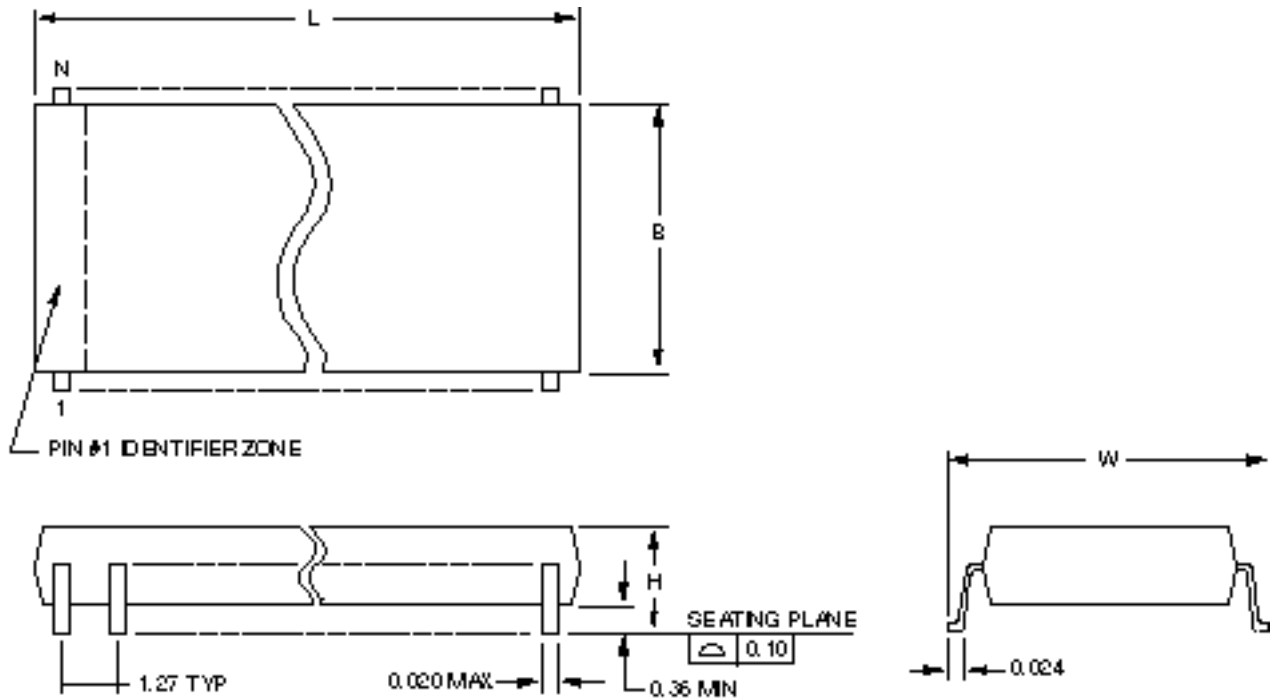
NOT RECOMMENDED FOR NEW DESIGNS

NOT RECOMMENDED FOR NEW DESIGNS

Outline Drawings (continued)

8-Pin, SONB (L7591AS)

Dimensions are in millimeters.



5-4414r.2

Package Description	Number of Pins (N)	Package Dimensions			
		Maximum Length (L)	Maximum Width Without Leads (B)	Maximum Width Including Leads (W)	Maximum Height Above Board (H)
SONB (Small Outline, Narrow Body)	8	5.08	4.01	6.17	1.73

NOT RECOMMENDED FOR NEW DESIGNS NOT RECOMMENDED FOR NEW DESIGNS

NOT RECOMMENDED FOR NEW DESIGNS NOT RECOMMENDED FOR NEW DESIGNS

**Ordering Information**

Device Part No.	Description	Package	Comcode
ATTL7591AB	SLIC Protector	8-Pin DIP	107056582
ATTL7591AS	SLIC Protector	8-Pin SONB	107056590
ATTL7591AS-TR	SLIC Protector	8-Pin SONB (Tape and Reel)	107232787

NOT RECOMMENDED FOR NEW DESIGNS NOT RECOMMENDED FOR NEW DESIGNS

NOT RECOMMENDED FOR  
NEW DESIGNS

NOT RECOMMENDED FOR NEW DESIGNS NOT RECOMMENDED FOR NEW DESIGNS

Legerity, Inc. reserves the right to make changes to the product(s) or information contained herein without notice. No liability is assumed as a result of their use or application. No rights under any patent accompany the sale of any such product(s) or information.