

# ADSL Magnetics

## EP-7 Surface-Mount Inductors



TM00329

- Designed with two well balanced and coupled windings for use in ADSL applications where filtering is required
- Operating temperature range: -40° C to +85° C
- Meets IEC 695, 2-2 flammability requirements
- PWB Process Capability: standard printed wiring board assembly techniques, total-immersion cleaning
- Reliability testing: shock, vibration, temperature cycling, temperature - humidity - bias

### ELECTRICAL SPECIFICATIONS AT 25° C

Part Number	Inductance <sup>1</sup> μH ±5%	DCR Ω max
	(1-4)	(1-4)
S560-6100-09	20.6	0.25
S560-6100-10	73.6	0.5
S560-6100-11	103.25	0.5

1. measured at 0.1 Vac, 10 kHz

Part Number	Turns Ratio <sup>1</sup> ± 1%	Inductance <sup>2</sup> μH ± 5%	Leakage Inductance <sup>3</sup> μH max	DCR Ω max		Direct Capacitance <sup>4</sup> pF max	Dielectric Rating Vrms
	(1-5) : (2-4)	(1-5)	(1-5)	(1-5)	(2-4)	(1-5) & (2-4)	(1-5) to (2-4)
S560-6100-12	1 : 1	88	5	0.65	0.65	100	1000
S560-6100-13	1 : 1	99	5	0.7	0.7	100	1000
S560-6100-14	1 : 1	120.5	5	0.55	0.55	100	1000
S560-6100-15	1 : 1	129.5	5	0.55	0.55	100	1000
S560-6100-33	1 : 1	2 mH	5	8.5	8.5	-	1000

- measured at 20 kHz, 1 Vrms
- measured at 0.1 Vac, 10 kHz
- measured at 0.1 Vac, 100 kHz, short terminals 2 & 4
- measured at 1 Vrms, 1 kHz

Part Number	Turns Ratio <sup>1</sup> ± 1%	Inductance <sup>2</sup> mH		DCR Ω max		Dielectric Rating Vac
	(1-3) : (4-6)	(1-3)	(1-6) <sup>3</sup>	(1-3)	(4-6)	between 1 & core <sup>4</sup>
S560-6100-24	1 : 1	0.5	2	1.65	1.65	600

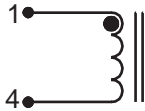
- measured at 20 kHz, 1 Vrms
- measured at 10 kHz, 0.1 Vrms
- connect 3 to 4
- connect 3 to 6

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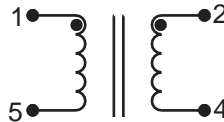
TM00329

### SCHEMATIC

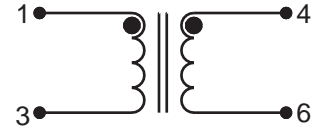
S560-6100-09  
S560-6100-10  
S560-6100-11



S560-6100-12  
S560-6100-13  
S506-6100-14  
S560-6100-15  
S560-6100-33

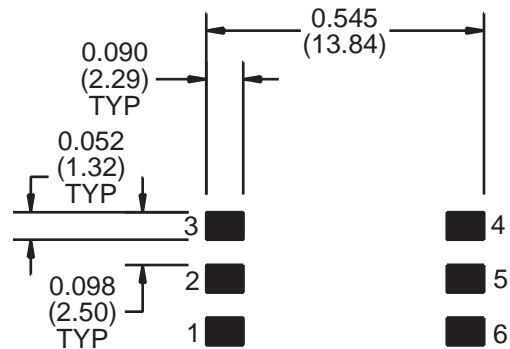
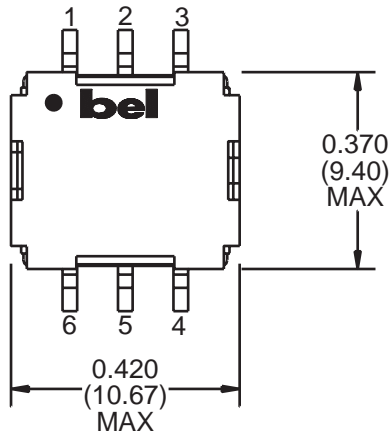


S560-6100-24

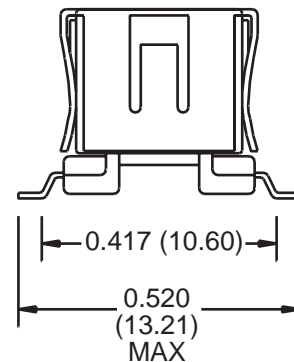
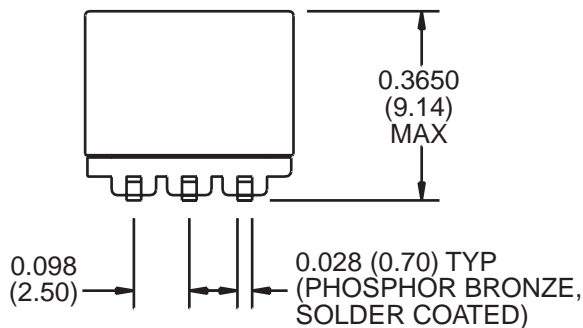


### MECHANICAL

S560-6100-09      S506-6100-14  
S560-6100-10      S560-6100-15  
S560-6100-11      S560-6100-24  
S560-6100-12      S560-6100-33  
S560-6100-13



SUGGESTED PCB PAD LAYOUT



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