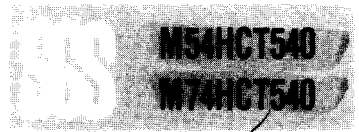


HS-C²MOS™ INTEGRATED CIRCUITS

048233



PRODUCT PREVIEW

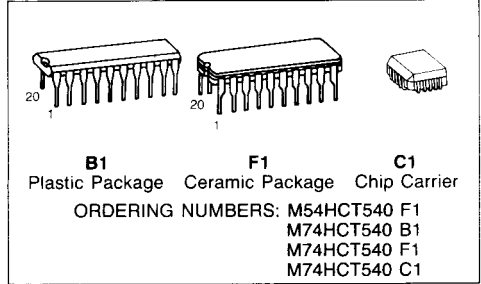
OCTAL BUS BUFFER (INVERTING, 3-STATE)

DESCRIPTION

The M54HCT540/74HCT540 is a high speed CMOS OCTAL BUS BUFFER (INVERTING, 3-STATE) fabricated in Silicon Gate C²MOS Technology. It has the same high speed performance of LSTTL combined with true CMOS low power consumption.

The M54HCT540/M74HCT540 is an inverting buffer. The 3-STATE control gate operates as a two-input NOR such that if either G1 or G2 are high, all eight outputs are in the high-impedance state. All inputs are equipped with protection circuits against static discharge or transient excess voltage. This integrated circuit has totally compatible, for input and output characteristic, with standard 54/74 LSTTL logic families.

M54HCT/74HCT devices are designed to directly interface HSC²MOS systems with TTL and NMOS components. These devices are also plug in replacements for LSTTL devices giving a reduction of power consumption.



FEATURES

- Low Power Dissipation
 $I_{CC} = 4 \mu A$ (Max.) at $T_A = 25^\circ C$
- High Noise Immunity
 $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (Min.)
- Output Drive Capability
15 LSTTL Loads
- Symmetrical Output Impedance
 $|I_{OH}| = |I_{OL}| = 6 mA$ (Min.)
- Balanced Propagation Delays
 $t_{PLH} = t_{PHL}$
- Pin and Function compatible with 54/74LS540

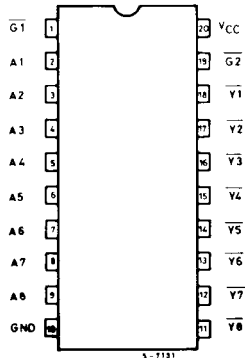
TRUTH TABLE

| INPUTS | | | OUTPUTS |
|-----------------|-----------------|---|----------------|
| $\overline{G2}$ | $\overline{G1}$ | A | \overline{Y} |
| * | H | * | Z |
| H | * | * | Z |
| L | L | L | H |
| L | L | H | L |

Z : High impedance

*: Don't care

PIN CONNECTIONS (top view)



Dual in line

CHIP CARRIER

