



TELEPHONE ANSWERING DEVICES

SUPERINTEGRATION PRODUCTS GUIDE

Block Diagram	ROM UART 8611 COUNTER/TIMERS P0 P1 P2 P3 CPU RAM	ROM WDT 236 RAM P1 P0 P2 P3 P0	Z8 DSP 24K 4K ROM ROM A/D D/A 31 or 47 DIGITAL I/O	Z8 DSP 24K 6K ROM ROM A/D D/A 31 or 47 DIGITAL I/O
PART NUMBER	Z8600/Z8611	Z86C30/E30/C31/E31	Z89C65/Z89C66	Z89165/Z89166
DESCRIPTION	Z8 [®] NMOS (CCP [™]) Z8600 = 2K ROM Z8611 = 4K ROM	Z8 [®] Consumer Controller Processor (CCP [™]) Z86C30 = 28-Pin, 4K ROM Z86C31 = 28-Pin, 2K ROM Z86C40 = 40-Pin, 4K ROM Z86E30, Z86E31, Z86E40 = OTP Version	Telephone Answering Controller Z89C66 = ROMLess with 31 I/O Pins	Low-Cost DTAD Controller Z89166 = ROMLess with 31 I/O Pins
PROCESS/SPEED	NMOS: 8.12 MHz	CMOS: 12 MHz	CMOS: 20 MHz	CMOS: 20 MHz
FEATURES	2K/4K ROM 128 Bytes RAM 22/32 I/O Lines - On-Chip Oscillator - Two Counter/Timers - Six Vectored, Priority Interrupts - UART (Z8611 Only)	4K ROM/236 RAM - Two Standby Modes - Two Counter/Timers - ROM/RAM Protect - Four Ports (Z86C40/E40) - Three Ports (Z86C30/E30/C31/E31) - Low-Voltage Protection - Two Analog Comparators - Low-EMI Option - Watch-Dog Timer (WDT) - Auto Power-On Reset - Low-Power Option	24K ROM (Z89C65 Only) 16-Bit DSP 6K Word ROM 8-Bit A/D with Automatic Gain Control (AGC) - DTMF Macro Available - LPC Macro Available 10-Bit PWM D/A - Other DSP Software Options Available 47 I/O Pins (Z89C65 Only)	24K ROM (Z89165 Only) 16-Bit DSP 6K Word DSP ROM 8-Bit A/D with Automatic Gain Control (AGC) - DTMF Macro Available - LPC Macro Available 10-Bit PWM D/A - Other DSP Software Options Available 47 I/O Pins (Z89165 Only)
PACKAGE	28-Pin DIP 40-Pin DIP 44-Pin PLCC	28-Pin DIP 40-Pin DIP 44-Pin PLCC, QFP	68-Pin PLCC	68-Pin PLCC 80-Pin QFP
SUPPORT PRODUCTS	Z86C120/ZEM - Emulator Z086000ZCO - Evaluation Board Z086000ZDP - Adaptor Kit	Z86COP00ZEM - Emulator Z86COP00ZAC - Emulator Z86C500ZEM - Emulator Z86E300ZDP - Adaptor Kit Z86E400ZDP - Program Adaptor Kit	Z89C6501ZEM - Emulator Z89C6500ZDB - Emulator Z8916500ZCO - Evaluation Board	