

T1/E1 Slim Line Hybrids for PMC Sierra

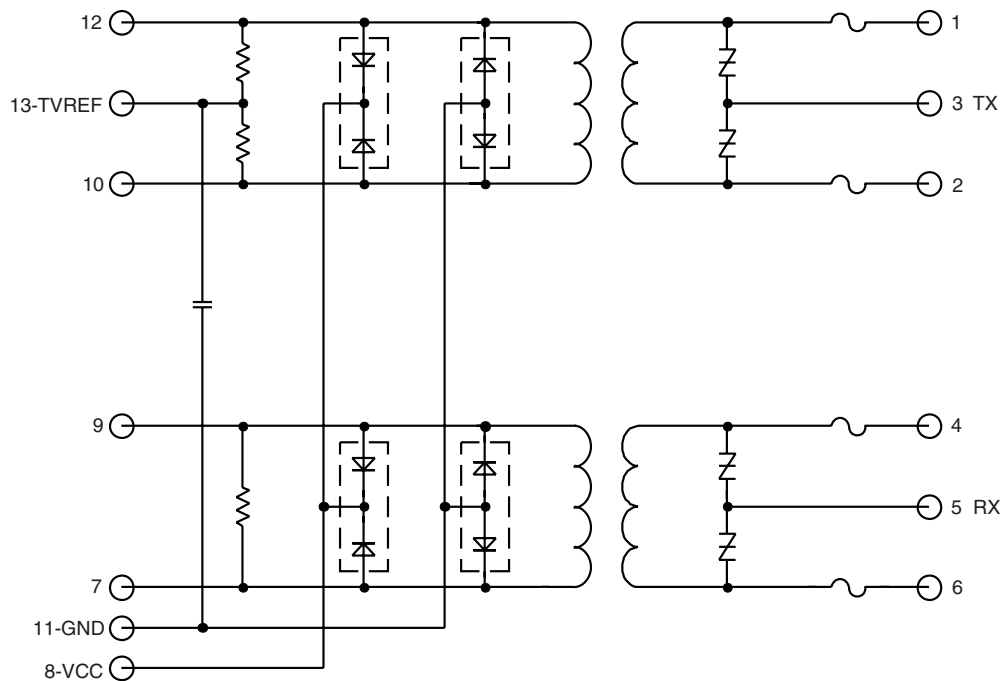
APC822xx

FEATURES

- Supports PMC Sierra single and quad chipsets for T1/E1 applications including PM4351 and PM4354
- Minimum board space requirement, ideal for multiple interface line cards
- Telcordia Technologies GR1089 compliant
- Approved to BSEN60950, basic insulation @ 250Vrms working voltage
- Designed to minimize board space requirements for line interface circuitry on the T1/E1 multichannel line cards
- Analog receive interface is configurable to operate both E1 and T1 short haul and long haul applications

SCHEMATIC

APC822xx





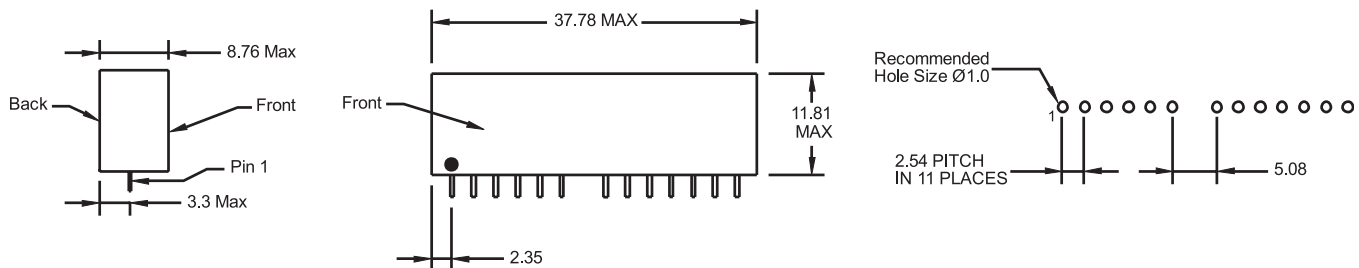
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Part Number	PMC Sierra Chipset	Application	Voltage	Protection	R x T/R	T x T/R
82217	PM4351 / 4354	T1/E1 (Z0 = 100/120 ohm)	3.3V	Longitudinal	2.42	2.42
82218	PM4351 / 4354	E1 (Z0 = 75 ohm)	3.3V	Longitudinal	2.42	2.42
82219	PM4351 / 4354	T1/E1 (Z0 = 100/120 ohm)	3.3V	Metallic	2.42	2.42
82220	PM4351 / 4354	E1 (Z0 = 75 ohm)	3.3V	Metallic	2.42	2.42

MECHANICAL

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NOTES: DIMENSIONS MM UNLESS OTHERWISE SPECIFIED
ALL TOLERANCES +/- 0.010
0.25

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