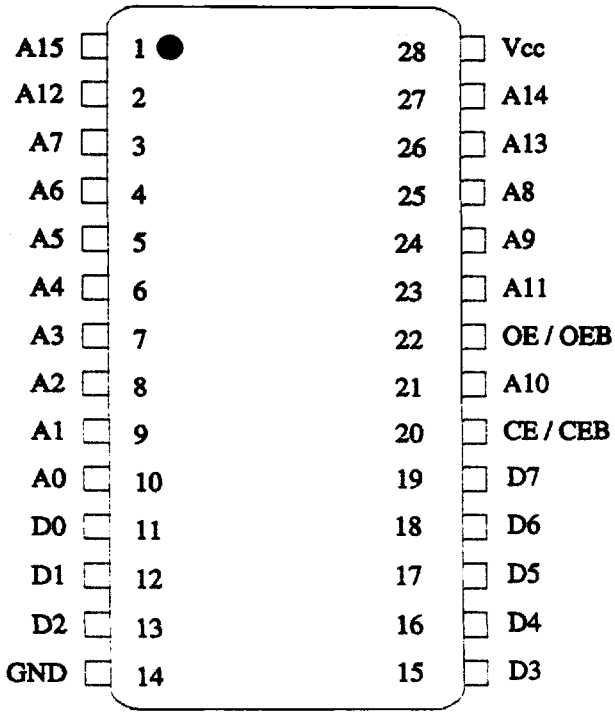
EUREKA

EK23C512

64K x 8 CMOS Mask ROM

Features

- Address Access Times
  100ns, 120ns, 150ns, 200ns
- Configurations
  65,536 x 8 bits
- Single +5V ± 10% Power Supply
- Completely TTL compatible
- Operating Current
  operating: 40mA
  standby: 100µA
- Package Options
  28 pin 330mil PDIP
  28 pin 330mil SOP



Description

The EK23C512 is a 5V static CMOS ROM with an access time of 100ns/120ns/150ns/200ns and low standby current of 100µA. It has a total of 512K programmable bits arranged as 65,536 x 8 bits. It offers a broad range of compatibility to high speed and large program storage system designs.The EK23C512 pin 20 chip enable (CE / CEB) and pin 22 output enable (OE / OEB) may be programmed either active HIGH or LOW.

Pin Description

Symbol	Description
A0-A15	Address Inputs
D0-D7	Data Inputs / Outputs
CE / CEB	Chip Enable
OE / OEB	Output Enable
NC	No Connect
Vcc	Power Supply Pin
Vss	Ground

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Ordering Information

(Order by Complete Part Number)

EK 23C512 X - Y

- Eureka Prefix
- Part Number
- Package Type
  - S = 28 pin 300mil SOP
  - P = 28 pin 600mil PDIP
- 12=120ns,
  15=150ns,
  20=200ns

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