

STV9379A

VERTICAL DEFLECTION BOOSTER

FEATURES

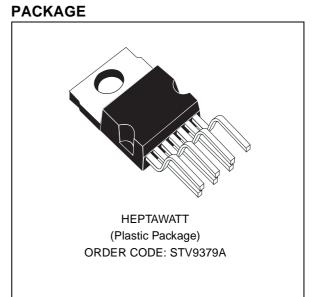
- Power Amplifier
- Flyback Generator
- Thermal Protection
- Output Current up to 2.6 App
- Flyback Voltage up to 90V (on pin 5)
- Suitable for DC Coupling Application

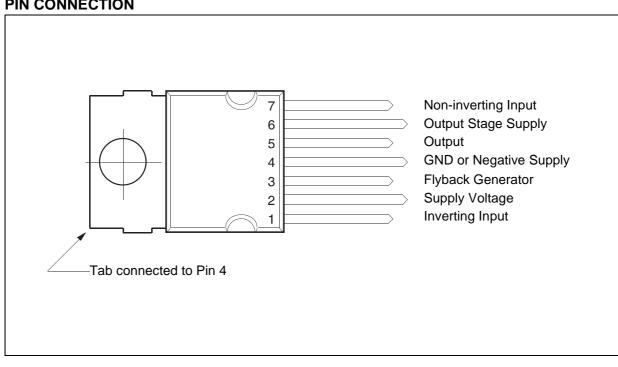
DESCRIPTION

Designed for monitors and high performance TVs, the STV9379A vertical deflection booster delivers flyback voltages close to 90V.

The STV9379A operates with supplies up to 42V and provides up to 2.6 App output current to drive the yoke.

The STV9379A is inserted in HEPTAWATT package.

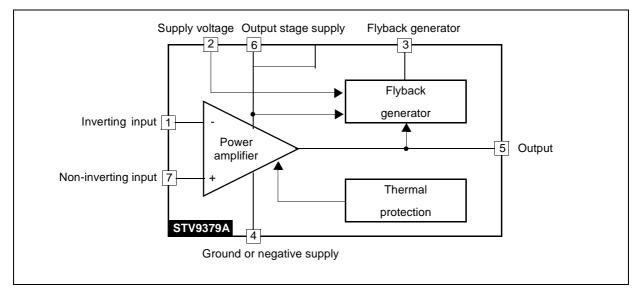




PIN CONNECTION

BLOCK DIAGRAM

Figure 1. STV9379A block diagram



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit	
V _S	Supply Voltage (Pin 2) (Note 1)	50	V	
V ₆	Flyback Peak Voltage (Pin 6) (Note 1)	100	V	
V ₁ ,V ₇	Amplifier Input Voltage (Pins 1-7) (Note 1)	-0.3, + V _S	V	
Ι _Ο	Maximum Output Peak Current (Note 2, Note 3)	1.8	Α	
l ₃	Maximum Sink Current (first part of flyback) (t < 1ms)	1.8	Α	
l ₃	Maximum Source Current (t < 1ms) (Note 2)	1.8	А	
V _{ESD}	ESD Susceptibility: EIAJ Norm (200pF discharged through 0Ω)	300	V	
T _{oper}	Operating Ambient Temperature	-20, +75	°C	
T _{stg}	Storage Temperature	-40, +150	°C	
Tj	Junction Temperature	+ 150	°C	

Note 1:Versus Pin 4.

Note 2:The output current can reach 5A peak for $t \le 10\mu s$ (up to 120Hz) **Note 3:**Provided SOAR is respected (see Figures 2 and 3).

THERMAL DATA

Symbol	Parameter	Value	Unit	
R _{th (j-c)}	Junction-Case Thermal Resistance Max.	3	°C/W	
Τ _t	Temperature for Thermal Shutdown	150	°C	
ΔT_t	Hysteresis on Tt	10	°C	
T _{jr}	Recommended Max. Junction Temperature	120	°C	

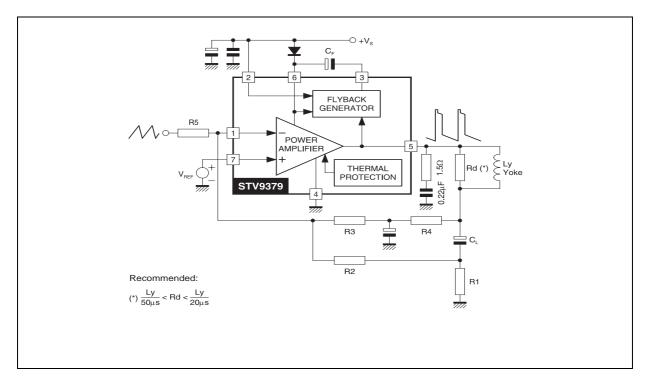
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ELECTRICAL CHARACTERISTICS V_s =42V, T_A = 25°C, unless otherwise specified

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
Vs	Operating Supply Voltage Range	Versus Pin 4	10		42	V
l ₂	Pin 2 Quiescent Current	$I_3 = 0, I_5 = 0$		13	20	mA
I ₆	Pin 6 Quiescent Current	$I_3 = 0, I_5 = 0$	5	10	30	mA
Ι _ο	Max. Peak Output Current				1.3	A
I ₁	Amplifier Bias Current	V ₁ = 25V, V ₇ = 26V		-0.15	- 1	μΑ
I ₇	Amplifier Bias Current	V ₁ = 26V, V ₇ = 25V		-0.15	- 1	μA
V _{IO}	Offset Voltage				7	mV
∆V _{IO} /dt	Offset Drift Versus Temperature			- 10		μV/°C
GV	Voltage Gain		80			dB
V _{5L}	Output Saturation Voltage to GND (Pin 4)	l ₅ = 1.3A		1	1.5	V
V _{5H}	Output Saturation Voltage to Supply (Pin 6)	I ₅ = -1.3A		1.6	2.1	V
V _{D5-6}	Diode Forward Voltage between Pins 5-6	l ₅ = 1.3A		1.3	2	V
V _{D3-2}	Diode Forward Voltage between Pins 3-2	l ₃ = 1.3A		1.3	2	V
V _{3L}	Saturation Voltage on Pin 3	l ₃ = 20mA		0.8	1.2	V
V _{3SH}	Saturation Voltage to Pin 2 (2nd part of flyback)	l ₃ = - 1.3A		2.9	3.6	V

APPLICATION CIRCUITS

AC COUPLING



STV9379A

APPLICATION CIRCUITS (CONTINUED)

DC COUPLING.

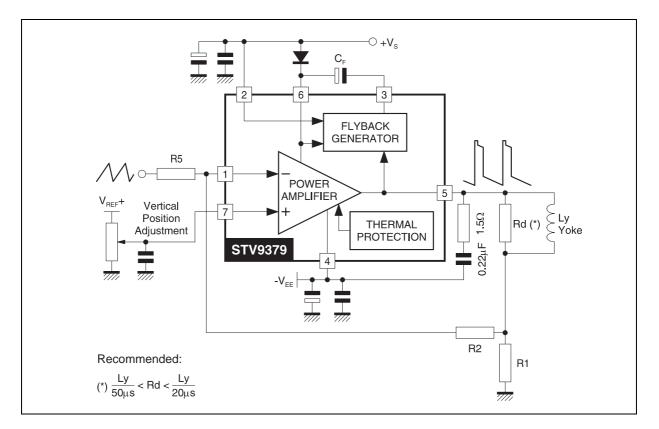


Figure 2. Output transistors SOA (for secondary breakdown)

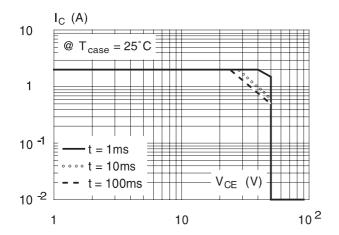
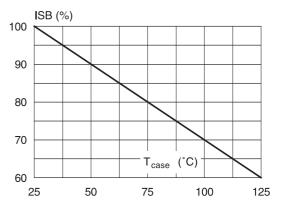
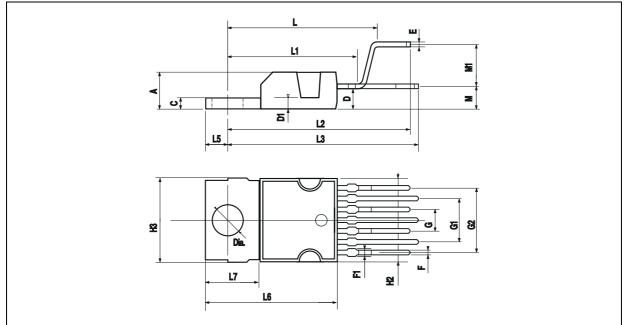


Figure 3. Secondary breakdown Temperature Derating Curve (ISB = Secondary Breakdown current)



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PACKAGE MECHANICAL DATA: 7 PINS - PLASTIC HEPTAWATT

Dimensions		Millimeters			Inches	
Dimensions	Min.	Тур.	Max.	Min.	Тур.	Max.
Α			4.8			0.189
С			1.37			0.054
D	2.4		2.8	0.094		0.110
D1	1.2		1.35	0.047		0.053
E	0.35		0.55	0.014		0.022
F	0.6		0.8	0.024		0.031
F1			0.9			0.035
G	2.41	2.54	2.67	0.095	0.100	0.105
G1	4.91	5.08	5.21	0.193	0.200	0.205
G2	7.49	7.62	7.8	0.295	0.300	0.307
H2			10.4			0.409
H3	10.05		10.4	0.396		0.409
L		16.97			0.668	
L1		14.92			0.587	
L2		21.54			0.848	
L3		22.62			0.891	
L5	2.6		3	0.102		0.118
L6	15.1		15.8	0.594		0.622
L7	6		6.6	0.236		0.260
М		2.8			0.110	
M1		5.08			0.200	
Dia.	3.65		3.85	0.144		0.152

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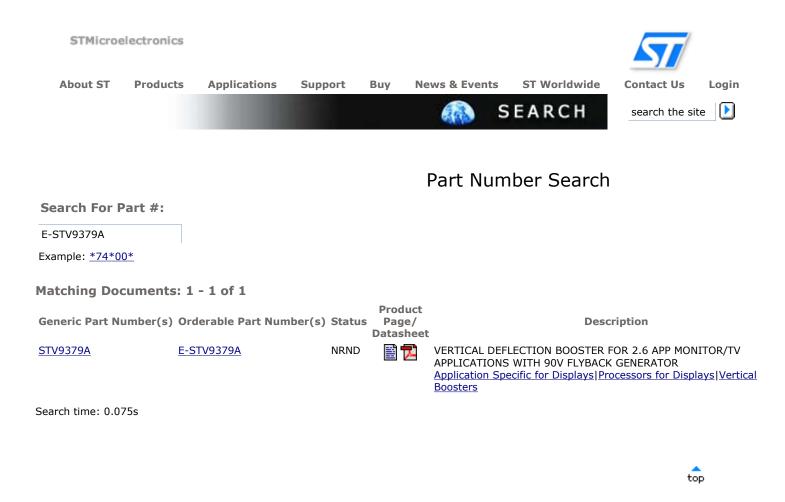
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