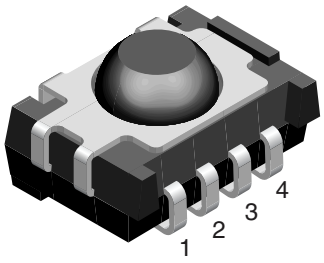
IR Receiver for Synchronizing of 3D Goggles



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FEATURES

- Low supply current
- Photo detector and preamplifier in one package
- Internal filter for 21 kHz IR signals
- Shielding against EMI
- Supply voltage: 2.7 V to 5.5 V
- Visible light is suppressed by IR filter
- Insensitive to supply voltage ripple and noise
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

Pb-free

e3

RoHS COMPLIANT

GREEN (5-2008)

MECHANICAL DATA

Pinning:

1 = GND, 2 = N.C., 3 = OUT, 4 = V_S

DESCRIPTION

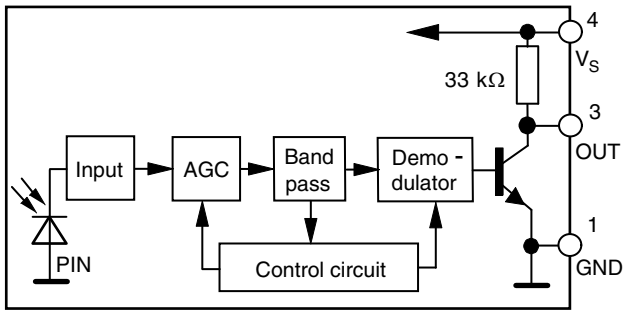
The TSOP5321 is a miniaturized receiver for an infrared synchronizing signal of active 3D eyewear. A pin diode and a preamplifier are assembled on a lead frame, the epoxy package acts as an IR filter.

The output of the TSOP5321 is a demodulated signal. The main benefit of the TSOP5321 is the high sensitivity and the good suppression of noise signals from lighting sources.

This component has not been qualified according to automotive specifications.

PARTS TABLE	
CARRIER FREQUENCY	SENSOR APPLICATIONS
21 kHz	TSOP5321

BLOCK DIAGRAM



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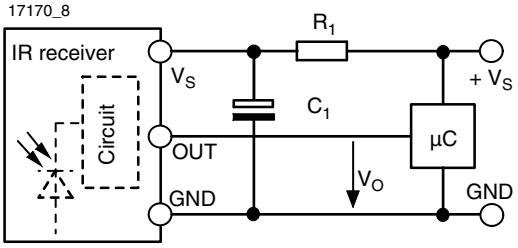
APPLICATION CIRCUIT



The external components R_1 and C_1 are optional to improve the robustness against electrical overstress (typical values are R_1 = 100 Ω, C_1 = 0.1 µF).
The output voltage V_O should not be pulled down to a level below 1 V by the external circuit.
The capacitive load at the output should be less than 2 nF.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	**TEST CONDITION**	**SYMBOL**	**VALUE**	**UNIT**
Supply voltage (pin 4)		V_S	- 0.3 to + 6	V
Supply current (pin 4)		I_S	5	mA
Output voltage (pin 3)		V_O	- 0.3 to 5.5	V
Voltage at output to supply		$V_S - V_O$	- 0.3 to (V_S + 0.3)	V
Output current (pin 3)		I_O	5	mA
Junction temperature		T_j	100	°C
Storage temperature range		T_{stg}	- 25 to + 85	°C
Operating temperature range		T_{amb}	- 25 to + 85	°C
Power consumption	$T_{amb} \leq 85$ °C	P_{tot}	10	mW

Note

- Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect the device reliability.

ELECTRICAL AND OPTICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)						
PARAMETER	**TEST CONDITION**	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Supply current (pin 4)	E_v = 0, V_S = 5 V	I_{SD}	0.65	0.85	1.05	mA
	E_v = 40 klx, sunlight	I_{SH}		0.95		mA
Supply voltage		V_S	2.7		5.5	V
Transmission distance	E_v = 0, test signal see fig. 1, IR diode TSAL6200, I_F = 400 mA	d		30		m
Output voltage low (pin 3)	I_{OSL} = 0.5 mA, E_e = 2 mW/m², test signal see fig. 1	V_{OSL}			100	mV
Minimum irradiance	Pulse width tolerance: $t_{pi} - 5/f_o < t_{po} < t_{pi} + 6/f_o$, test signal see fig. 1	$E_{e\ min.}$		0.3	0.45	mW/m²
Maximum irradiance	$t_{pi} - 5/f_o < t_{po} < t_{pi} + 6/f_o$, test signal see fig. 1	$E_{e\ max.}$	30			W/m²
Directivity	Angle of half transmission distance	$\varphi_{1/2}$		± 50		deg

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

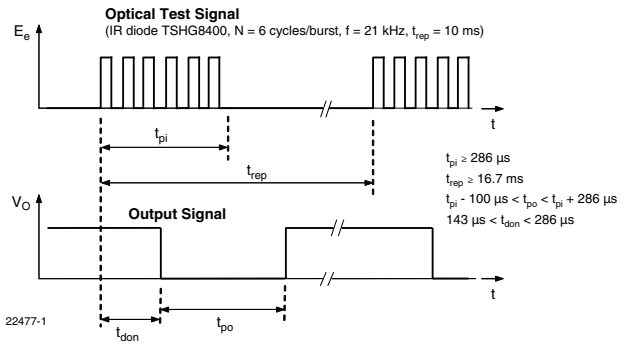


Fig. 1 - Output Function

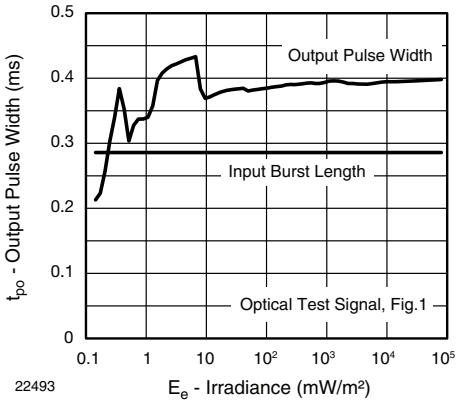


Fig. 2 - Pulse Length and Sensitivity in Dark Ambient

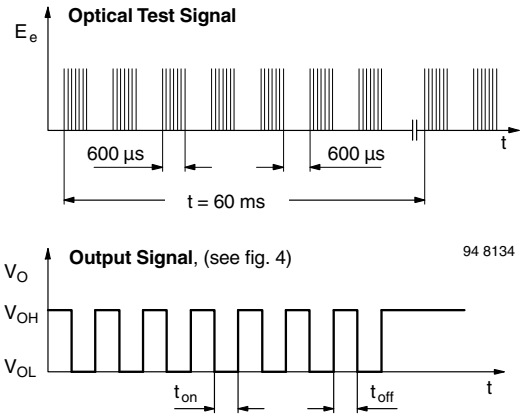


Fig. 3 - Output Function

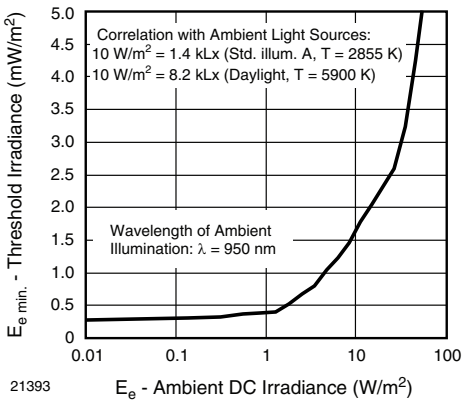


Fig. 6 - Sensitivity in Bright Ambient

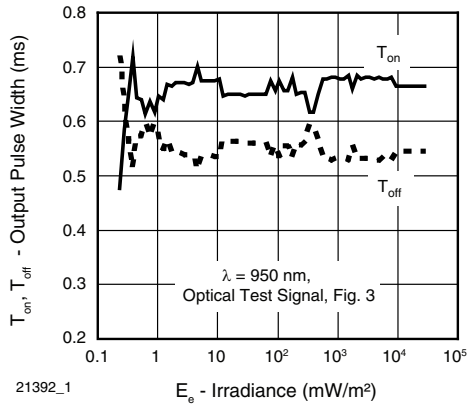


Fig. 4 - Output Pulse Diagram

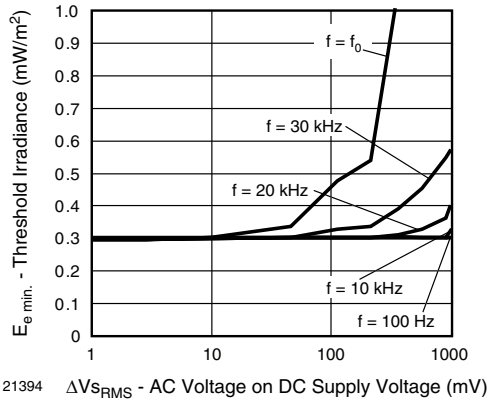


Fig. 7 - Sensitivity vs. Supply Voltage Disturbances

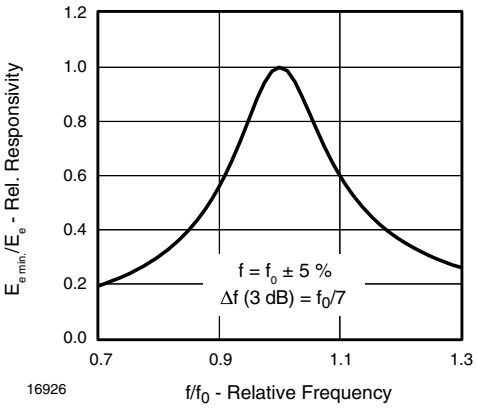


Fig. 5 - Frequency Dependence of Responsivity

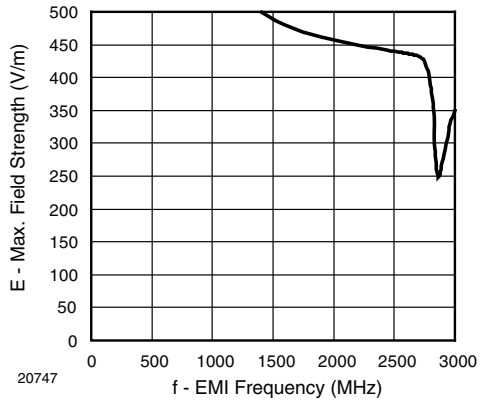


Fig. 8 - Sensitivity vs. Electric Field Disturbances

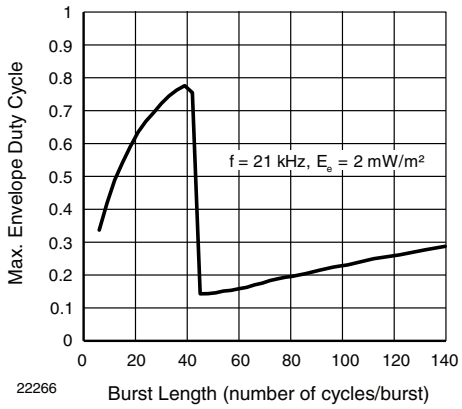


Fig. 9 - Max. Envelope Duty Cycle vs. Burstlength

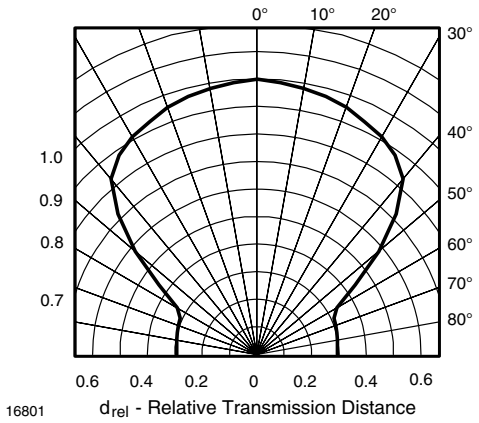


Fig. 12 - Horizontal Directivity

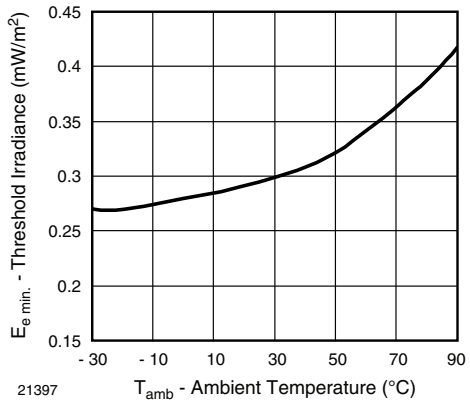


Fig. 10 - Sensitivity vs. Ambient Temperature

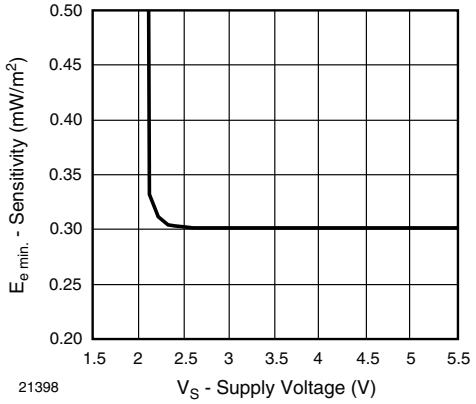


Fig. 13 - Sensitivity vs. Supply Voltage

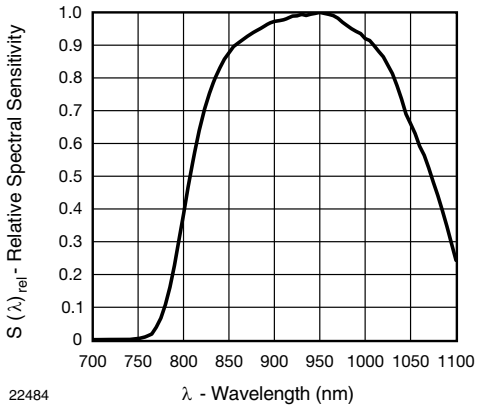


Fig. 11 - Relative Spectral Sensitivity vs. Wavelength

SUITABLE DATA FORMAT

The circuit in the TSOP5321 is designed to suppress spurious output pulses due to noise or disturbance signals. Data and disturbance signals can be distinguished by the devices according to carrier frequency, burst length and envelope duty cycle. The data signal should be close to the band-pass center frequency (e.g. 21 kHz) and fulfill the conditions in the table below.

When a data signal is applied to the TSOP5321 in the presence of a disturbance signal, the sensitivity of the receiver is reduced to insure that no spurious pulses are present at the output. Some examples of disturbance signals which are suppressed are:

- DC light (e.g. from tungsten bulb or sunlight)
- Continuous signals at any frequency
- Modulated noise from fluorescent lamps with electronic ballasts (see figure 15 or 16)

Data that should be received continuously by the TSOP5321 must not exceed certain limitations of the envelope duty cycle. If the burst repetition rate is too fast then it may happen that there is no more output signal at the TSOP5321 after some time.

The chart below shows typical timing restrictions of the TSOP5321.

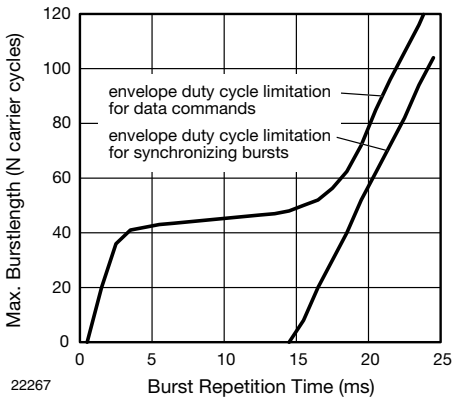


Fig. 14 - Max.Burstlength vs. Burst Repetition Time

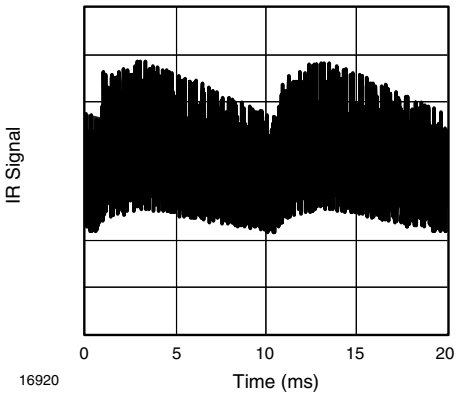


Fig. 15 - IR Signal from Fluorescent Lamp with Low Modulation

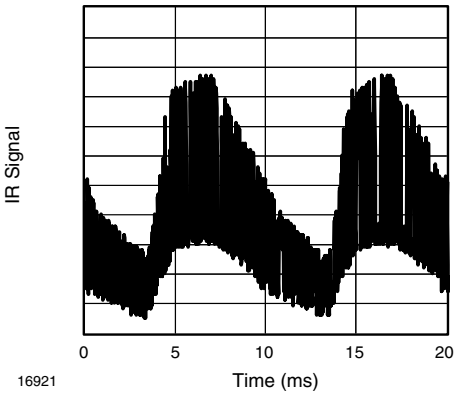
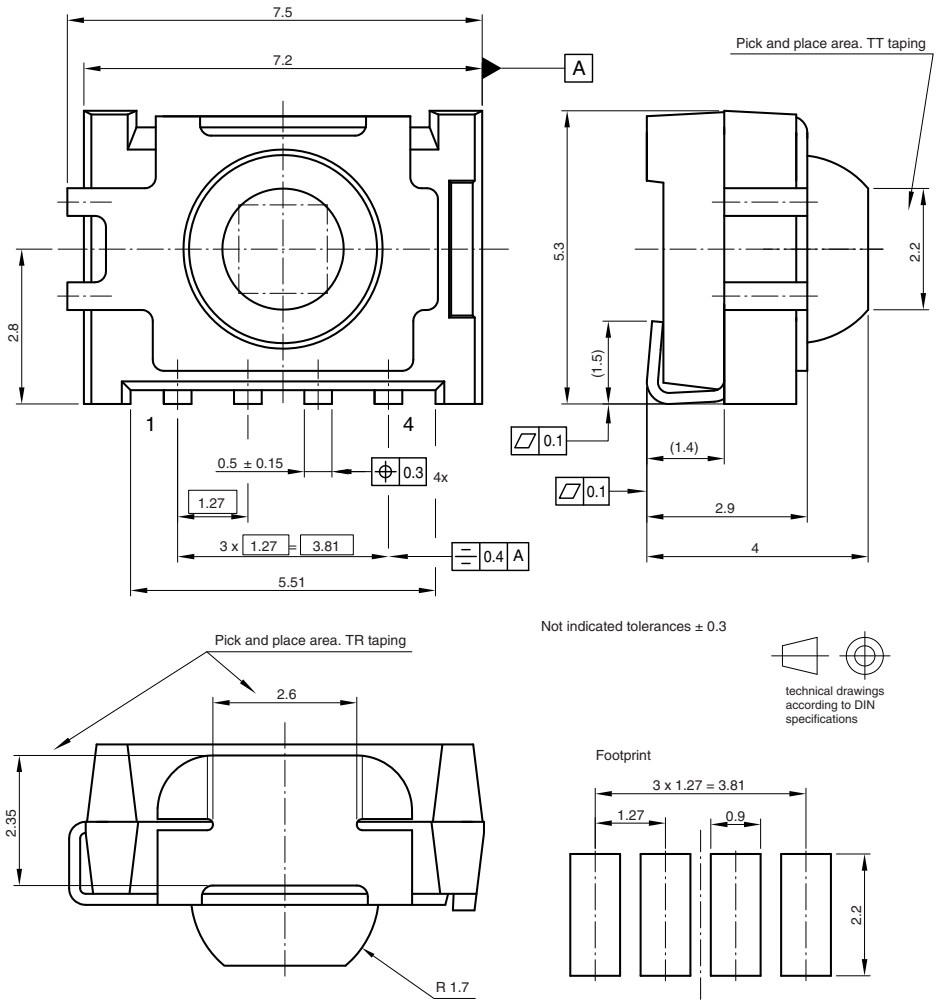


Fig. 16 - IR Signal from Fluorescent Lamp with High Modulation

PACKAGE DIMENSIONS in millimeters



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Issue: 8; 02.09.09
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ASSEMBLY INSTRUCTIONS

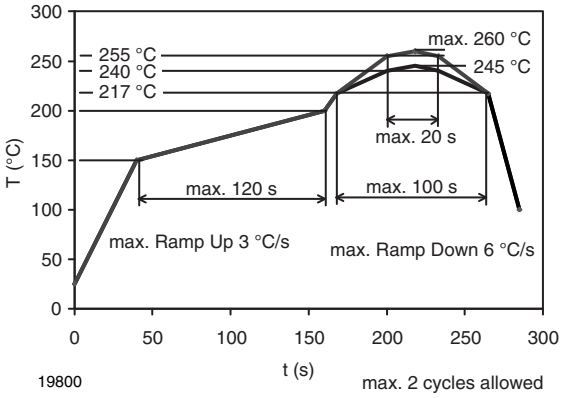
Reflow Soldering

- Reflow soldering must be done within 72 h while stored under a max. temperature of 30 °C, 60 % RH after opening the dry pack envelope
- Set the furnace temperatures for pre-heating and heating in accordance with the reflow temperature profile as shown in the diagram. Excercise extreme care to keep the maximum temperature below 260 °C. The temperature shown in the profile means the temperature at the device surface. Since there is a temperature difference between the component and the circuit board, it should be verified that the temperature of the device is accurately being measured
- Handling after reflow should be done only after the work surface has been cooled off

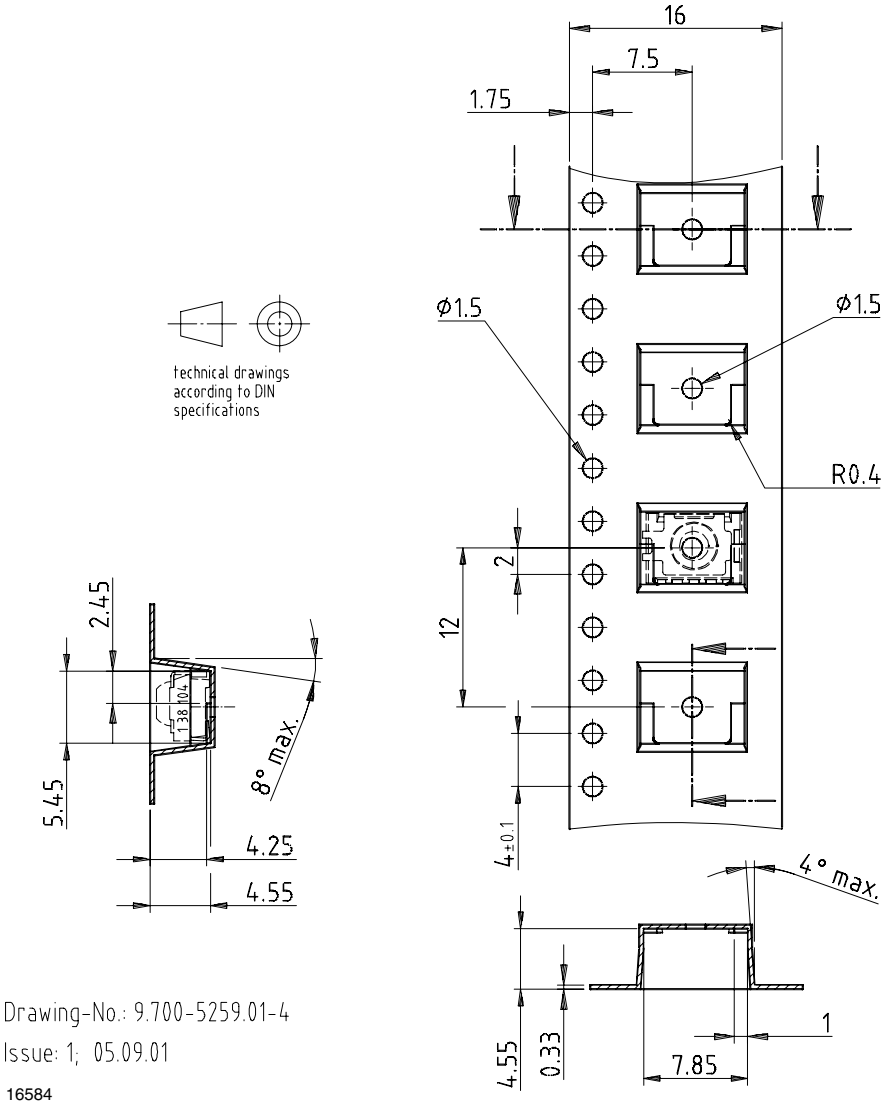
Manual Soldering

- Use a soldering iron of 25 W or less. Adjust the temperature of the soldering iron below 300 °C
- Finish soldering within 3 s
- Handle products only after the temperature has cooled off.

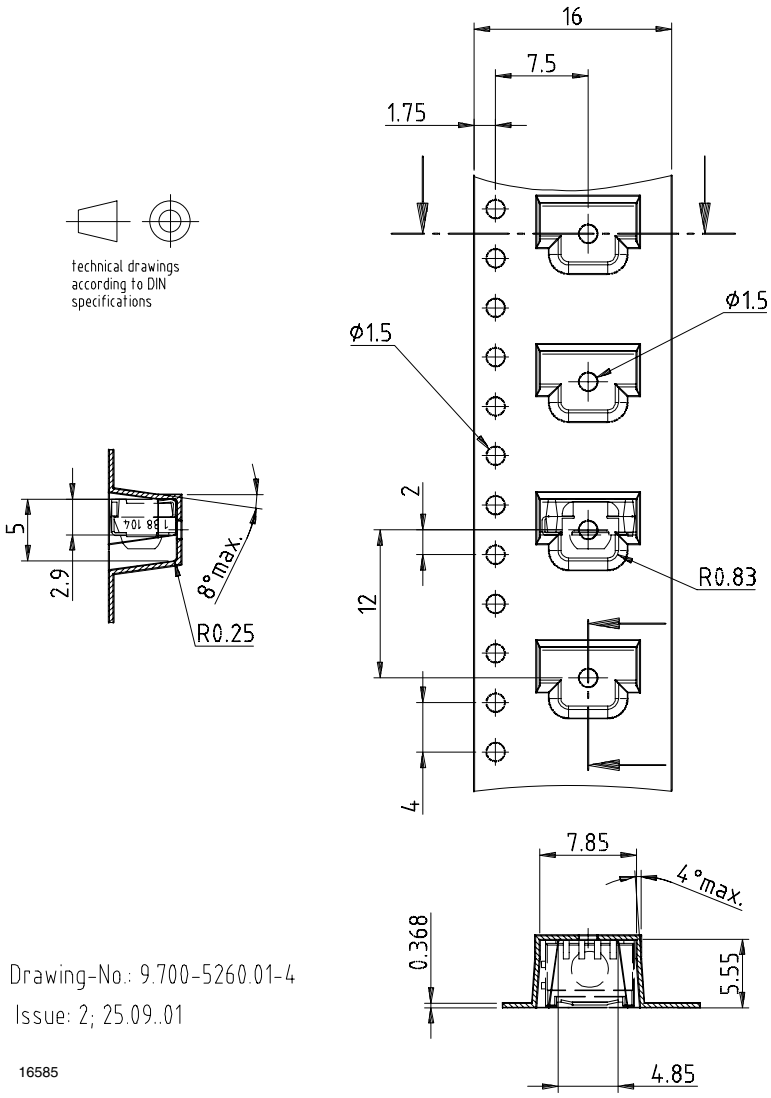
VISHAY LEAD (Pb)-FREE REFLOW SOLDER PROFILE



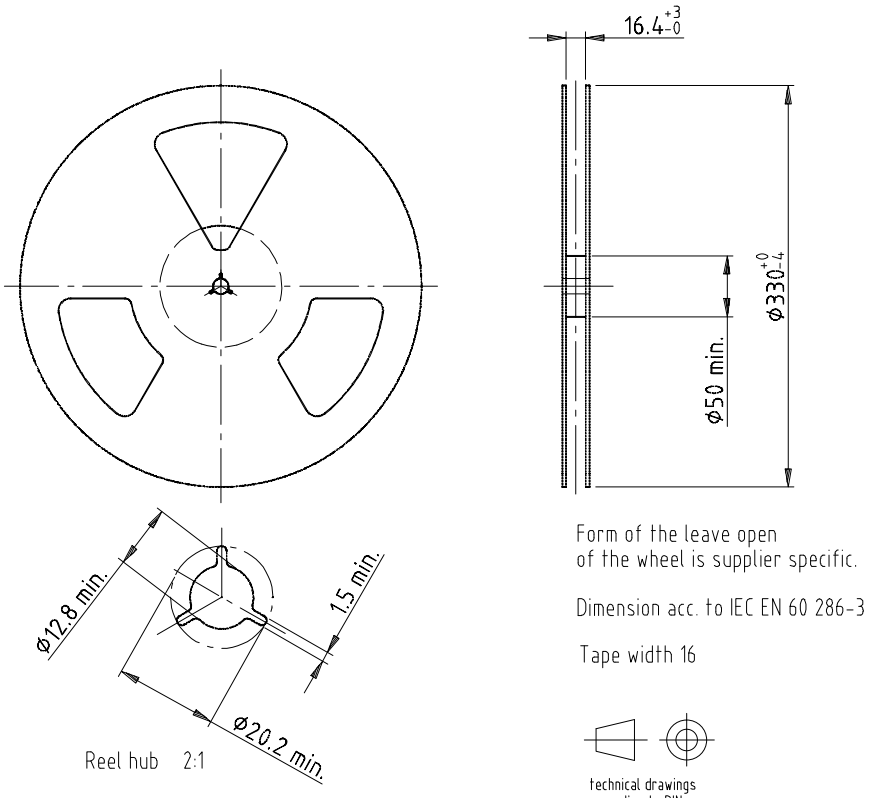
TAPING VERSION TSOP..TT DIMENSIONS in millimeters



TAPING VERSION TSOP..TR DIMENSIONS in millimeters



REEL DIMENSIONS in millimeters



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Issue: 1; 07.05.02

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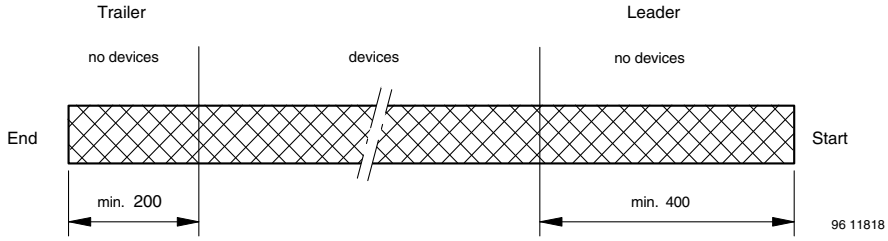
LEADER AND TRAILER Dimensions in millimeters



COVER TAPE PEEL STRENGTH

According to DIN EN 60286-3
0.1 N to 1.3 N
300 mm/min. ± 10 mm/min.
165° to 180° peel angle

LABEL

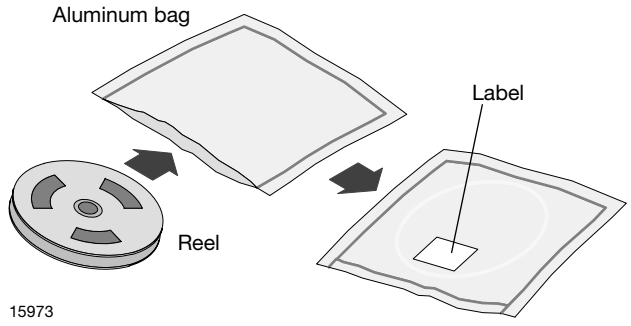
Standard bar code labels for finished goods

The standard bar code labels are product labels and used for identification of goods. The finished goods are packed in final packing area. The standard packing units are labeled with standard bar code labels before transported as finished goods to warehouses. The labels are on each packing unit and contain Vishay Semiconductor GmbH specific data.

VISHAY SEMICONDUCTOR GmbH STANDARD BAR CODE PRODUCT LABEL (finished goods)		
PLAIN WRITTING	**ABBREVIATION**	**LENGTH**
Item-description	-	18
Item-number	INO	8
Selection-code	SEL	3
LOT-/serial-number	BATCH	10
Data-code	COD	3 (YWW)
Plant-code	PTC	2
Quantity	QTY	8
Accepted by	ACC	-
Packed by	PCK	-
Mixed code indicator	MIXED CODE	-
Origin	xxxxxxx+	Company logo
LONG BAR CODE TOP	**TYPE**	**LENGTH**
Item-number	N	8
Plant-code	N	2
Sequence-number	X	3
Quantity	N	8
Total length	-	21
SHORT BAR CODE BOTTOM	**TYPE**	**LENGTH**
Selection-code	X	3
Data-code	N	3
Batch-number	X	10
Filter	-	1
Total length	-	17

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

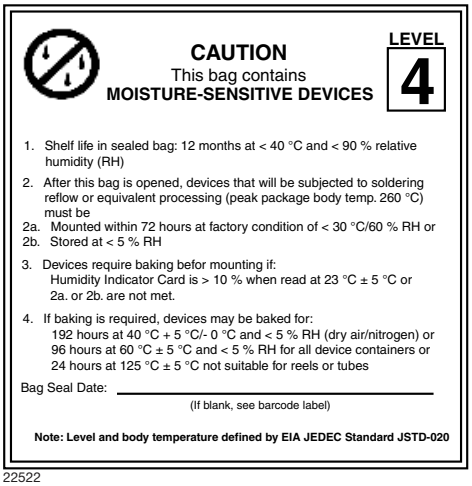
- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 72 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C/- 0 °C and < 5 % RH (dry air/nitrogen)
or
96 h at 60 °C + 5 °C and < 5 % RH for all device containers
or
24 h at 125 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JSTD-020 level 4 label is included on all dry bags.



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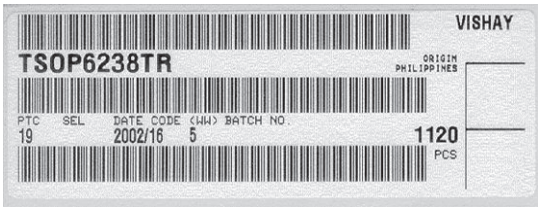
EIA JEDEC standard JSTD-020 level 4 label is included on all dry bags

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electro-static sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABELS

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.