

Receive I/Q Module Using Medium Barrier Schottky Mixers 850 - 960 MHz

Rev. V4

Features

- Excellent Carrier Suppression ~43 dBc
- Sideband Suppression ~ 41 dBc
- 0 to +6 dBm LO Drive
- Excellent ACPR ~ 77 dBc
- Low Output Noise Floor ~161 dBm/Hz
- Phase Balance < 2.5 deg
- Lead-Free 6 x 6 mm PQFN Package
- 100% Matte Tin Plating over Copper
- Halogen-Free "Green" Mold Compound
- 260°C Reflow Compatible
- RoHS Compliant

Description

M/A-COM's MAIA-007859-000100 is a glass / silicon monolithic 850-960MHz, I/Q Modulator/Demodulator. Encapsulated in a low cost, miniature surface mount PQFN 6mm square, 28-lead plastic package, the die utilizes M/A-COM's unique HMIC silicon/glass and GaAs processes. The product performance maximizes the advantages provided by both processes through the realization of low loss passive elements and efficient diode technology. The net result provides excellent harmonic suppression and output noise performance. In addition, the incorporated monolithic design techniques provide unparalleled amplitude and phase balance performance during demodulation thus adding to the unit's overall versatility.

Applications

These modulators/demodulators are well suited for GSM and CDMA Cellular base station applications, as well as most RFID systems, particularly where small size and high performance are required. Typical applications include quadrature modulation requirements in wireless receivers and transmitters.

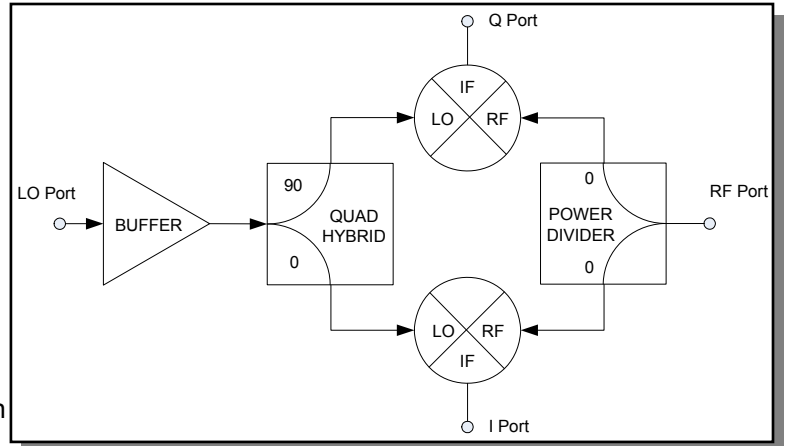
Ordering Information

Part Number	Package
MAIA-007859-000100	Bulk Packaging
MAIA-007859-0001TR	1000 piece reel
MAIA-007859-0001TB	Sample Test Board

Note: Reference Application Note M513 for reel size information.

* Restrictions on Hazardous Substances, European Union Directive 2002/95/EC.

Functional Block Diagram



Pin Configuration

Pin No.	Function	Pin No.	Function
1	VCC	15	GND
2	GND	16	GND
3	GND	17	GND
4	LO	18	RF
5	GND	19	GND
6	GND	20	GND
7	GND	21	GND
8	GND	22	GND
9	GND	23	GND
10	GND	24	GND
11	I	25	Q
12	GND	26	GND
13	GND	27	GND
14	GND	28	GND

The exposed pad centered on the package bottom must be connected to RF and DC ground. (For PQFN Packages)

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Electrical Specifications (Modulator): $T_A = 25^\circ\text{C}$, $Z_0 = 50\Omega$, $V_{CC} = +5.0\text{V}$

Parameter	Test Conditions	LO Frequency	Units	Min	Typ	Max
LO Input Power	-	-	dBm	0	-	6
I, Q Input Power	-	-	dBm	-	-	10
Output Power	LO Drive = +2 dBm I/Q = -7 dBm, I/Q = 400 kHz $F_{RF} = F_{LO} - 0.4 \text{ MHz}$	900 MHz 850-960 MHz	dBm dBm	-13.8 -14	-13 -13	- -
LO Carrier Suppression	LO Drive = +2 dBm I/Q = -7 dBm, I/Q = 400 kHz $F_{RF} = F_{LO} - 0.4 \text{ MHz}$	850-960 MHz	dBc	34	43	-
SSB Rejection ¹	LO Drive = +2 dBm I/Q = -7 dBm, I/Q = 400 kHz $F_{RF} = F_{LO} + 0.4 \text{ MHz}$	850-960 MHz	dBc	32	41	-
3 x 1 Harmonic Suppression	LO Drive = +2dBm I/Q = -7 dBm, I/Q = 400 kHz $F_{RF} = F_{LO} + 1.2 \text{ MHz}$	850-960 MHz	dBc	52	62	-
5 x 1 Harmonic Suppression	LO Drive = +2 dBm I/Q = -7 dBm, I/Q = 400 kHz $F_{RF} = F_{LO} + 2.0 \text{ MHz}$	850-960 MHz	dBc	84	93	-
ACPR CDMA 2000 ²	LO Drive = +2 dBm BB AC Voltage = 275 mVp-p	900 MHz Carrier Freq	dBc	73	77	-
Output Noise Floor	LO Drive = +2 dBm, I/Q = -7 dBm Freq offset = 20 MHz	850-960 MHz	dBm/Hz	-	-163	-
IF Bandwidth	$850 \text{ MHz} \leq \text{LO} \leq 970 \text{ MHz}$	-	MHz	65	-	-
LO Return Loss ³	LO Drive = +2 dBm $F_{RF} = F_{LO} + 0.4 \text{ MHz}$	850-960 MHz	dB	-	9	-
RF Return Loss	LO Drive = +2 dBm RF Input = -7 dBm $F_{RF} = F_{LO} + 0.4 \text{ MHz}$	850-960 MHz	dB	-	9.5	-
Supply Voltage	VCC	-	V	4.5	5.0	5.5
Supply Current	ICC	Typical @ 25°C	mA	50	75	100

- When the LO frequency is greater than the RF frequency, the upper sideband is suppressed.
- The Baseband I and Q input signals were generated using the following settings in the Agilent E3844C Vector Signal Generator:
 FWD CDMA2000 SR1 Pilot
 Filter: IS-95 Mod w/EQ
 Link: Forward
 IQ Mod Filter: Through
 PRE Clip: 100.0 %
- LO Port Return Loss may be improved by inserting a 6.2 nH coil (e.g. Coilcraft type 0402CS-6N2XJL) in series with the LO input terminal.

Electrical Specifications (Demodulator): $T_A = 25^\circ\text{C}$, $Z_0 = 50\Omega$, $V_{CC} = +5.0\text{V}$

Parameter	Test Conditions	LO Frequency	Units	Min	Typ	Max
LO Input Power	-	850-900 MHz	dBm	0	-	6
Conversion Loss	LO Drive = +2 dBm RF Input = -7 dBm $F_{RF} = F_{LO} + 0.4 \text{ MHz}$	900 MHz 850-960 MHz	dB dB	- -	12 12	- -
Amplitude Imbalance	LO Drive = +2 dBm RF Input = -7 dBm $F_{RF} = F_{LO} + 0.4 \text{ MHz}$	850-960 MHz	dB	-	0.05	0.2
Phase Imbalance ⁴	LO Drive = +2 dBm RF Input = -7 dBm $F_{RF} = F_{LO} + 0.4 \text{ MHz}$	850-960 MHz	deg	-	0.5	2.5
Input IP3	LO Drive = +2 dBm RF Input = -7 dBm (each tone) Tone 1 is 10 MHz above LO Freq Tone 2 is 11 MHz above LO Freq	850-960 MHz	dBm	28.5	30	-
1 dB Compression Point	LO Drive = +2 dBm $F_{RF} = F_{LO} + 0.4 \text{ MHz}$	850-960 MHz	dBm	10	12	-
IF Bandwidth	$850 \text{ MHz} \leq \text{LO} \leq 970 \text{ MHz}$ $F_{RF} = F_{LO} + F_{IF}$; $0 \leq F_{IF} \leq 65 \text{ MHz}$	-	MHz	65	-	-
LO Return Loss ³	LO Drive = +2 dBm RF Input = -7 dBm $F_{RF} = F_{LO} + 0.4 \text{ MHz}$	850-960 MHz	dB	-	9	-
RF Return Loss	LO Drive = +2 dBm RF Input = -7 dBm $F_{RF} = F_{LO} + 0.4 \text{ MHz}$	850-960 MHz	dB	-	9.5	-
Supply Voltage	VCC	-	V	4.5	5.0	5.5
Supply Current	ICC	Typical @ 25°C	mA	50	75	100

4. When the LO frequency is greater than the RF frequency, the “Q” output leads the “I” output by 90 degrees nominal.

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Absolute Maximum Ratings^{5,6}

Parameter	Absolute Maximum
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +150°C
Incident LO Power	+15 dBm C.W.
Incident RF Power	+20 dBm C.W.
Supply Voltage (VCC)	-0.5V to 6.0V
Supply Current (ICC)	100 mA

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.

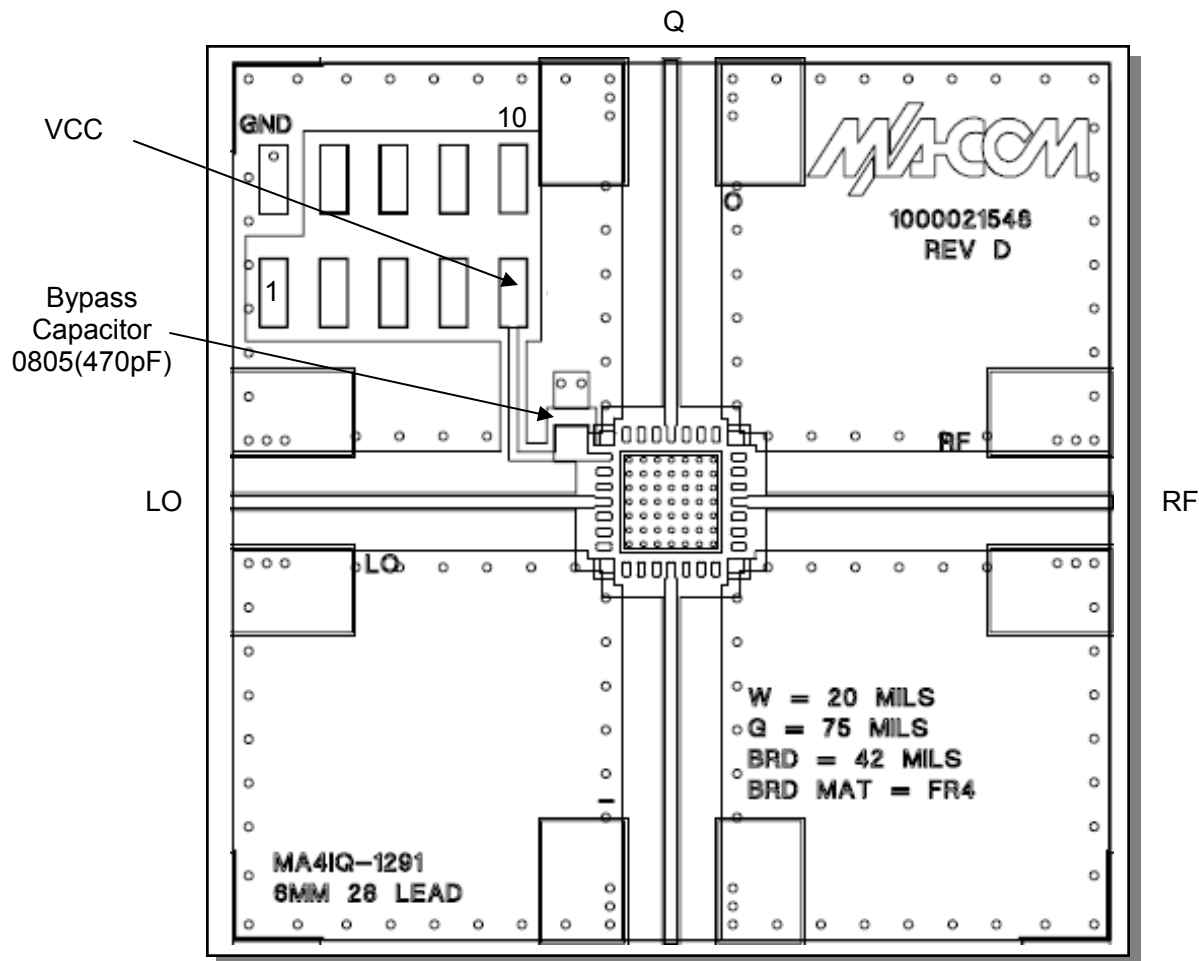
Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

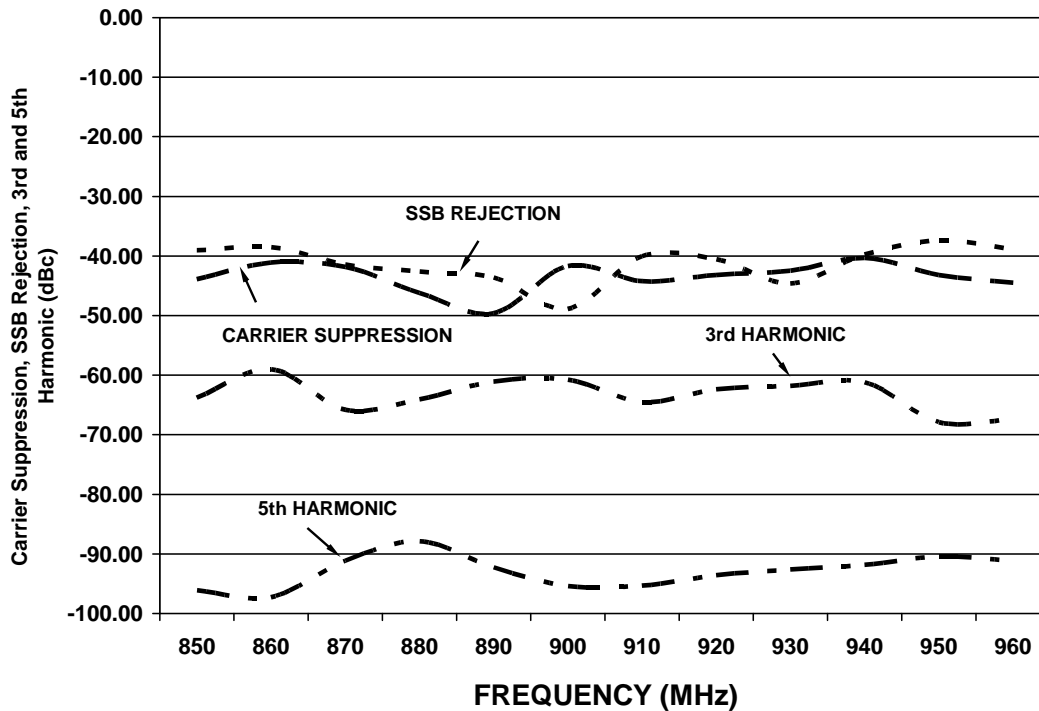
Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Recommended PCB Configuration

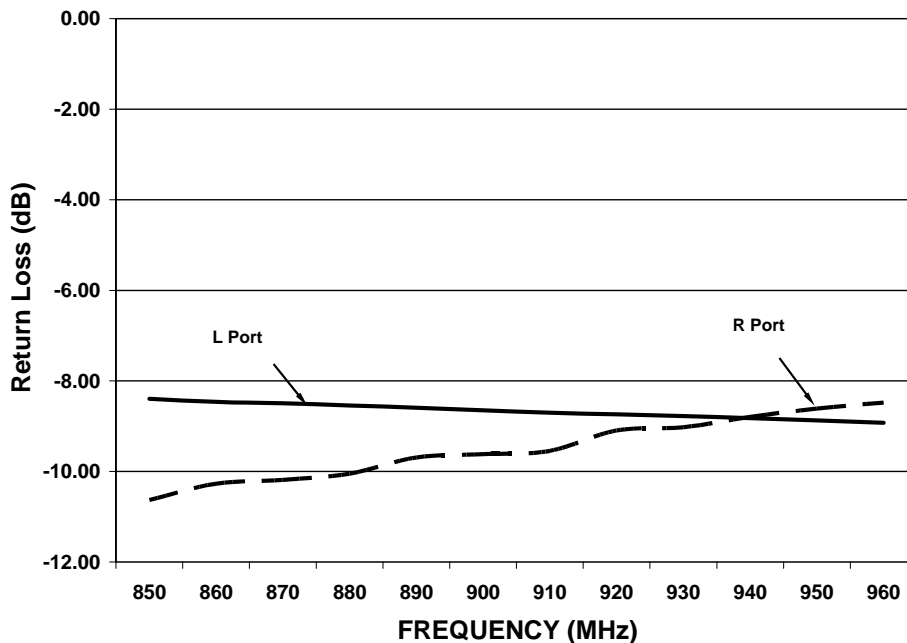


Typical Performance Curves @ +2 dBm LO Drive, 400 kHz C.W. I/Q

Harmonic and Carrier Suppression

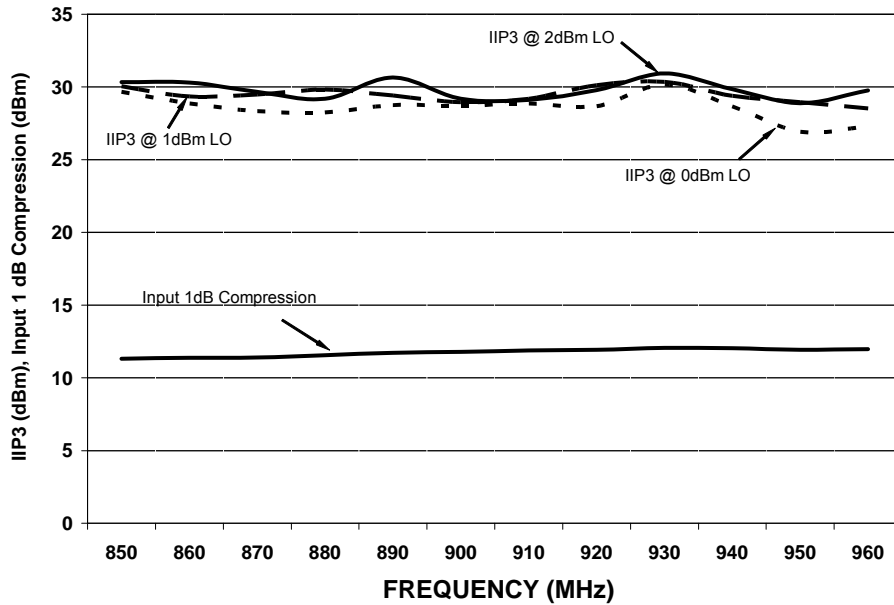


LO and RF Port Match

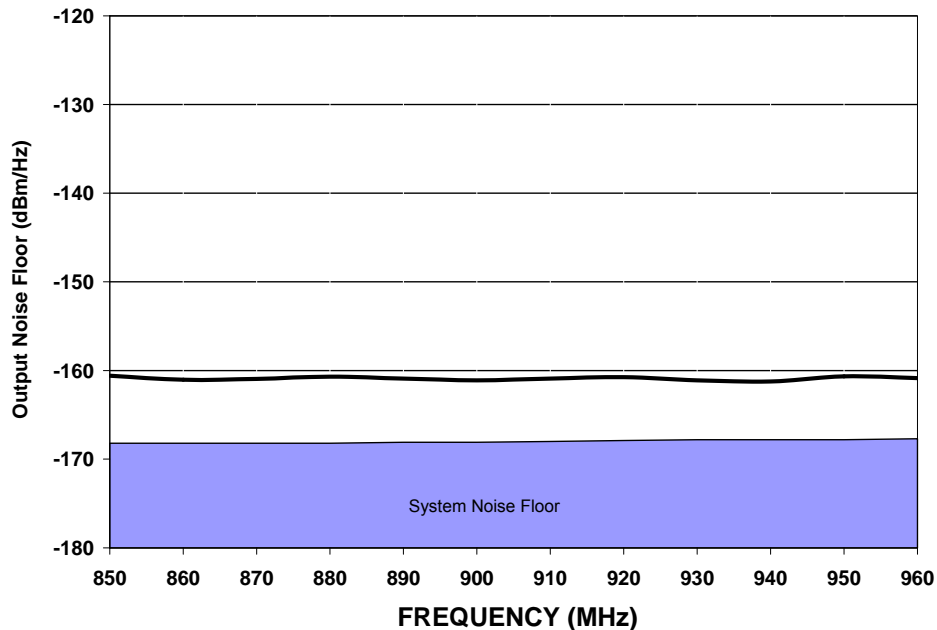


Typical Performance Curves @ +2 dBm LO Drive, 400 kHz C.W. I/Q

Input IP3 & 1 dB Compression Point

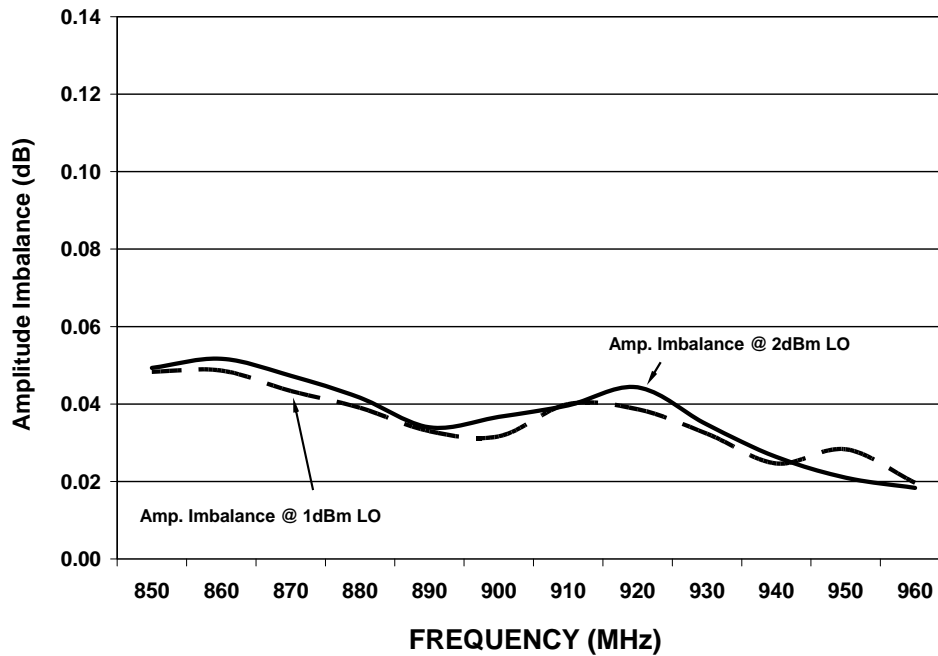


Output Noise Floor

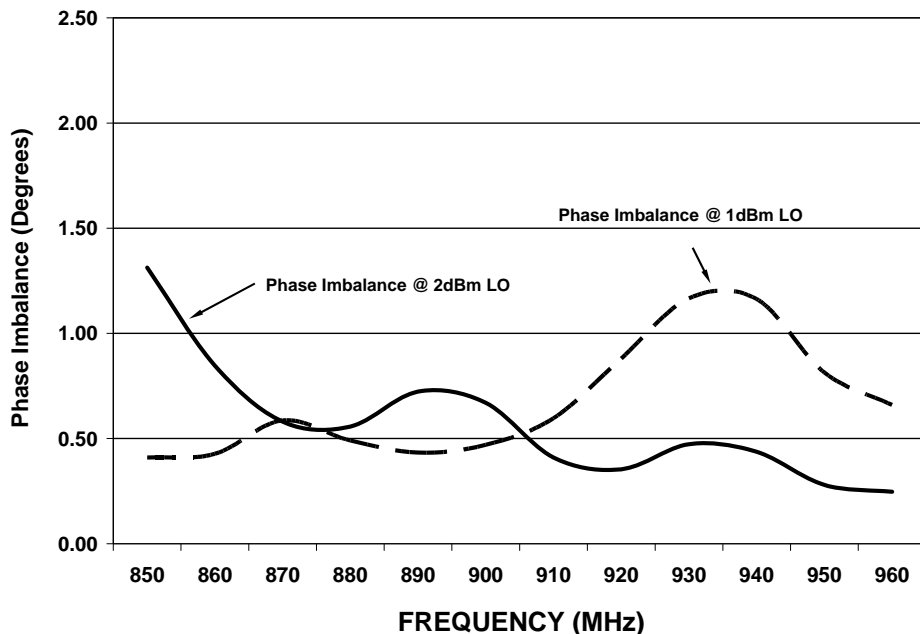


Typical Performance Curves @ +2 dBm LO Drive, 400 kHz C.W. I/Q

Demodulator Amplitude Balance vs. LO Power

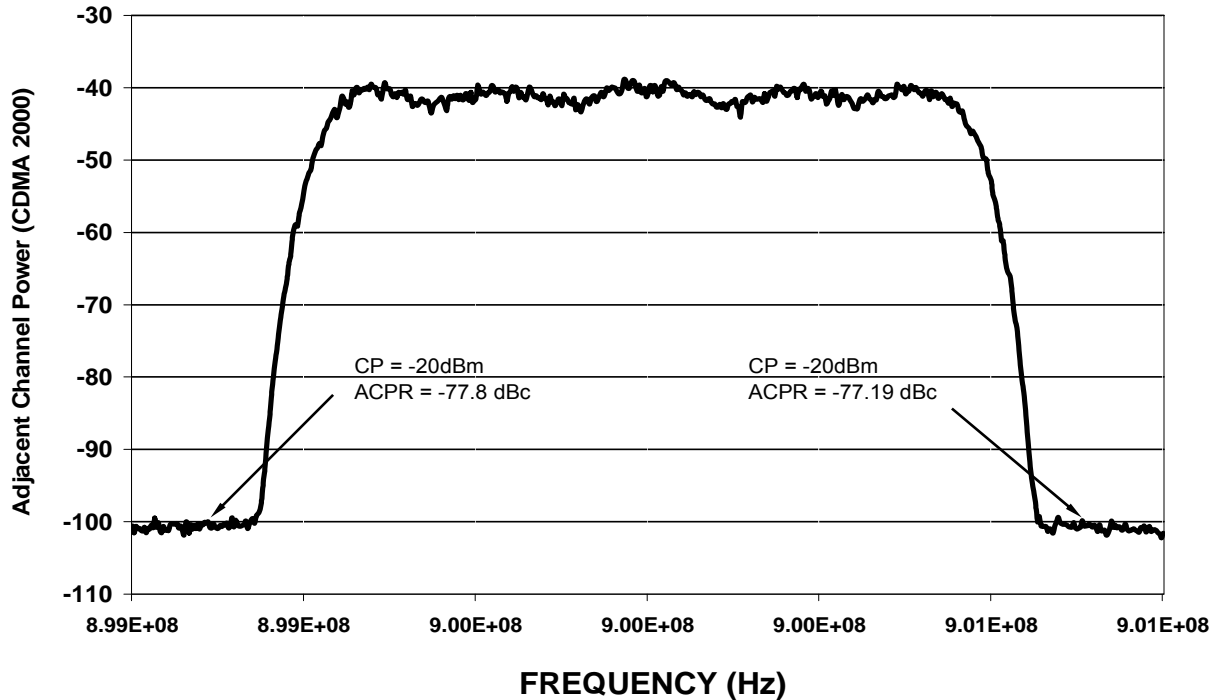


Demodulator Phase Balance vs. LO Power

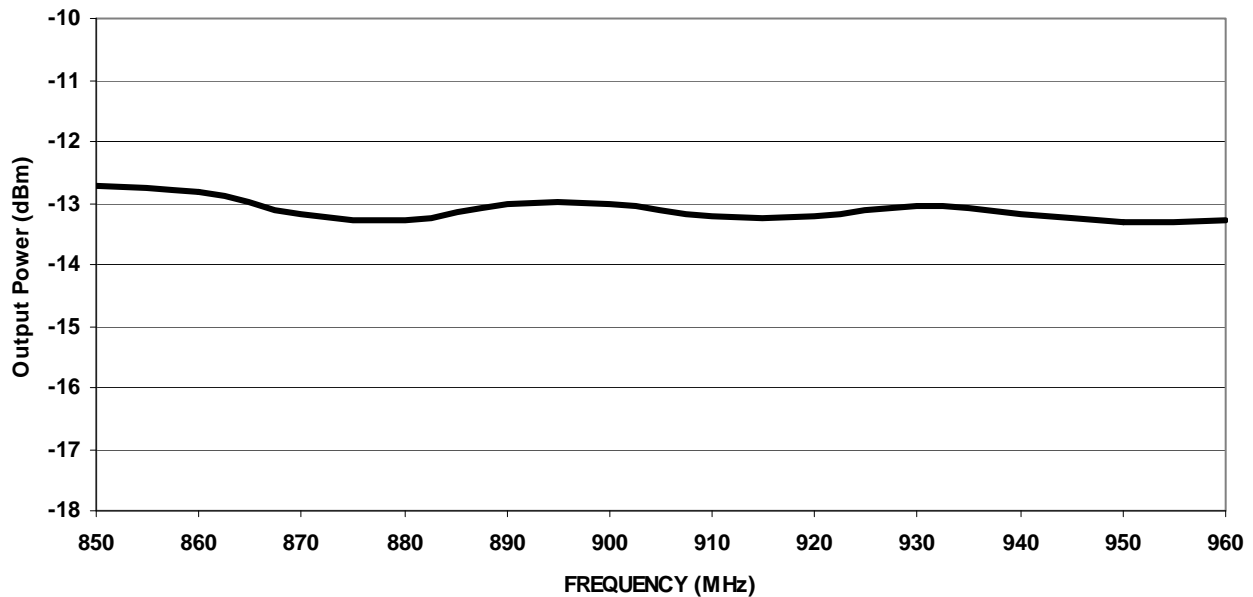


Typical Performance Curves @ +2 dBm LO Drive

CDMA 2000 @ 900 MHz, ACPR @ 885 kHz, VCC = 5V



OUTPUT POWER

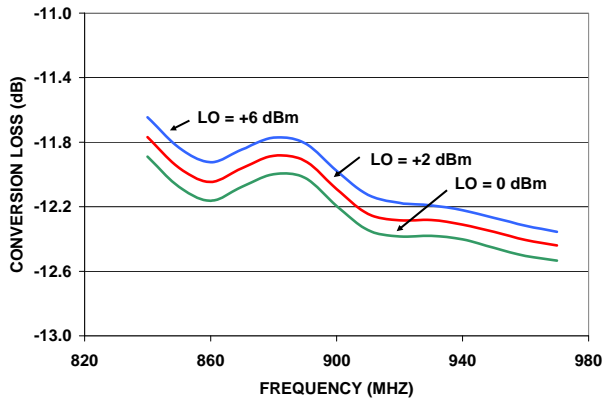


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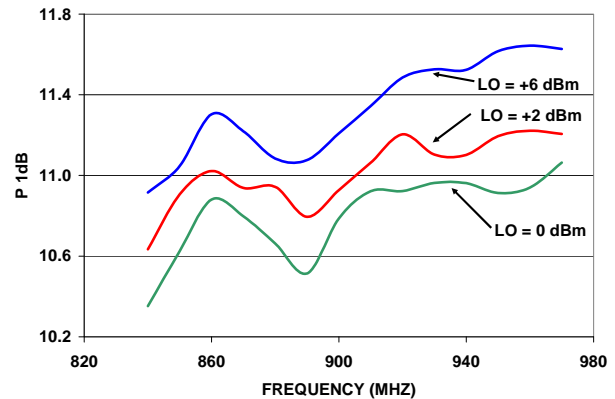
Rev. V4

Electrical Specifications: $T_A = 25^\circ\text{C}$, $Z_0 = 50\Omega$, $V_{CC} = +3.3\text{V}$

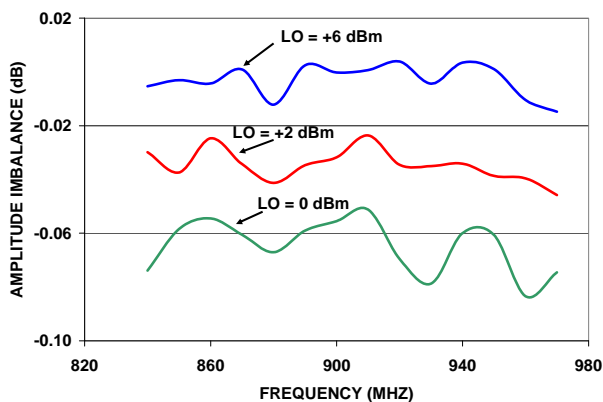
Conversion Loss vs. PLO: $V_{CC} = 3.3\text{V}$



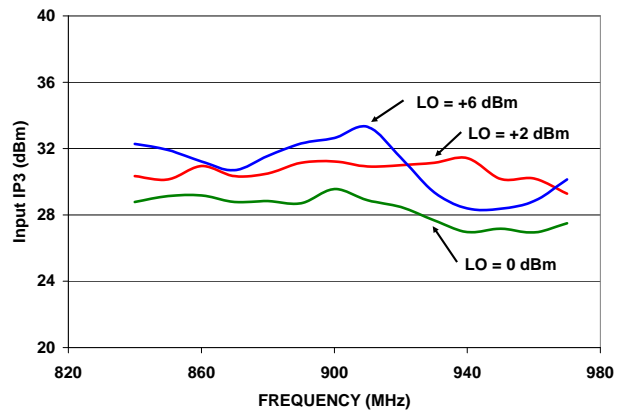
P_{1dB} vs. PLO: $V_{CC} = 3.3\text{V}$



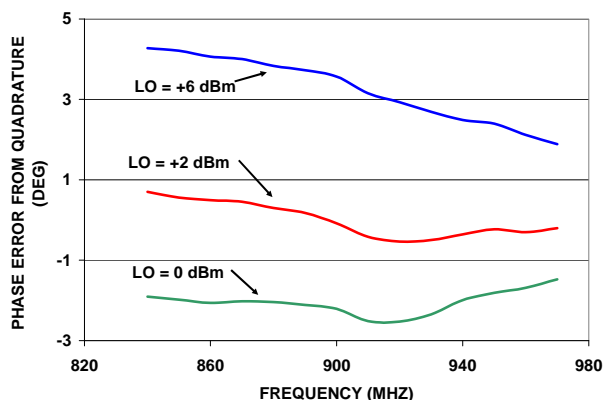
Amplitude Imbalance vs. PLO: $V_{CC} = 3.3\text{V}$



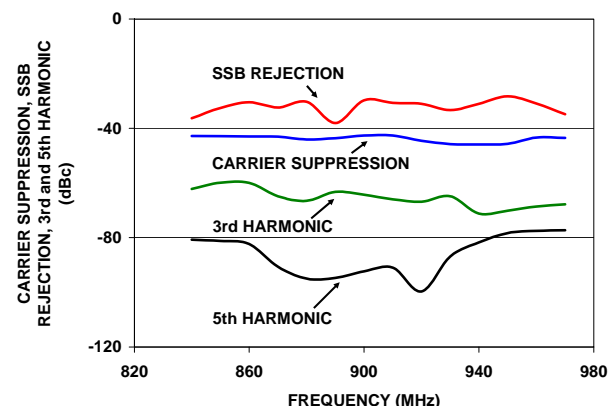
IIP3 vs. PLO: $V_{CC} = 3.3\text{V}$



**Phase Error from Quadrature vs PLO:
 $V_{CC} = 3.3\text{V}$**



**Carrier Suppression, SSB Rejection,
3rd and 5th Harmonic Suppression:
 $V_{CC} = 3.3\text{V}$, PLO = +2 dBm**



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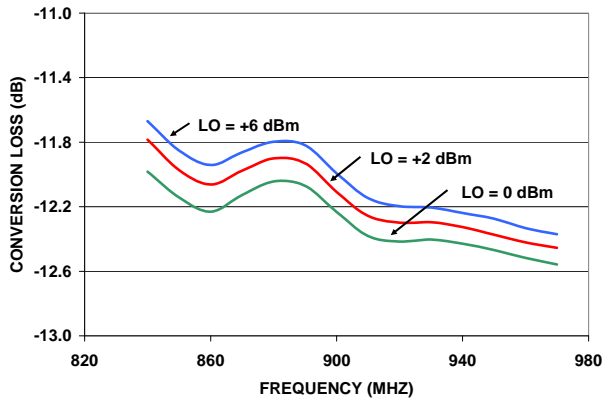
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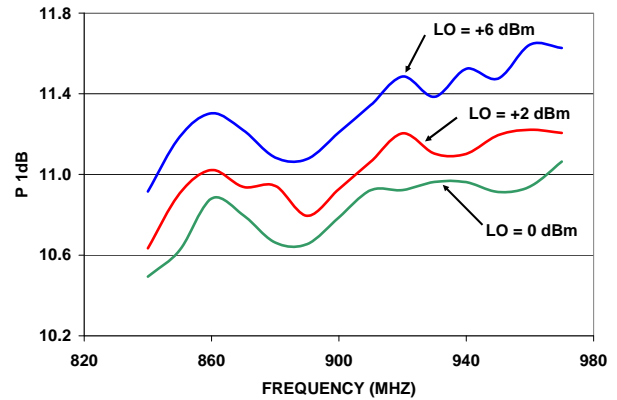
Rev. V4

Electrical Specifications: $T_A = 25^\circ\text{C}$, $Z_0 = 50\Omega$, $V_{CC} = +2.85\text{V}$

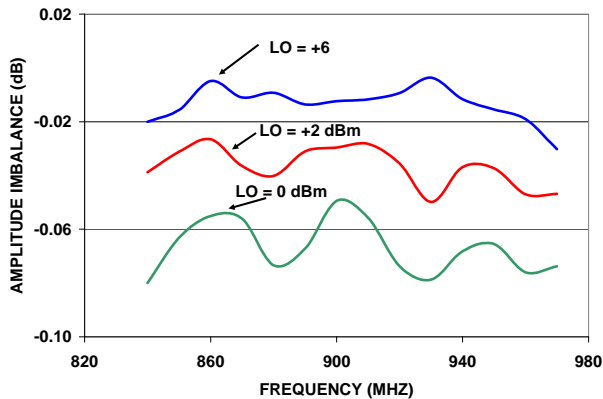
Conversion Loss vs. PLO: $V_{CC} = 2.85\text{V}$



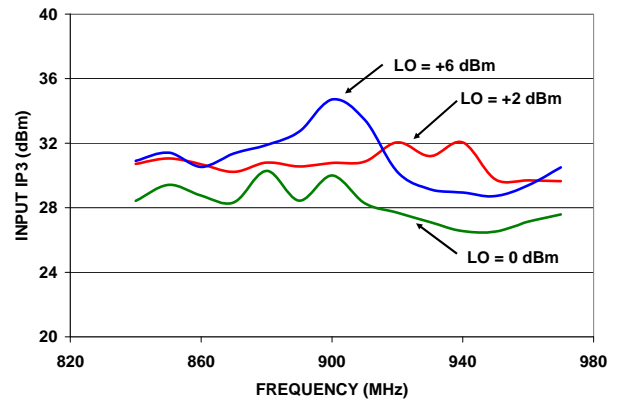
P_{1dB} vs. PLO: $V_{CC} = 2.85\text{V}$



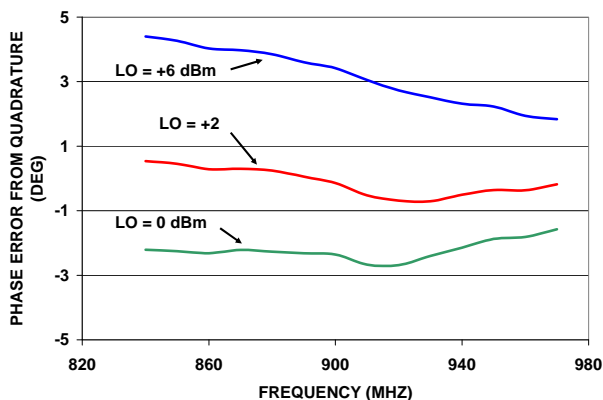
Amplitude Imbalance vs. PLO: $V_{CC} = 2.85\text{V}$



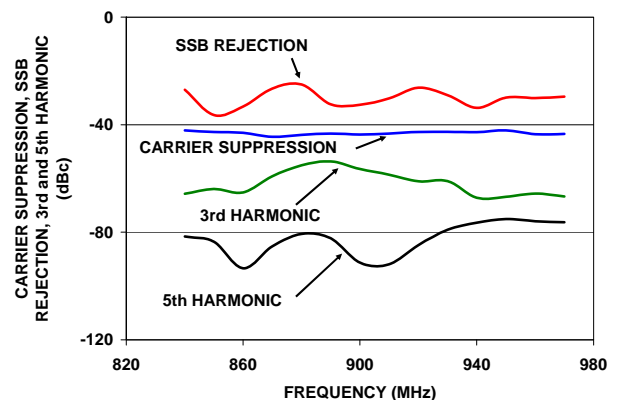
IIP3 vs. PLO: $V_{CC} = 2.85\text{V}$



**Phase Error from Quadrature vs PLO:
 $V_{CC} = 2.85\text{V}$**



**Carrier Suppression, SSB Rejection,
3rd and 5th Harmonic Suppression:
 $V_{CC} = 3.3\text{V}$, $P_{LO} = +2\text{ dBm}$**



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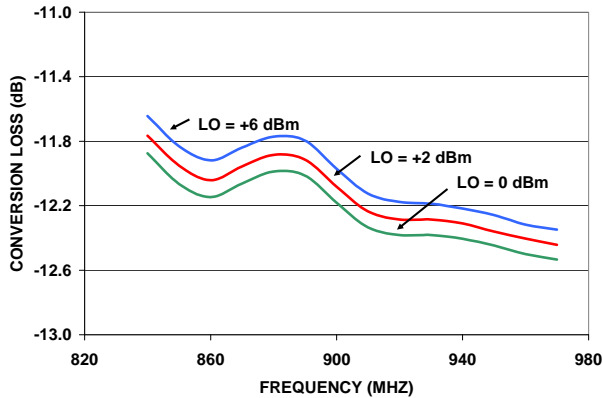
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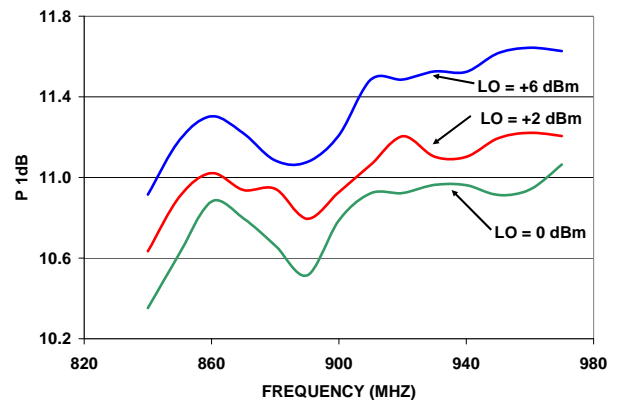
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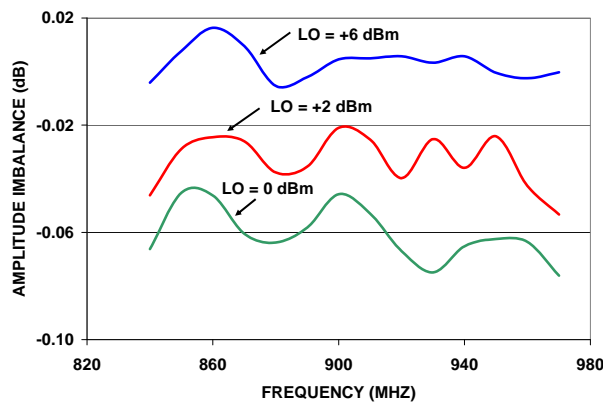
Conversion Loss vs. PLO: $V_{CC} = 3.6\text{V}$



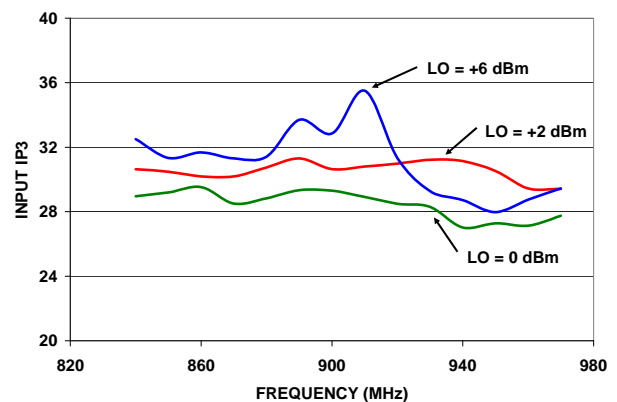
P_{1dB} vs. PLO: $V_{CC} = 3.6\text{V}$



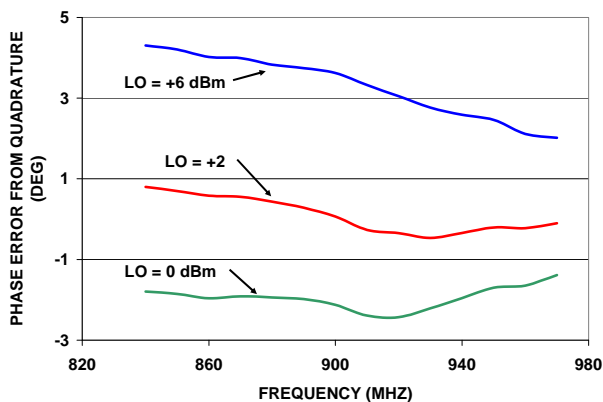
Amplitude Imbalance vs. PLO: $V_{CC} = 3.6\text{V}$



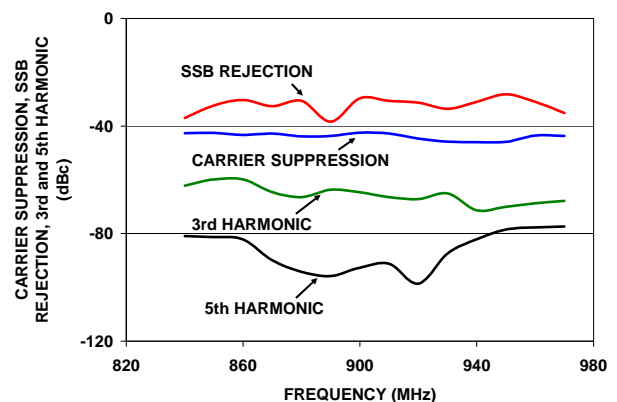
IIP3 vs. PLO: $V_{CC} = 3.6\text{V}$



**Phase Error from Quadrature vs PLO:
 $V_{CC} = 3.6\text{V}$**



**Carrier Suppression, SSB Rejection,
3rd and 5th Harmonic Suppression:
 $V_{CC} = 3.6\text{V}$, PLO = +2 dBm**



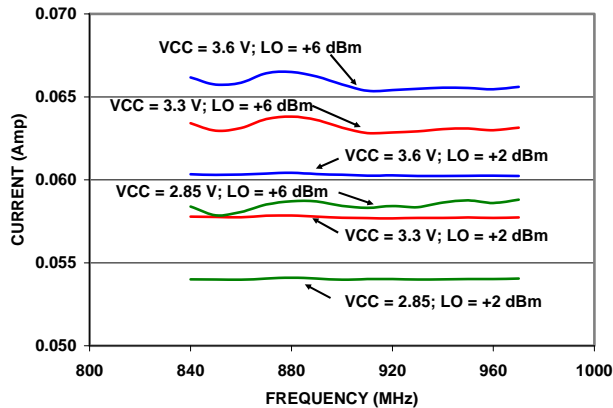
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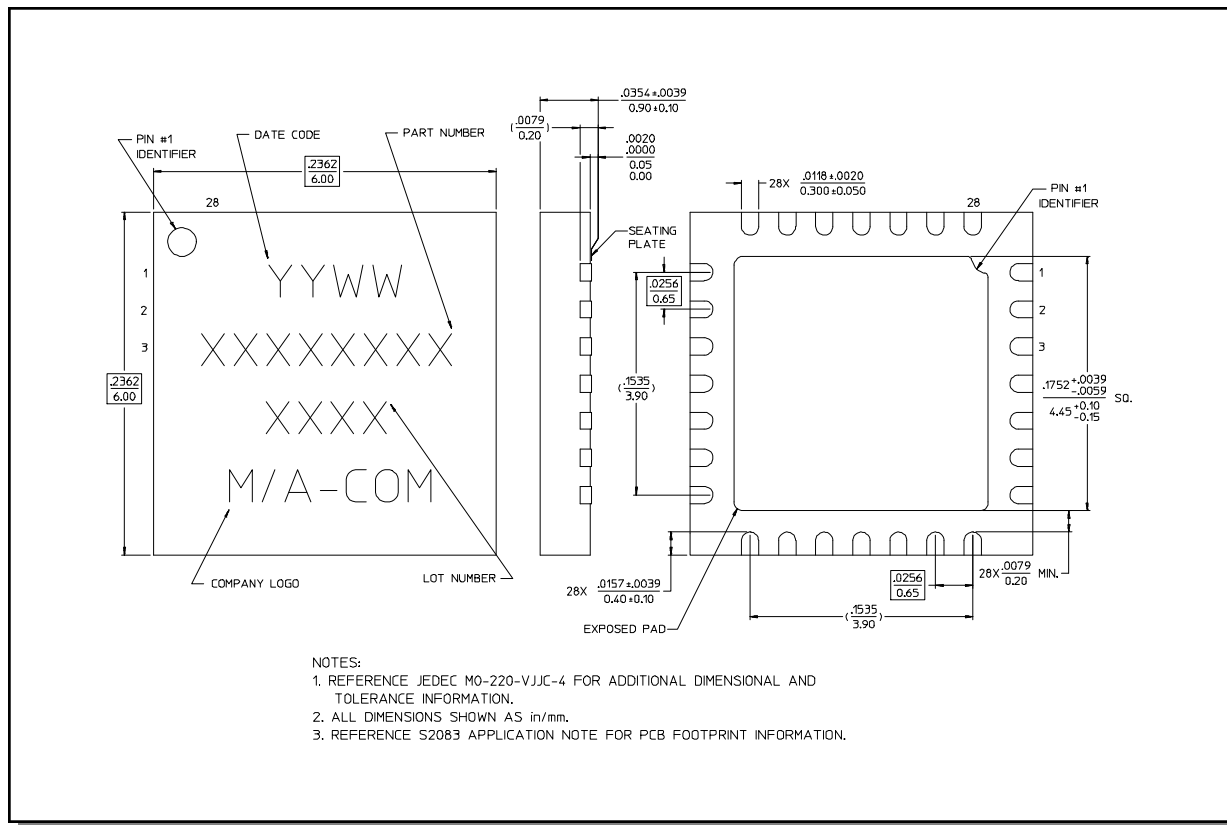
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I(VCC) vs. VCC AND P_{LO}



Lead-Free 6 x 6 mm, 28-lead PQFN†



† Reference Application Note M538 for lead-free solder reflow recommendations.