

LINEAR INTEGRATED CIRCUITS

DUAL PERIPHERAL POSITIVE-AND DRIVER

DESCRIPTION

The SG5541B/SG55461/SG55471 (SG75451B/SG75461/SG75471) series of dual peripheral Positive-AND drivers are a family of versatile devices designed for use in systems that employ TTL or DTL logic. This family of drivers are direct replacements for the Texas Instruments SN55451B/61/71 (SN75451B/61/71)series. Diode-clamped inputs simplify circuit design. Typical applications include high-speed logic buffers, power drivers, relay drivers, MOS drivers, line drivers, and memory drivers. The SG55451B/SG55461/SG55471 drivers are characterized for operation over the full military ambient temperature range of -55°C to 125°C and the SG75451B/SG75461/SG75471 drivers are chracterized for operation from 0°C to 70°C.

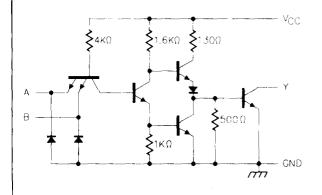
FEATURES

- . 300mA output current capability
- High-voltage output
- . No output latch-up at 20V
- · High speed switching
- . TTL or DTL compatible diode-clamped inputs
- · Standard supply voltages

HIGH RELIABILITY FEATURES

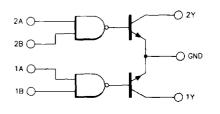
- SG55451B/SG55461/SG55471
- Available to MIL-STD-883
- + Scheduled for MIL-M-38510 QPL listing
- SG level "S"processing available

EQUIVALENT CIRCUIT SCHEMATIC (each driver)



BLOCK DIAGRAM

Positive Logic: Y = AB



FUNCTION TABLE (each gate)

A	В	Y
L	L	L (on-state)
L	н	L (on-state)
н	L	L (on-state)
н	н	H (off-state)

H = High Level, L = Low Level

April 1990

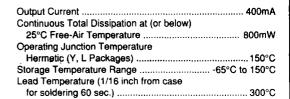
ABSOLUTE MAXIMUM RATINGS (Note 1)

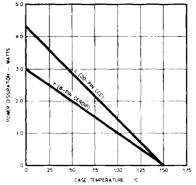
Supply Voltage (V _{cc})	7V
Input Voltage	
Interemitter Voltage	
Off-state Output Voltage	
X5451B Series	30V
X5461 Series	
X5471 Series	70V
N. C. A. E. Prophysics Prop. 1 Med. 10 Co.	

Note 1. Exceeding these ratings could cause damage to the device. THERMAL DERATING CURVES

	2.5		T						
	20	<u> </u>		-			-	+	-
TON - WATTS	1.5		-					-	
POWER DISSIPATION	10		O. PM	CEROIP)				+	-
	0.5			10/6)					-
	0	1	25	50 AMB#	75 ENT TEM	100 PERATURE	125 - °C	150	175

MAXIMUM POWER DISSIPATION vs AMBIENT TEMPERATURE





MAXIMUM POWER DISSIPATION vs CASE TEMPERATURE

RECOMMENDED OPERATING CONDITIONS (Notes 2 & 3)

Operating Ambient Temperature Range SG55451B, SG55461, SG55471-55°C to 125°C SG75451B, SG75461, SG754710°C to 70°C

Note 2. Range over which device is functional.

Note 3. The substrate (pin 8) must always be at the most-negative device voltage for proper operation.

ELECTRICAL SPECIFICATIONS

(Unless otherwise specified, these specifications apply over the operating ambient temperatures for SG55451B/461/471 with -55°C \leq T_A \leq 125°C, and SG75451B/461/471 with 0°C \leq T_A \leq 70°C. Typical values are tested at V_{cc} = 5V, and T_A = 25°C. Low duty cycle pulse testing techniques are used which maintains junction and case temperatures equal to the ambient temperature.)

Parameter	Test Conditions	S	SG55451B SG55461 SG55471			SG75451B SG75461 SG75471		
		Min.	Typ.	Max.	Min.	Тур.	Max.	
High-level Input Voltage (V,,)		2			2		ų.	[V
Low-level Input Voltage (V,,)		I	Ī	0.8	ĺ		0.8] v
Input Clamp Voltage (V,,)	$\dot{V}_{cc} = MIN, I_M = -12mA$	i	-1.2	-1.5	1	-1.2	-1.5	l v
High-level Output Current (IOH)	$V_{CC} = MIN, V_{IH} = 2V,$	Ī		300	Ī]	100	μA
, ,,,,	Ŭ _~ = 30V SGX5451B	- 1	ĺ	1	İ		ĺ	<u> </u>
	V _{OH} ≈ 35V SGX5461	1	1	1	l		1	
	VOH = 70V SGX5471	- 1	ţ	i			ĺ	ı
Low-level Output Voltage (Vol.)	$V_{CC} = MIN, V_{II} = 0.8V, I_{OL} = 100mA$	ľ	0.25	0.5	ĺ	0.25	0.4	Ιv
	V ₀₀ = MIN, V _k = 0.8V, I _{0k} = 300mA		0.5	8.0	ì	0.5	0.7	l v
Input Current at Max V _{IN} (I _{IN})	$V_{CC} = MAX, V_{IN} = 5.5V$	ľ		1.0	ľ		1.0	mA
High-level Input Current (I)	V = MAX, V = 2.4V			40		ì	40	μА
Low-level Input Current (I,,)	$V_{CC} = MAX, V_{IN} = 0.4V$	1	-1.0	-1.6		-1.0	-1.6	mΑ
Supply Current, Outputs High	$V_{CC}^{CC} = MAX, V_{M}^{N} = 5V$	ľ	8	11		8	11	mA
Supply Current, Outputs Low	V _{CC} = MAX, V _{IN} = 0V			' '	ľ	-		
	SGX5451B	Ì	52	65		52	65	mA
	SGX5461		56	76	i	56	76	mA
	SGX5471		56	76		56	76	mA.

SG55451B/61/71 SERIES

SWITCHING SPECIFICATIONS $(V_{cc} = 5V, T_A = 25^{\circ}C)$

Parameter	Test Conditions	SG55451B SG75451B			SG55461 SG75461			SG55471 SG75471			Units
		Min.	Тур.	Max.	Min.	Тур.	Max.	Min.	Тур.	Max.	
Propagation Delay Time, Low- to-High Level Output			18	25		30	55		30	55	ns
Propagation Delay Time, High- to-Low Level Output	I _c = 200mA, C _i = 15pF,		18	25		25	40		25	40	ns
Transition Time, Low-to-High Output	$I_c = 200\text{mA}, C_t = 15\text{pF},$ $R_t \approx 50\Omega$	ı	5	8		8	20		8	20	ns
Transition Time, High-to-Low Level Output			7	12		10	20		10	20	ns
High-Level Output Voltage	I _c = 300mA,	1		İ			1	1	1	1	l
After Switching	V _s = 20V SGX5451B V _s = 30V SGX5461 V _s = 55V SGX5471	V _s -6.5			V ₈ -10		Ì	V _e -18			mV mV mV

CONNECTION DIAGRAMS & ORDERING INFORMATION (See Notes Below)

Package	Part No. Amblent Temperature Rang	Connection Diagram						
8-PIN CERAMIC DIP Y - PACKAGE	\$\ \text{SG55451BY/883B} \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	1A 1 8 V _{oc} 1B 2 7 2B 1Y 3 6, 2A GND 4 5 2Y						
20-PIN CERAMIC LEADLESS CHIP CARRIER L- PACKAGE	SG55451BL/883B -55°C to 125°C SG55451BL -55°C to 125°C SG55461L/883B -55°C to 125°C SG55471L/883B -55°C to 125°C SG55471L -55°C to 125°C SG55471L	1. N.C. 3 2 1 20 18 11. N.C. 2. 1A 12. 2Y 18 13. N.C. 4 18 13. N.C. 17 15. 2A 14. N.C. 5. 1B 15. 2A 16. N.C. 7. 1Y 7 15. 17. 2B 8. N.C. 8 18. N.C. 8 18. N.C. 9. N.C. 10. GND 9 10 11 12 13 20. V _{CC}						

Note 1. Contact factory for JAN and DESC product availability.

2. All parts are viewed from the top.

3. Product is also available in flat pack. Consult factory for price and delivery.