PYRAMID
SEMICONDUCTOR CORPORATION

P54FCT244/74FCT244

OCTAL BUFFER/LINE DRIVER WITH 3-STATE OUTPUTS

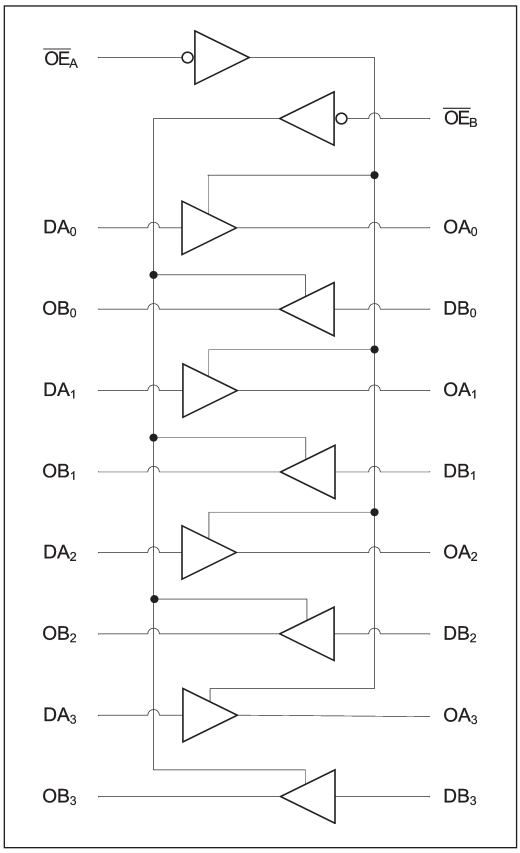
FEATURES

- **Function, Pinout and Drive Compatible with the FCT and F Logic**
- **FCT-A speed at 5.1ns max (MIL)**
- **Output levels compatible with TTL and CMOS**
- **Edge-rate control circuitry for significantly improved noise characteristics**
- **ESD protection exceeds 2000V**
- **Power-off disable feature**
- **Matched rise and fall times**
- **Fully compatible with TTL input and output logic levels**
- **48 mA sink current, 12 mA source current (MIL)**

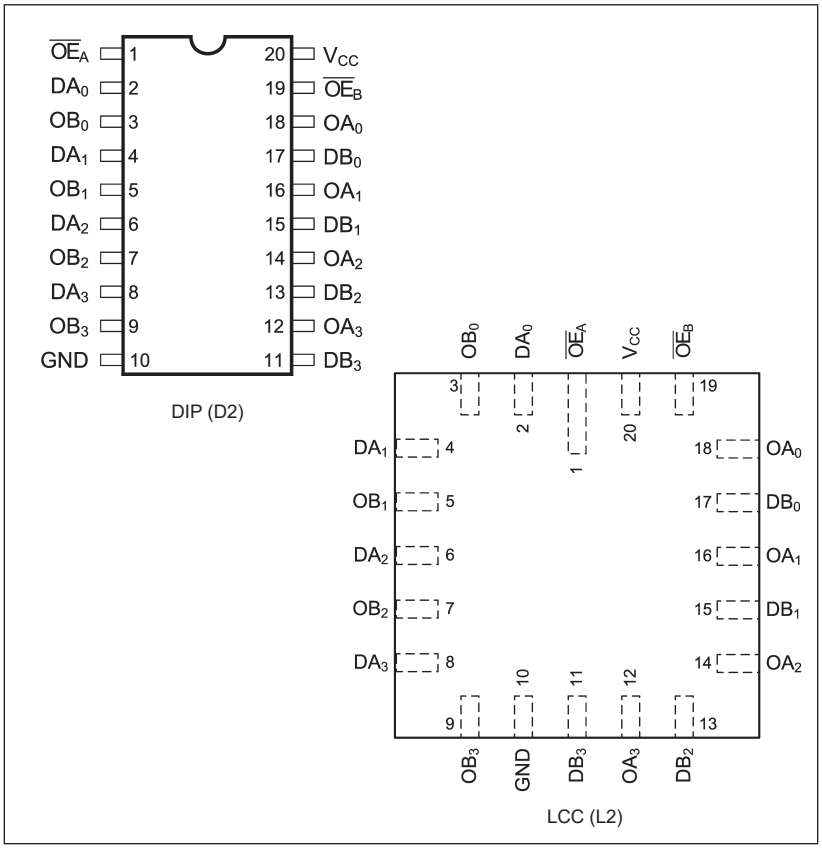
DESCRIPTION

The P54/74FCT244 is a non-inverting octal buffer and line driver designed to be employed as memory address drivers, clock drivers and bus-oriented transmitters/receivers. The device provides speed and drive capabilities equivalent to the fastest bipolar logic counterparts while reducing power dissipation by using advanced CMOS technoloy. The input and output voltage levels allow direct interface with TTL, NMOS and CMOS devices without external components.

FUNCTIONAL BLOCK DIAGRAM



PIN CONFIGURATIONS



MAXIMUM RATINGS[1,2]

Sym	Parameter	Value	Unit
T_{STG}	Storage Temperature	-65 to +150	°C
T_A	Ambient Temperature Under Bias	-65 to +135	°C
V_{CC}	V_{CC} Potentional to Ground	-0.5 to +7.0	V
P_T	Power Dissipation	0.5	W
I_{OUTPUT}	Current Applied to Output	120	mA
V_{IN}	Input Voltage	-0.5 to +7.0	V
V_{OUT}	Voltage Applied to Output	-0.5 to +7.0	V

RECOMMENDED OPERATING CONDITIONS

Grade	Ambient Temp	GND	V_{cc}
Military	-55°C to +125°C	0V	5.0V ± 10%
Industrial	0°C to +85°C	0V	5.0V ± 5%

CAPACITANCES

(V_{CC} = 5.0V, T_A = 25°C, f = 1.0MHz)

Sym	Parameter	Conditions	Typ	Unit
C_{IN}	Input Capacitance	V_{IN} = 0V	6	pF
C_{OUT}	Output Capacitance	V_{OUT} = 0V	8	pF

DC ELECTRICAL CHARACTERISTICS

(Over Recommended Operating Temperature & Supply Voltage)

Sym	Parameter	Test Conditions		Min	Typ	Max	Unit
V_{IH}	Input HIGH Voltage			2.0			V
V_{IL}	Input LOW Voltage					0.8	V
I_{IH}	Input HIGH Current	V_{CC} = Max, V_I = 2.7V				±1	µA
I_{IL}	Input LOW Current	V_{CC} = Max, V_I = 0.5V				±1	µA
I_{OZH}	High Impedance Output Current	V_{CC} = Max, V_O = 2.7 V				±1	µA
I_{OZL}		V_{CC} = Max, V_O = 0.5 V				±1	µA
I_I	Input HIGH Current	V_{CC} = Max, V_I = V_{CC} (Max)				±1	µA
V_{IC}	Clamp Diode Voltage	V_{CC} = Min, I_{IN} = -18mA			-0.7	-1.2	V
V_H	Input Hysteresis				200		mV
I_{CC}	Quiescent Power Supply Current	V_{CC} = Max, V_{IN} = GND or V_{CC}			0.01	1	mA
V_{OH}	Output HIGH Voltage	V_{CC} = Min, V_{IN} = V_{IH} or V_{IL}	I_{OH}=-6mA (MIL)	2.4	4.3		V
			I_{OH}=-8mA (IND)				
		V_{CC} = Min, V_{IN} = V_{IH} or V_{IL}	I_{OH}=-12mA (MIL)	2.4	4.0		V
			I_{OH}=-15mA (IND)				
V_{OL}	Output LOW Voltage	V_{CC} = Min, V_{IN} = V_{IH} or V_{IL}	I_{OL} = 48mA (MIL)		0.3	0.55	V
			I_{OL} = 64mA (IND)				
I_{OS}	Short Circuit Current	V_{CC} = Max, V_O = GND		-60	-120	-225	mA
ΔI_{CC}	Quiescent Power Supply Current (TTL inputs)	V_{CC} = Max, V_{IN} = 3.4V, f_1 = 0, Outputs open [3]			0.5	2.0	mA
I_{CCD}	Dynamic Power Supply Current	V_{CC} = Max, One input toggling, 50% Duty cycle, Outputs open, $\overline{OE}_A$ = $\overline{OE}_B$ = GND, OE_B = V_{CC}, V_{IN} ≤ 0.2V or V_{IN} ≥ V_{CC} - 0.2V			0.15	0.25	mA/ mHz
I_{CCH}	Quiescent supply current, outputs high	$\overline{OE}_A$ = $\overline{OE}_B$ = GND For all other inputs, V_{IN} = V_{CC} or GND			1.5		mA
I_{CCL}	Quiescent supply current, outputs low				1.5		mA
I_{CLZ}	Quiescent supply current, outputs three-state	$\overline{OE}_A$ = $\overline{OE}_B$ = V_{CC} For all other inputs, V_{IN} = V_{CC} or GND			1.5		mA

Notes:

1. Operation beyond the limits set forth in the above table may impair the useful life of the device. Unless otherwise noted, these limits are over the operating free-air temperature range.
2. Unused inputs must always be connected to an appropriate logic voltage level, preferably either V_{CC} or ground.
3. Per TTL driven input (V_{IN}=3.4V); all other inputs at V_{CC} or GND.

DC ELECTRICAL CHARACTERISTICS

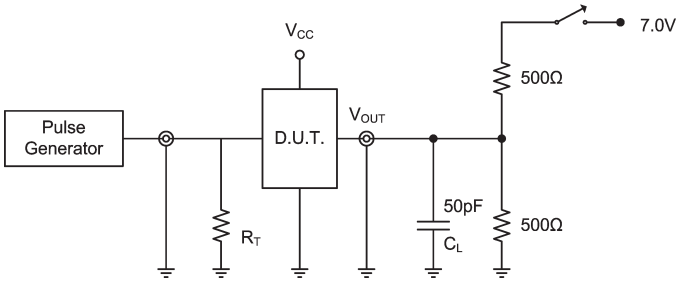
(Over Operating Range)

Sym	Parameter	Test Conditions		Min	Typ	Max	Unit
I_C	Total Power Supply Current	V_{CC} = Max, Outputs open, fi = 10MHz, 50% Duty cycle, $\overline{OE}_A$ = $\overline{OE}_B$ = GND, One bit toggling	V_{IN} = V_{CC} V_{IN} = GND		1.5	3.5	mA
			V_{IN} = 3.4V V_{IN} = GND		1.8	4.5	
		V_{CC} = Max, Outputs open, fi = 2.5MHz, 50% Duty cycle, $\overline{OE}_A$ = $\overline{OE}_B$ = GND, Eight bits toggling	V_{IN} = V_{CC} V_{IN} = GND		3	6	
			V_{IN} = 3.4V V_{IN} = GND		5	14	

AC CHARACTERISTICS

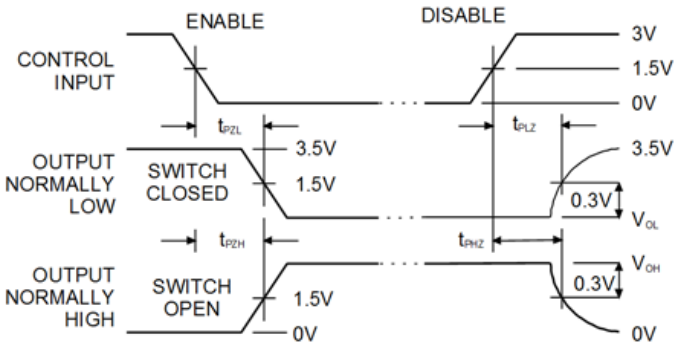
(Over Operating Range for FCT244)

Sym	Parameter	Condition	54/74FCT244				54/74FCT244A				Unit
			Ind		Mil		Ind		Mil		
			Min	Max	Min	Max	Min	Max	Min	Max	
t_{PLH} t_{PHL}	Propagation Delay D_X to O_X	C_L = 50pF R_L = 500Ω	1.5	6.5	1.5	7	1.5	4.8	1.5	5.1	ns
t_{PZH} t_{PZL}	Output Enable Time		1.5	8.0	1.5	8.5	1.5	6.2	1.5	6.5	ns
t_{PHZ} t_{PLZ}	Output Disable Time		1.5	7.0	1.5	7.5	1.5	5.6	1.5	5.9	ns

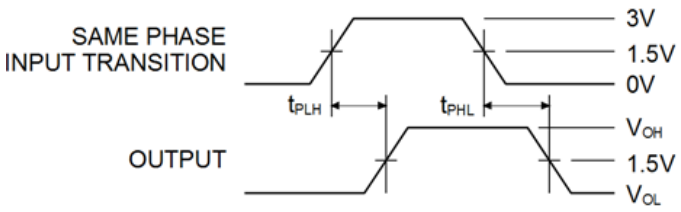


Test	Switch
Open Drain Disable Low Enable Low	Closed
All Other Tests	Open

ENABLE/DISABLE TIMES



PROPAGATION DELAY



PIN DESCRIPTION

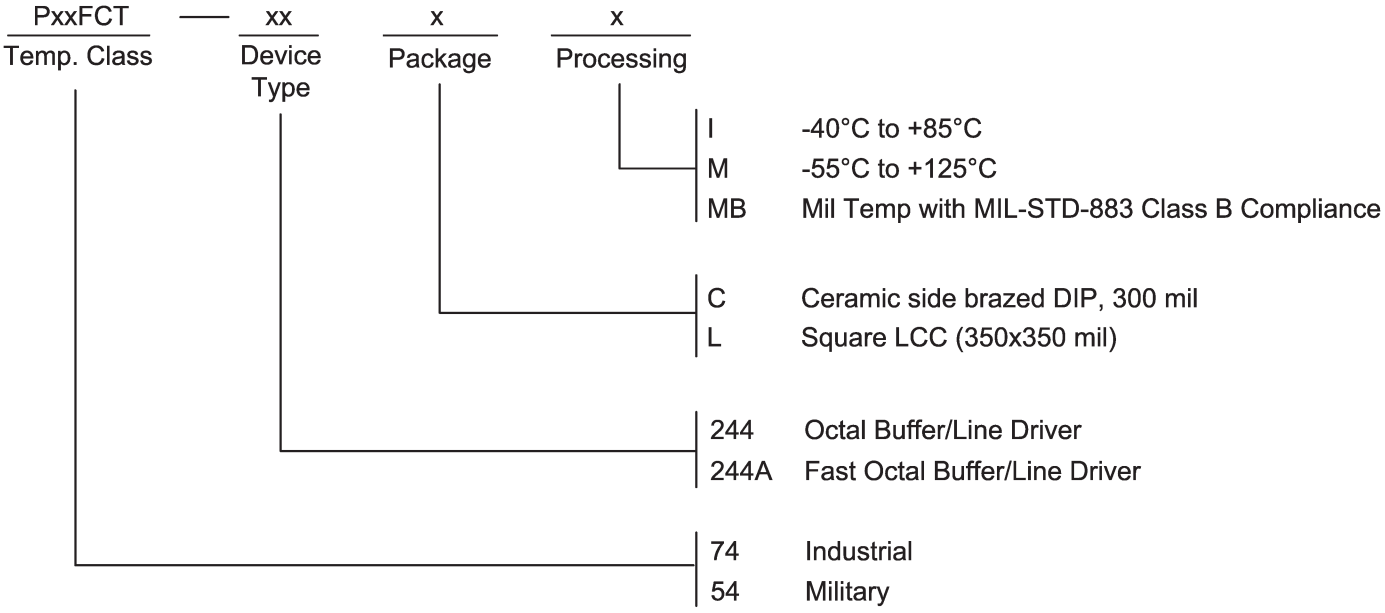
Pin Names	Description
$\overline{OE}_A$, $\overline{OE}_B$	3-State Output Enable Inputs (Active LOW)
Dxx	Inputs
Oxx	Outputs

FUNCTION TABLE

Inputs			Outputs
$\overline{OE}_A$	$\overline{OE}_B$	D	244
L	L	L	L
L	L	H	H
H	H	X	Z

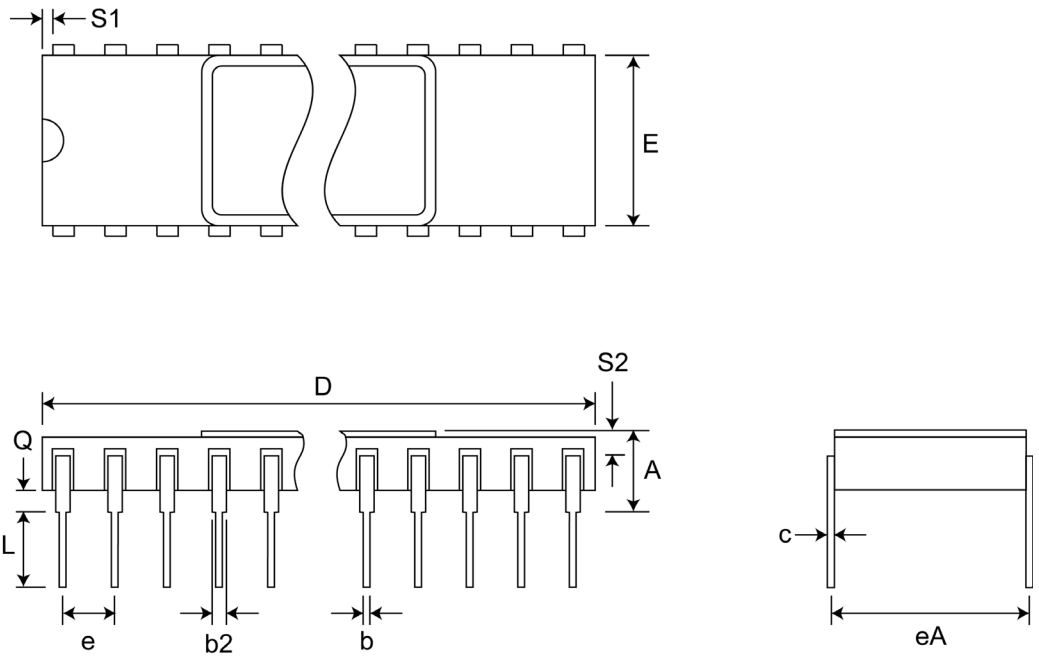
H = High Voltage Level
X = Don't Care
L = Low Voltage Level
Z = High Impedance

ORDERING INFORMATION



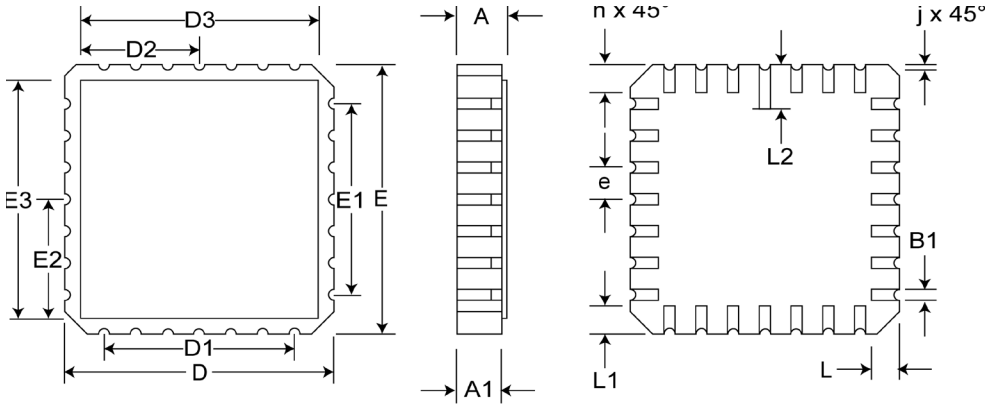
Pkg #	D2	
# Pins	20 (300 mil)	
Symbol	**Min**	**Max**
A	-	0.200
b	0.014	0.026
b2	0.045	0.065
C	0.008	0.018
D	-	1.060
E	0.220	0.310
eA	0.300 BSC	
e	0.100 BSC	
L	0.125	0.200
Q	0.015	0.070
S1	0.005	-
α	0°	15°

SIDEBRAZED DUAL INLINE PACKAGE



Pkg #	L2	
# Pins	20	
Symbol	**Min**	**Max**
A	0.060	0.075
A1	0.050	0.065
B1	0.022	0.028
D/E	0.342	0.358
D1/E1	0.200 BSC	
D2/E2	0.100 BSC	
D3/E3	-	0.358
e	0.050 BSC	
h	0.040 REF	
j	0.020 REF	
L	0.045	0.055
L1	0.045	0.055
L2	0.075	0.095
ND	5	
NE	5	

SQUARE LEADLESS CHIP CARRIER



REVISIONS

DOCUMENT NUMBER	LOGIC102
DOCUMENT TITLE	P54FCT244 - OCTAL BUFFER/LINE DRIVER WITH 3-STATE OUTPUTS

REV	ISSUE DATE	ORIGINATOR	DESCRIPTION OF CHANGE
OR	Sept 2015	JDB	New Data Sheet