

HCMOS 7.5x5mm 3.3V VCXO



Model: FVXO-HC73 SERIES RoHS Compliant / Pb Free

Rev. 9/6/2006 Preliminary

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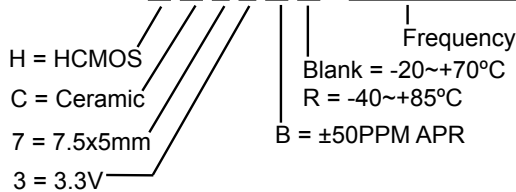
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Low Cost, Low Jitter, Fast Delivery

MODEL SELECTION GUIDE

F V X O - H C 7 3 b R - 155.520000



ELECTRICAL CHARACTERISTICS

PARAMETERS	MAX (unless otherwise noted)
Frequency Range (F _o)	1.000 ~ 250.000 MHz
Absolute Pull Range (APR) ¹	±50PPM Min
Storage Temperature Range (T _{STG})	-55°C ~ +125°C
Supply Voltage (V _{DD})	3.3V ± 5%
Control Voltage (V _C)	1.65V ± 1.5V
Input Current (I _{DD})	1.000 ~ 50.000 40mA 50.000+ ~ 250.000 60mA
Output Symmetry (50% V _{DD} Level)	45% ~ 55%
Rise Time (T _R) (10% ~ 90% V _{DD})	1.000 ~ 150.000 3nS
(20% ~ 80% V _{DD})	150.000+ ~ 250.000
Fall Time (T _F) (90% ~ 10% V _{DD})	1.000 ~ 150.000 3nS
(80% ~ 20% V _{DD})	150.000+ ~ 250.000
Output Voltage (V _{OL})	1.000 ~ 150.000 10% V _{DD} 150.000+ ~ 250.000 20% V _{DD}
(V _{OH})	1.000 ~ 150.000 90% V _{DD} MIN 150.000+ ~ 250.000 80% V _{DD} MIN
Output Load (HCMOS)	15pF
Start-up Time (T _s)	10mS
Output Enable / Disable Time ²	100nS
Linearity	±10%
Modulation Bandwidth	10kHz Min
Maximum Soldering Temp / Time	260°C / 10 Seconds
Moisture Sensitivity Level (MSL)	1
Termination Finish	Au

¹ Inclusive of 25°C tolerance, operating temperature range, input voltage change, load change, aging, shock, and vibration.

² An internal pullup resistor from pin 2 to pin 6 allows active output if pin 2 is left open.

³ A complementary output version with output 2 on pin 5 is available.

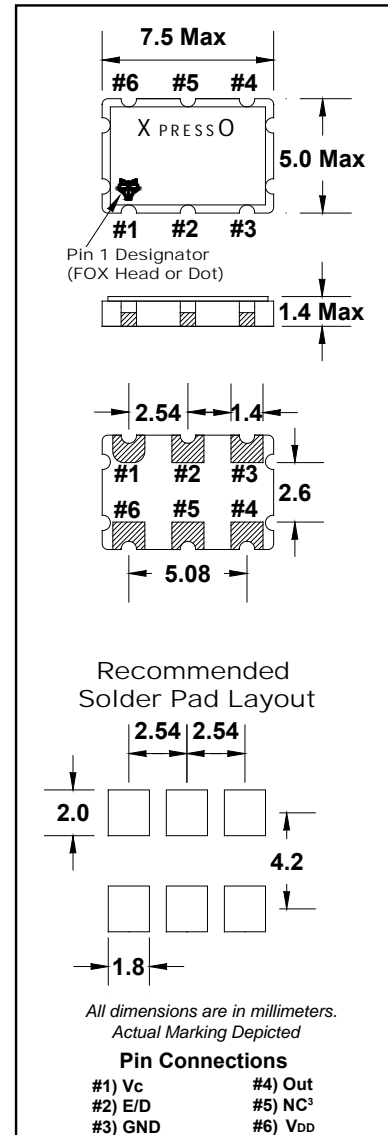
- If you have specific jitter or phase noise requirements please contact Fox Technical Support for availability.

- A 0.01µF bypass capacitor should be placed between V_{DD} (Pin 6) and GND (Pin 3) to minimize power supply line noise.

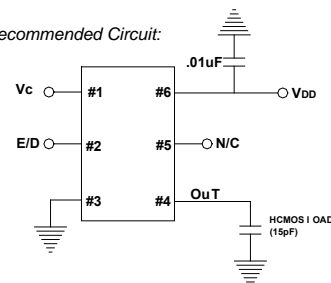
- The above specifications, having been carefully prepared and checked, is believed to be accurate at the time of publication; however, no responsibility is assumed by Fox Electronics for inaccuracies.

Patent Numbers:

US 6,664,860, US 5,960,403, US 5,952,890, US 5,960,405; US 6,188,290;
Foreign Patents: R.S.A. 98/0866, R.O.C. 120851, Singapore 67081, 67082; EP 0958652
China ZL 98802217.6, Malaysia MY-118540-A, Philippines 1-1998-000245
US and Foreign Patents Pending
XPRESSO® Fox Electronics



Recommended Circuit:



ENABLE / DISABLE FUNCTION²

(Pin 2)	OUTPUT (Pin 4)
OPEN	ACTIVE
'1' Level V _{IH} ≥ 70% V _{DD}	ACTIVE
'0' Level V _{IL} ≤ 30% V _{DD}	High Z

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• TAPE SPECIFICATIONS (millimeters)							
MODEL	A	b	C	D	E	F	STD Reel QTY
FVXO-HC73	∅1.5	4.0	8.0	7.5	16.0	2.15	2,000

• REEL SPECIFICATIONS (millimeters)							
MODEL	G	H	I	J	K	L	M
FVXO-HC73	2.0	∅13	∅21	∅80	∅255	17.5	2.0

