

**SERIES 602 5-TAPS 14 PIN FAST LOGIC DELAY MODULES**

- 14 Pin Chip Carrier • Designed For Surface Mounting
- TTL and DTL Compatible • 5 Equally Spaced Taps

**Specifications:**

- Supply Voltage : 4.75 to 5.25VDC
- Logic 1 Input Current : 100µA Max
- Logic 0 Input Current : -1.6mA Max
- Logic 1 Volt Out : 2.5V Min
- Logic 0 Volt Out : 0.5V Max
- Logic 1 Fan-Out : 25 Max
- Logic 0 Fan-Out : 12.5 Max
- Output Rise Time<sup>(2)</sup> : 2NSEC TYP
- Operating Temp Range : 0°C to 70°C
- Temperature Coefficient : 100 PPM/°C

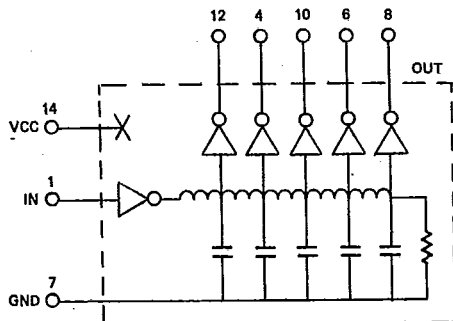
**Input Test Conditions:**

- Input Pulse Voltage : 3.2V
- Input Rise Time : ≤2NS
- Pulse Width : 40% of Total Delay
- Supply Current ICCL\* : 32mA
- ICCH\* : 7mA

Electrical Specifications at 25°C (Measured with no Loads on Taps)

Part Number	Total Delay Nanosecond (1)	Tap To Tap Delay NSEC (1)
602-4*	4±1	1±0.5
602-5*	5±1	1.2±0.5
602-6*	6±1	1.5±0.5
602-8*	8±2	2±0.5
602-10*	10±2	2.5±0.5
602-12*	12±2	3±0.5
602-16*	16±2	4±0.6
602-25	25±2	5±0.8
602-30	30±2	6±1
602-35	35±2	7±1
602-40	40±2	8±1
602-45	45±2.2	9±1
602-50	50±2.5	10±2
602-60	60±3	12±2
602-75	75±3.5	15±2
602-100	100±5.0	20±2
602-125	125±5.5	25±2
602-150	150±6.0	30±3
602-175	175±8.7	35±3.5
602-200	200±10	40±4
602-250	250±12.5	50±5

Note: (1) Measured at 1.5v level leading edge.  
 (2) Measured from 0.75v to 2.4v.  
 \*Time Delay measured with respect to 1st Tap.



SCHEMATIC

INCHES .XXX ± .010  
 MILLIMETERS .XX ± .25

