

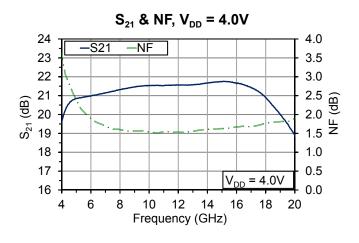
# 6-18GHz, 21dB Gain, 1.5dB NF Low Noise Amplifier

#### **Features**

- 16dBm P<sub>SAT</sub> with 1.5dB NF and 21.5dB gain typical from 6-18GHz
- Gain flatness < +/-0.5dB</li>
- Input and Output matched to 50Ω
- Self biased for simple biasing, small solution size and ease of manufacture
- +24dBm maximum input power rating
- 1.1mm x 1.36mm x 0.1mm die size

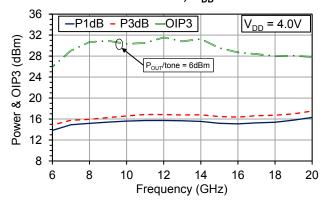
#### **Applications**

- Instrumentation
- Electronic warfare
- · Microwave communications









Typical Performance (CW, Typical Device, RF Probe):  $T_A$ =25°C,  $V_{D1,2}$  = 4V

Parameter	Min	Тур	Max	Units
Frequency	6	-	18	GHz
Small Signal Gain	21.0	-	21.7	dB
Noise Figure	1.5	1.6	1.8	dB
Output Power, P <sub>1dB</sub>	14.0	15.0	15	dBm
Output Power P <sub>SAT</sub>	15.0	16.0	17	dBm
Output IP3	26	29	31	dBm
Drain Current		105		mA



**Table 1: Absolute Maximum Ratings, Not Simultaneous** 

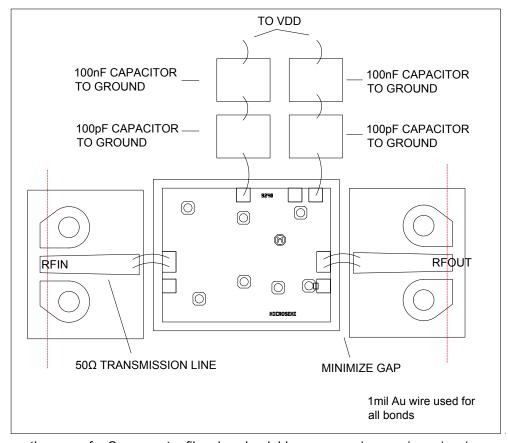
Parameter	Rating	Units
Drain Voltage (V <sub>D</sub> )	+4.5	V
Input Power (P <sub>IN</sub> )	24	dBm
Channel Temperature (T <sub>C</sub> )	150 <sup>1</sup>	°C
Operating Ambient Temperature (T <sub>A</sub> )	-55 to +85	°C
Storage Temperature	-65 to +150	°C
Thermal Resistance, Channel to Die Backside	TBD (140 est)	°C/W



Table 2: Specifications (CW, 100% Test):  $T_A = 25$ °C,  $V_{DD} = 4V$ ,  $I_{DD} = 100$ mA

Parameter	Frequency	Min	Тур	Max	Units
Small Signal Gain	18GHz	18.0	21.0	-	dB
Output Power, P <sub>1dB</sub>	18GHz	1	1.8	2.3	dBm

# RF Probe Measurement Set-Up With Reference Planes<sup>2</sup>

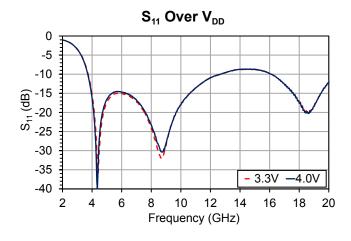


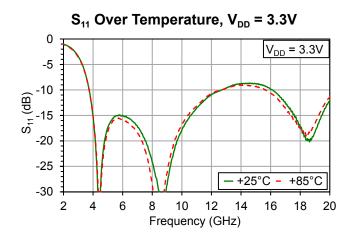
<sup>&</sup>lt;sup>2</sup> Reference planes are the same for S-parameter files downloadable on www.microsemi.com/mmics

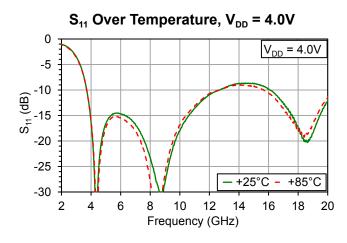
 $<sup>^{1}</sup>$  MTTF @  $T_{C}$  = 150°C > 10 $^{7}$  hours

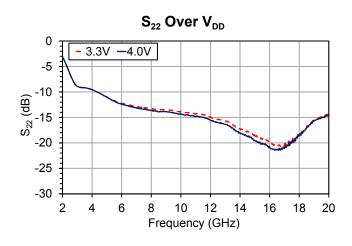


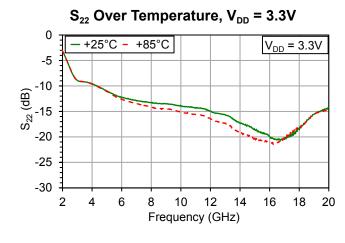
 $V_{DD} = 4V$ ,  $I_{DD} = 105$ ,  $T_A = 25$ °C unless otherwise noted

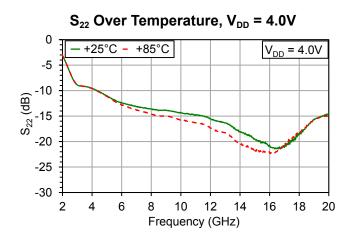






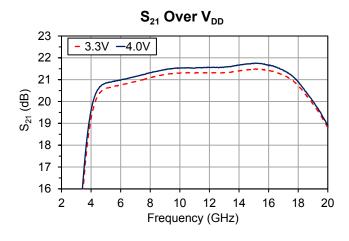


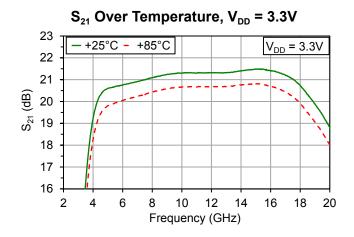


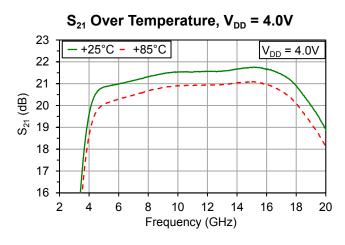


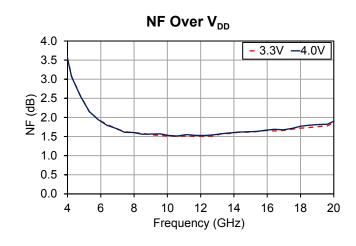


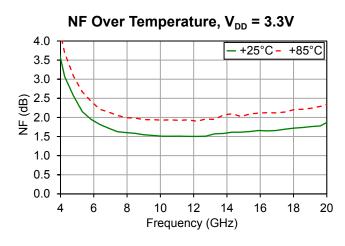
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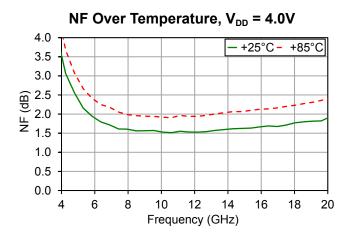






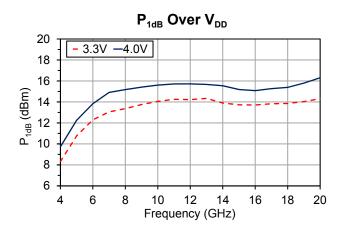


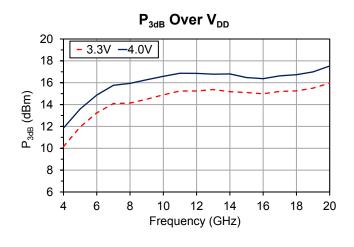


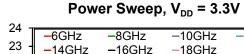


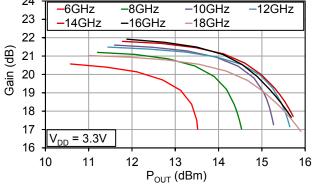


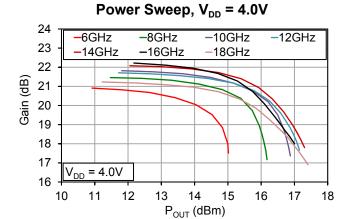
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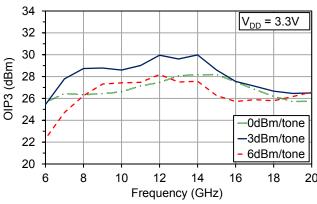


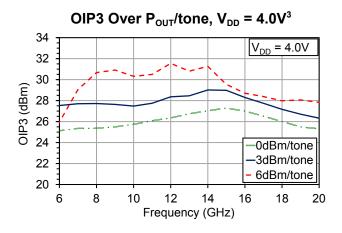






## OIP3 Over $P_{OUT}/tone$ , $V_{DD} = 3.3V^3$

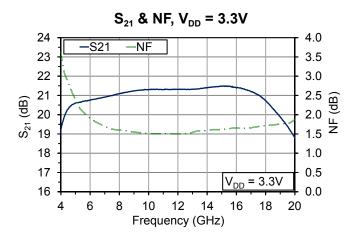


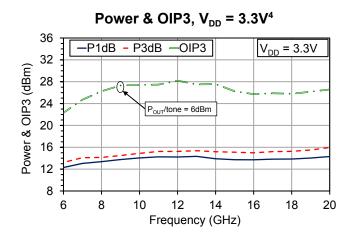


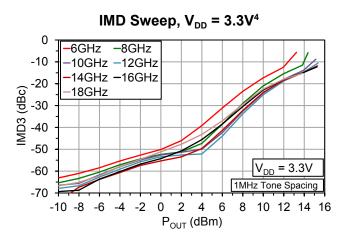
 $<sup>^{3}</sup>$  OIP3 over  $P_{OUT}$ /tone can be adjusted using  $V_{D1}$  and  $V_{D2}$ 

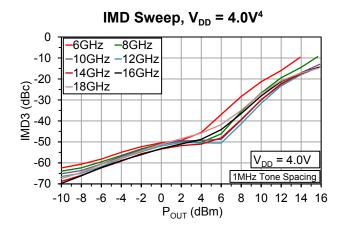


 $V_{DD}$  = 4V,  $I_{DD}$  = 105,  $T_A$  = 25°C unless otherwise noted







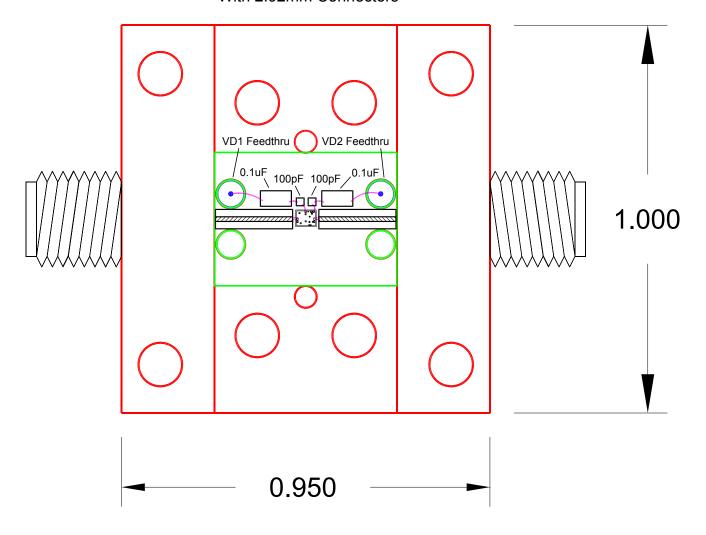


 $<sup>^4</sup>$  OIP3 over  $P_{OUT}$ /tone can be adjusted using  $V_{D1}$  and  $V_{D2}$ 



## **Connectorized Test Fixture**

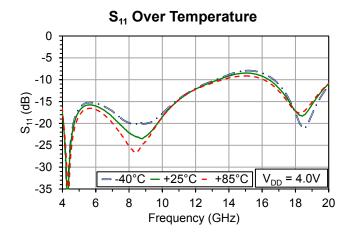
With 2.92mm Connectors

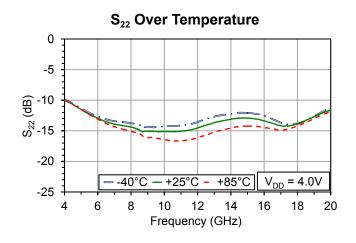


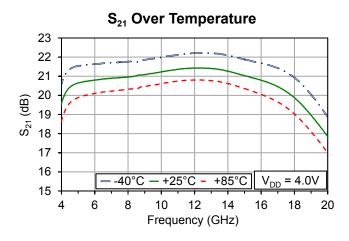


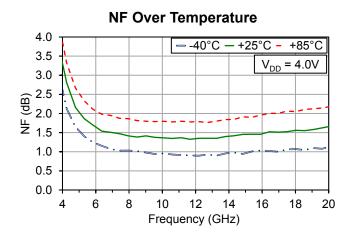
# Typical Performance, Connectorized Test Fixture

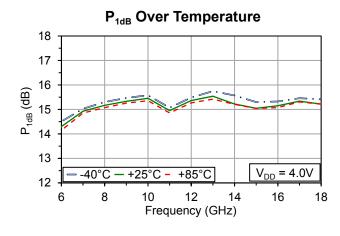
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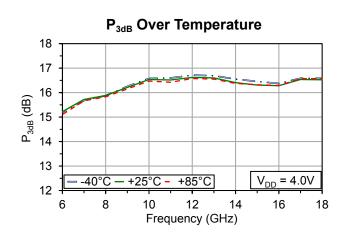










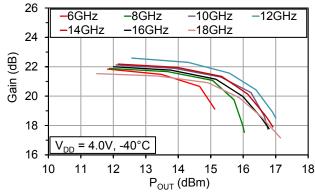




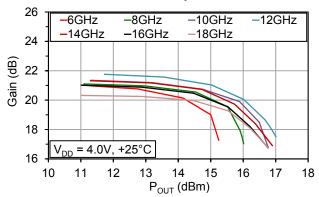
## Typical Performance, Connectorized Test Fixture

 $V_{DD}$  = 4V,  $I_{DD}$  = 105,  $T_A$ =25°C unless otherwise noted

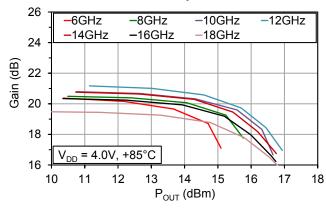




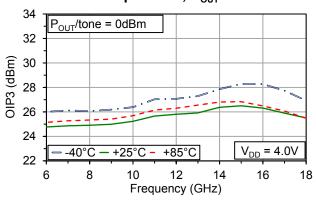
#### Power Sweep, +25°C



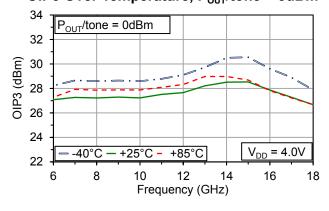
#### Power Sweep, +85°C



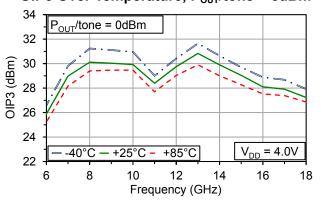
OIP3 Over Temperature, P<sub>OUT</sub>/tone = 0dBm<sup>5</sup>



### OIP3 Over Temperature, P<sub>OUT</sub>/tone = 3dBm<sup>5</sup>



OIP3 Over Temperature, P<sub>OUT</sub>/tone = 6dBm<sup>5</sup>

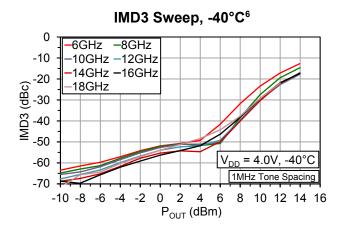


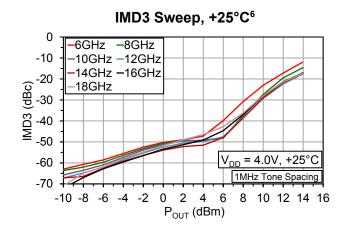
 $<sup>^{5}</sup>$  OIP3 over  $P_{OUT}$ /tone can be adjusted using  $V_{D1}$  and  $V_{D2}$ 

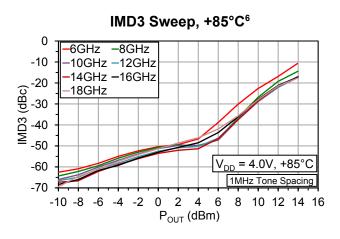


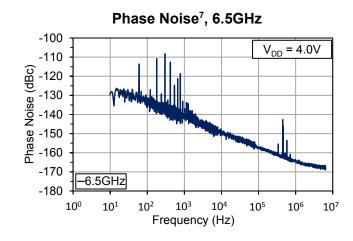
## Typical Performance, Connectorized Test Fixture

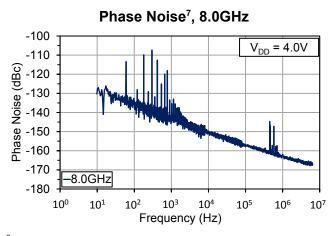
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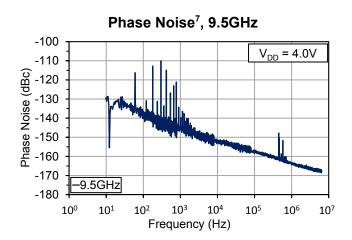








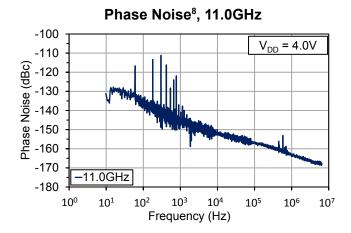


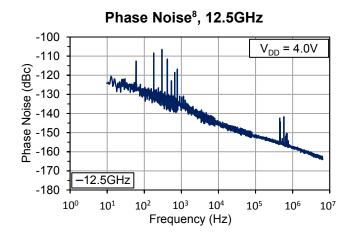


 $<sup>^6</sup>$  OIP3 over P $_{\rm OUT}$ /tone can be adjusted using V $_{\rm D1}$  and V $_{\rm D2}$   $^7$  Visit www.microsemi.com/mmics for application note on phase noise measurement at Microsemi



# Typical Performance, Connectorized Test Fixture $V_{DD}$ = 4V, $I_{DD}$ = 105, $T_A$ =25°C unless otherwise noted



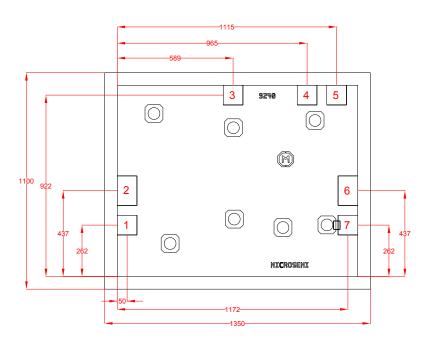


<sup>&</sup>lt;sup>8</sup> Visit www.microsemi.com/mmics for application note on phase noise measurement at Microsemi



#### Chip layout showing pad locations.

All dimensions are in microns. Die thickness is 100 microns. Backside metal is gold, bond pad metal is gold. Refer to Die Handling Application Note MM-APP-0001 (visit www.microsemi.com/mmics).



**Table 3: Pad Descriptions** 

Pad #	Description	Pad Dimensions (µm)	
1, 4, 7	Ground	100 x 100	
2	RF <sub>IN</sub> , pad is AC coupled	100 x 150	
3	$V_{D1}$	100 x 100	
5	$V_{\scriptscriptstyle D2}$	100 x 100	
6	RF <sub>out</sub> , pad is AC coupled	100 x 150	

#### **Biasing**

MMA004AA is a self-biased device with positive supply. Apply  $V_{DD}$  to pad 3 and 5.  $V_{D1}$  and  $V_{D2}$  should be RF isolated from each other. Bias sequence does not matter.





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