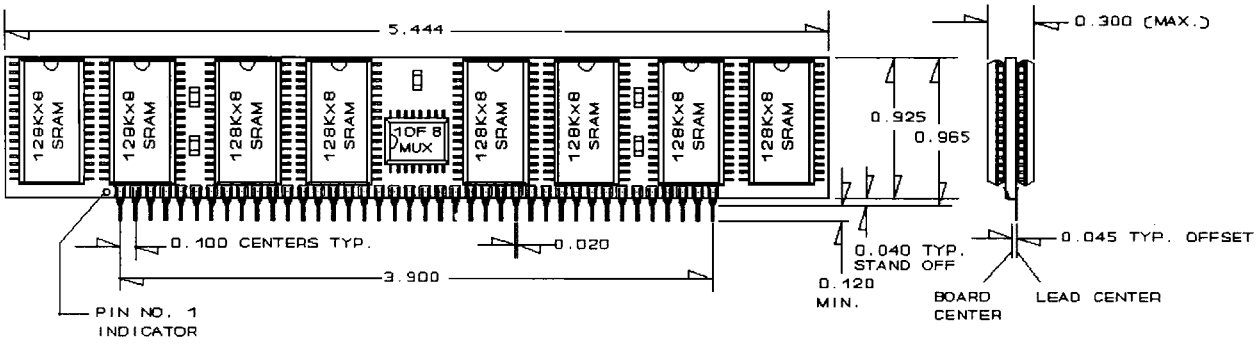
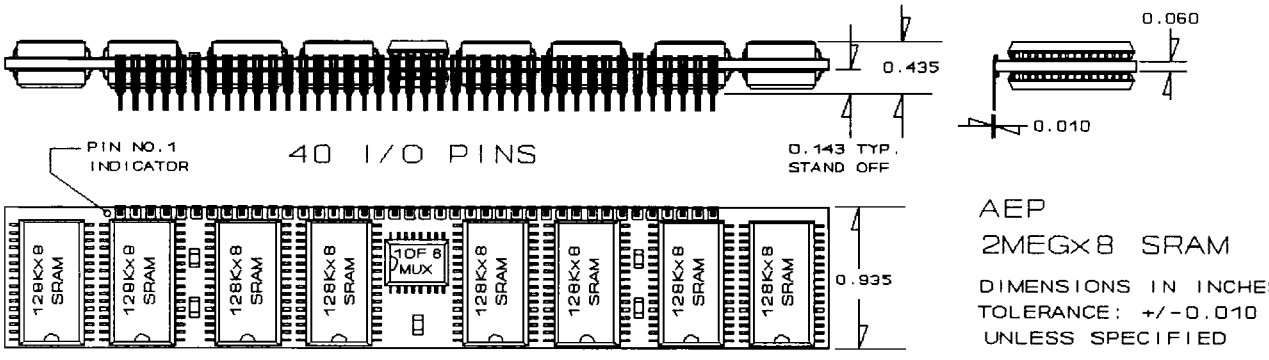
2 MEGWORD BY 8 BIT
HIGH SPEED SRAM MODULE
WITH ON BOARD DECODERS

SPECIFICATION DRAWING



FRONT AND SIDE VIEWS
(SHOWING VERTICAL MOUNTING LEADS OPTION)



BOTTOM, REAR, AND SIDE VIEWS
(SHOWING HORIZONTAL MOUNTING LEADS OPTION)

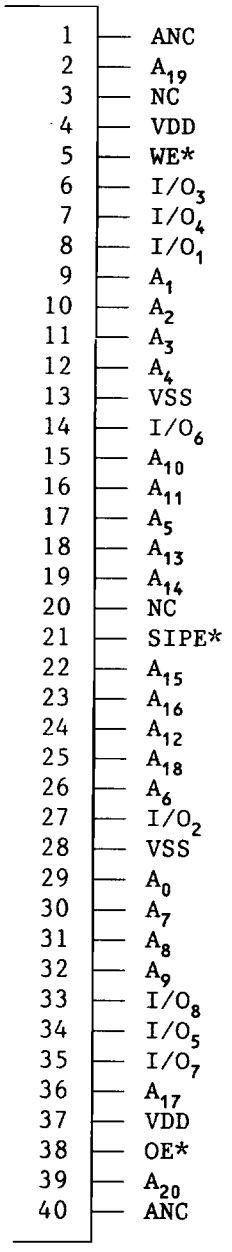
AEP
2MEGx8 SRAM

DIMENSIONS IN INCHES
TOLERANCE: +/-0.010
UNLESS SPECIFIED

PRELIMINARY

2M x 8 STATIC RAM MODULE

SIP PIN-OUT CONFIGURATION



A0 - A20	ADDRESS INPUTS
I/O1 - I/O8	DATA LINES
WE*	WRITE ENABLE
SIPE*	SIP ENABLE
OE*	OUTPUT ENABLE
VDD	POWER +5V
VSS	GROUND
NC	NO CONNECT
ANC	NO CONNECT (reserved for next generation address lines)

* ACTIVE WHEN LOW