

PCMCIA/JEIDA WIDE TEMPERATURE MASK ROM

UNDER
DEVELOPMENT

1. VARIATION

Memory Size	Description
256K BYTE	128K × 16 bit WIDE TEMP. MASK ROM CARD
512K BYTE	256K × 16 bit WIDE TEMP. MASK ROM CARD
1M BYTE	512K × 16 bit WIDE TEMP. MASK ROM CARD
2M BYTE	1M × 16 bit WIDE TEMP. MASK ROM CARD
4M BYTE	2M × 16 bit WIDE TEMP. MASK ROM CARD
8M BYTE	4M × 16 bit WIDE TEMP. MASK ROM CARD
16M BYTE	8M × 16 bit WIDE TEMP. MASK ROM CARD

2. OUTLINE OF FUNCTIONS AND FEATURES

- (1) This memory card conforms to PCMCIA/JEIDA.
- (2) Wide temperature specification
Operating temperature range: $T_{OPR} = -20 \sim 70^{\circ}\text{C}$
Storage temperature range: $T_{STR} = -30 \sim 85^{\circ}\text{C}$
- (3) Size of the card: $85.6 \times 54/0 \times 3.3 \text{ mm}$
- (4) Includes exclusive IC's for the control of I/O.
- (5) Supports 2 types of exclusive attribute memory.
 - (5-1) With 2K Bytes EEPROM which can be read/written
 - (5-2) Without exclusive attribute memory (out "FFh", read only)
- (6) Access time: 250 ns

(7) Maximum rating

Symbol	Description	Maximum Rating	Unit
V _{CC}	V _{CC} SUPPLY VOLTAGE	-0.3 ~ 7.0	V
V _{IN}	INPUT SIGNAL VOLTAGE (*1)	-0.3 ~ V _{CC} +0.5	V
V _{OUT}	OUTPUT SIGNAL VOLTAGE	-0.3 ~ V _{CC}	V
T _{OPR}	OPERATION TEMPERATURE	-20 ~ 70	°C
T _{STR}	STRAGE TEMPERATURE	-30 ~ 85	°C
HUM	HUMIDITY (*2)	10 ~ 95	%
PD	POWER DISSIPATION	1	W

*1: V_{IN} : Under 7.0 V

*2: No dew condition