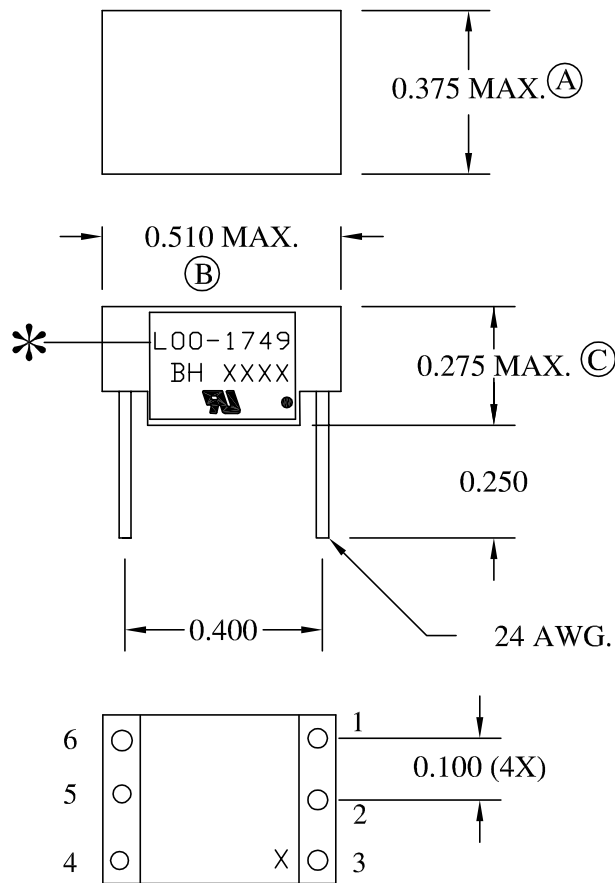
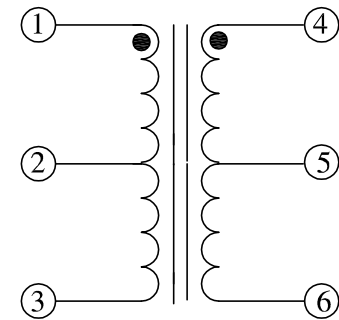


MECHANICAL



SCHEMATIC



ELECTRICAL SPECIFICATIONS

URNS RATIO: 1CT:1CT

OPEN CIRCUIT INDUCTANCE: 2mH MIN. (10 KHz 100mV)

PRIMARY LEAKAGE INDUCTANCE: 25uH MAX. (1-3 SHORT 4-6)


INTERWINDING CAPACITANCE: 10pF MAX. (1-3 TO 4-6)

DC RESISTANCE: (1-3) 0.6 OHMS NOM. (4-6) 0.6 OHMS NOM.

HIGH VOLTAGE TEST: 4000 VRMS / 1 SEC.

NOTE: "X" ON BOTTOM OF CASE INDICATES PIN 3.

* BRAND FOR LOCATION: L00-1749
L00-1749-A
L00-1749-C

				DESIGN PER: BH CATALOG	 BH ELECTRONICS, INC. BURNSVILLE & MARSHALL, MN © 1998 BH Electronics
				<input type="checkbox"/> CUSTOM NON-PROPRIETARY <input checked="" type="checkbox"/>	
				DRAWN: K. LOFTUS	TITLE: TRANSFORMER
				ENGR: J. DECRAMER	
B	3-28-11	ECR #8917 ADD UL TO LABEL	JDC	UNLESS OTHERWISE SPECIFIED: DIMENSIONS IN INCHES, TOLERANCES NON-CUMULATIVE .XX±.01 .XXX±.005 PAGE 1 OF 1	PART NO. L00-1749
A	11-1-05	CUSTOMER PRINT	JDC		
ISSUE	DATE	REVISION	BY		