

54AC/74AC151 • 54ACT/74ACT151

1-of-8 Decoder/Demultiplexer

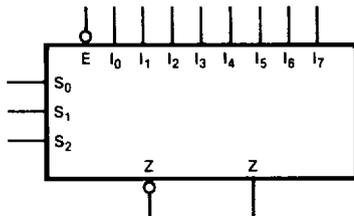
Description

The 'AC/'ACT151 is a high-speed 8-input digital multiplexer. It provides, in one package, the ability to select one line of data from up to eight sources. The 'AC/'ACT151 can be used as a universal function generator to generate any logic function of four variables. Both true and complementary outputs are provided.

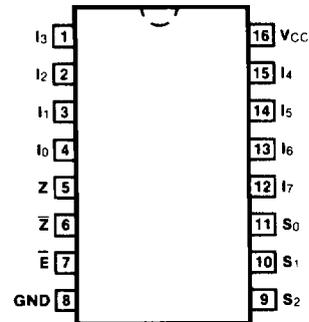
- Outputs Source/Sink 24 mA
- 'ACT151 has TTL-Compatible Inputs

Ordering Code: See Section 6

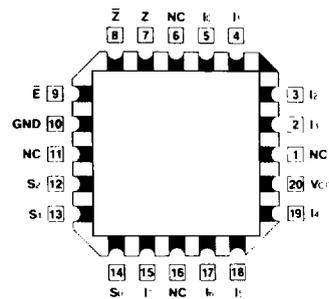
Logic Symbol



Connection Diagrams



Pin Assignment for DIP, Flatpak and SOIC



Pin Assignment for LCC

Truth Table

Inputs				Outputs	
E	S ₂	S ₁	S ₀	Z	Z
H	X	X	X	H	L
L	L	L	L	I ₀	I ₀
L	L	L	H	I ₁	I ₁
L	L	H	L	I ₂	I ₂
L	L	H	H	I ₃	I ₃
L	H	L	L	I ₄	I ₄
L	H	L	H	I ₅	I ₅
L	H	H	L	I ₆	I ₆
L	H	H	H	I ₇	I ₇

H = HIGH Voltage Level
 L = LOW Voltage Level
 X = Immaterial

Pin Names

- I₀ - I₇ Data Inputs
- S₀ - S₂ Select Inputs
- E Enable Input
- Z Data Output
- Z̄ Inverted Data Output

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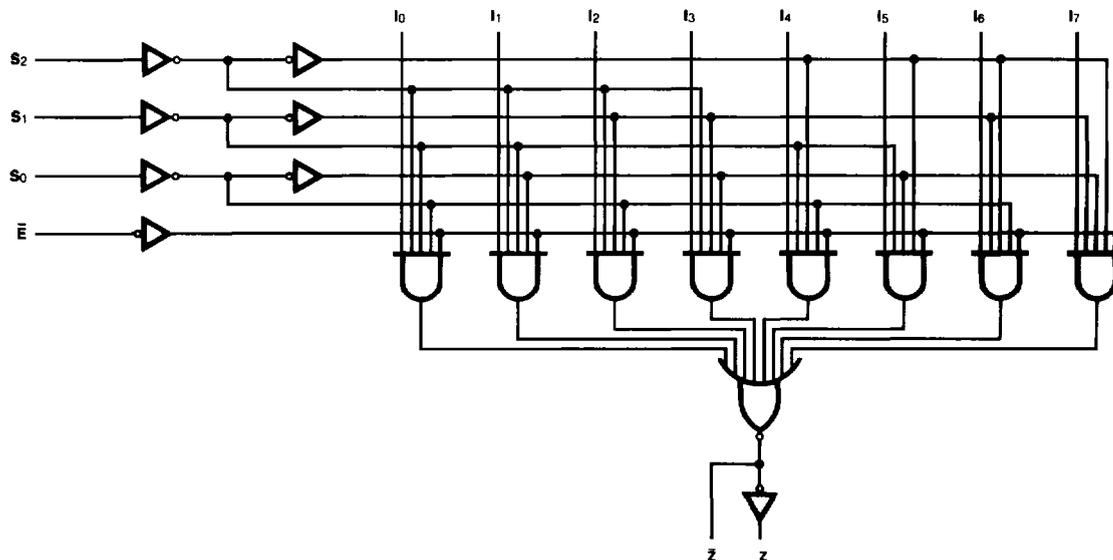
Functional Description

The 'AC/ACT151 is a logic implementation of a single pole, 8-position switch with the switch position controlled by the state of three Select inputs, S₀, S₁, S₂. Both true and complementary outputs are provided. The Enable input (\bar{E}) is active LOW. When it is not activated, the complementary output is HIGH and the true output is LOW regardless of all other inputs. The logic function provided at the output is:

$$Z = \bar{E} \cdot (I_0 \cdot \bar{S}_0 \cdot \bar{S}_1 \cdot \bar{S}_2 + I_1 \cdot S_0 \cdot \bar{S}_1 \cdot \bar{S}_2 + I_2 \cdot \bar{S}_0 \cdot S_1 \cdot \bar{S}_2 + I_3 \cdot S_0 \cdot S_1 \cdot \bar{S}_2 + I_4 \cdot \bar{S}_0 \cdot \bar{S}_1 \cdot S_2 + I_5 \cdot S_0 \cdot \bar{S}_1 \cdot S_2 + I_6 \cdot \bar{S}_0 \cdot S_1 \cdot S_2 + I_7 \cdot S_0 \cdot S_1 \cdot S_2)$$

The 'AC/ACT151 provides the ability, in one package, to select from eight sources of data or control information. By proper manipulation of the inputs, the 'AC/ACT151 can provide any logic function of four variables and its complement.

Logic Diagram



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

DC Characteristics (unless otherwise specified)

Symbol	Parameter	54AC/ACT	74AC/ACT	Units	Conditions
I _{CC}	Maximum Quiescent Supply Current	160	80	μA	V _{IN} = V _{CC} or Ground, V _{CC} = 5.5 V, T _A = Worst Case
I _{CC}	Maximum Quiescent Supply Current	8.0	8.0	μA	V _{IN} = V _{CC} or Ground, V _{CC} = 5.5 V, T _A = 25°C
I _{CC(T)}	Maximum Additional I _{CC} /Input ('ACT151)	1.6	1.5	mA	V _{IN} = V _{CC} - 2.1 V V _{CC} = 5.5 V, T _A = Worst Case

AC Characteristics

Symbol	Parameter	V _{CC} * (V)	74AC			54AC		74AC		Units	Fig. No.
			T _A = +25°C C _L = 50 pF			T _A = -55°C to +125°C C _L = 50 pF		T _A = -40°C to +85°C C _L = 50 pF			
			Min	Typ	Max	Min	Max	Min	Max		
t _{PLH}	Propagation Delay S _n to Z or \bar{Z}	3.3 5.0	1.0 1.0	11.5 8.5	18.0 13.0	1.0 1.0	22.0 15.5	1.0 1.0	20.0 15.0	ns	3-6
t _{PHL}	Propagation Delay S _n to Z or \bar{Z}	3.3 5.0	1.0 1.0	12.0 8.5	18.0 13.0	1.0 1.0	22.0 15.5	1.0 1.0	20.0 15.0	ns	3-6
t _{PLH}	Propagation Delay E to Z or \bar{Z}	3.3 5.0	1.0 1.0	8.0 6.0	13.0 10.0	1.0 1.0	15.5 12.0	1.0 1.0	14.0 11.0	ns	3-6
t _{PHL}	Propagation Delay E to Z or \bar{Z}	3.3 5.0	1.0 1.0	8.5 6.5	13.0 10.0	1.0 1.0	15.5 12.0	1.0 1.0	14.0 11.0	ns	3-6
t _{PLH}	Propagation Delay I _n to Z or \bar{Z}	3.3 5.0	1.0 1.0	9.5 7.0	14.0 10.5	1.0 1.0	16.0 12.0	1.0 1.0	15.5 11.0	ns	3-5
t _{PHL}	Propagation Delay I _n to Z or \bar{Z}	3.3 5.0	1.0 1.0	9.5 7.0	15.0 11.0	1.0 1.0	18.0 13.0	1.0 1.0	16.0 12.0	ns	3-5

*Voltage Range 3.3 is 3.3 V ± 0.3 V
Voltage Range 5.0 is 5.0 V ± 0.5 V

Military parameters given herein are for general references only. For current military specifications and subgroup testing information please request Fairchild's Table I data sheet from your Fairchild sales engineer or account representative.

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AC Characteristics

Symbol	Parameter	V _{CC} * (V)	74ACT			54ACT		74ACT		Units	Fig. No.
			T _A = +25°C C _L = 50 pF			T _A = -55°C to +125°C C _L = 50 pF		T _A = -40°C to +85°C C _L = 50 pF			
			Min	Typ	Max	Min	Max	Min	Max		
t _{PLH}	Propagation Delay S _n to Z	5.0	1.0	12.5	15.5			1.0	17.0	ns	3-6
t _{PHL}	Propagation Delay S _n to Z	5.0	1.0	12.5	15.5			1.0	16.5	ns	3-6
t _{PLH}	Propagation Delay S _n to Z̄	5.0	1.0	12.5	15.0			1.0	16.5	ns	3-6
t _{PHL}	Propagation Delay S _n to Z̄	5.0	1.0	12.5	16.5			1.0	18.5	ns	3-6
t _{PLH}	Propagation Delay E to Z	5.0	1.0	10.0	9.5			1.0	10.0	ns	3-6
t _{PHL}	Propagation Delay E to Z	5.0	1.0	10.5	9.0			1.0	10.0	ns	3-6
t _{PLH}	Propagation Delay E to Z̄	5.0	1.0	10.0	8.5			1.0	9.5	ns	3-6
t _{PHL}	Propagation Delay E to Z̄	5.0	1.0	10.5	10.0			1.0	10.5	ns	3-6
t _{PLH}	Propagation Delay I _n to Z	5.0	1.0	11.0	11.5			1.0	12.5	ns	3-6
t _{PHL}	Propagation Delay I _n to Z	5.0	1.0	11.0	12.0			1.0	13.5	ns	3-6
t _{PLH}	Propagation Delay I _n to Z̄	5.0	1.0	11.0	12.0			1.0	13.0	ns	3-6
t _{PHL}	Propagation Delay I _n to Z̄	5.0	1.0	11.0	12.5			1.0	14.0	ns	3-6

*Voltage Range 5.0 is 5.0 V ± 0.5 V

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Capacitance

Symbol	Parameter	54/74AC/ACT	Units	Conditions
		Typ		
C _{IN}	Input Capacitance	4.5	pF	V _{CC} = 5.5 V
C _{PD}	Power Dissipation Capacitance	70.0	pF	V _{CC} = 5.5 V