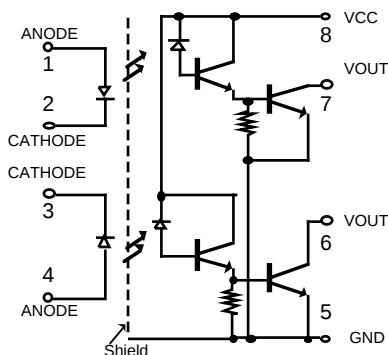




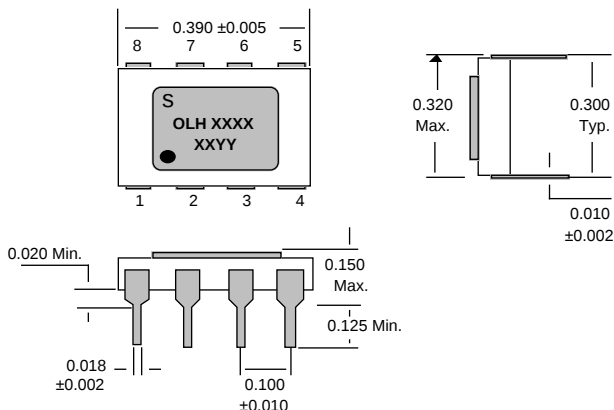
ISO LINK

OLH 5730/5731

Hermetic **LOW INPUT CURRENT**
DUAL CHANNEL OPTOCOUPLER



SCHEMATIC



PACKAGE OUTLINE

Features

- ◆ Rugged, reliable hermetic DIP package
- ◆ Performance guaranteed over full military temperature range
- ◆ High isolation voltage,
3000 Vdc
- ◆ Low input current, 0.5 mA
- ◆ Low power consumption
- ◆ High common mode rejection
- ◆ Radiation tolerant design
- ◆ High density dual-channel package

Description

The OLH5730 and 5731 are dual-channel hermetic 8-pin DIP optocouplers for low input current applications. The OLH5731 product is a 100% hi-rel screened version of the OLH5730.

Each channel consists of an AlGaAs LED optically coupled to an integrated photodiode-split darlington detector. The AlGaAs LED provides superior low current performance. The split darlington open collector output results in high gain and low saturation voltage.

These products are functionally compatible to HCPL2730/2731, and 6N140A optocouplers.

The performance of these products under radiation environment is much better than standard phototransistors. Test data is presented in Application Note 1003.

Special low input current and CTR selections are available upon request.

NOTES:

1. Measured between pins 1, 2, 3, 4 shorted together and pins 5, 6, 7, 8 shorted together.
2. Current Transfer Ratio is defined as the ratio of output collector current, I_c , to the forward LED current I_f , times 100%.
3. Derate I_f at 0.33 mA / °C above 110°C.
4. Output power is collector output power plus total supply power. Derate at 1.66 mW / °C above 110°C

Absolute Maximum Ratings

Coupled		
Input to Output Isolation Voltage ¹		± 3000 Vdc
Storage Temperature Range		-65°C to +150°C
Operation Temperature Range		-55°C to +125°C
Lead Solder Temperature (1.6mm below seating plane)		260°C for 10s
Input Diode		
Average Input Current		10 mA ³
Peak Forward Current (≤ 1mS duration)		20 mA
Reverse Voltage		5.0 V
Output Detector		
Average Output Current		40 mA
Supply Voltage, V _{CC}		-0.5 V to 18 V
Output Voltage, V _{out}		-0.5 V to 18 V
Power Dissipation		50 mW ⁴

ELECTRICAL CHARACTERISTIC (T_A = - 55 °C to +125 °C, Unless Otherwise Specified)

Parameter	Symbol	Min	Typ.	Max	Units	Test Conditions	Fig.	Note
Current Transfer Ratio	CTR	300 300 200			% % %	I _F =0.5 mA, V _O =0.4v, V _{CC} =4.5v I _F =1.6 mA, V _O =0.4v, V _{CC} =4.5v I _F =5.0 mA, V _O =0.4v, V _{CC} =4.5v	2	2
Logic Low Output Voltage	V _{OL}		.1 .1 .2	.4 .4 .4	V V V	I _F =0.5 mA, I _{OL} =1.5mA, V _{CC} =4.5v I _F =1.6 mA, I _{OL} =4.8mA, V _{CC} =4.5v I _F =5mA, I _{OL} =10mA, V _{CC} =4.5v		
Logic High Output Current	I _{OH} /I _{OHX}		.005	250	μA	I _F =2 μA, I _F =10mA (other channel), V _O =V _{CC} =18V		
Logic Low Supply Current	I _{CCL}		1.0	2.0	mA	I _F =1.6mA, V _{CC} =18v		
Logic High Supply Current	I _{CCH}		.01	40	μA	I _F =0mA, V _{CC} =18v		
Input Forward Voltage	V _F	1.0	1.65	2.0	V	I _F =1.6mA	1	
Input Reverse Breakdown Voltage	B _{VR}	3			V	I _R =10 μA		
Input to Output Leakage Current	I _{I-O}			1.0	μA	Relative Humidity ≤ 50%, T _A =25°C, V _{I-O} = 3000Vdc, t=1s		1
Propagation Delay Time Logic High to Low	t _{PHL}		26 5 2	100 30 10	μS μS μS	I _F =0.5mA, R _L =4.7 KΩ, I _F =1.6mA, R _L =2.2 KΩ, I _F =5mA, R _L =680 Ω	3,4,5	
Propagation Delay Time Logic Low to High	t _{PLH}		28 15 10	60 50 30	μS μS μS	I _F =0.5mA, R _L =4.7 KΩ, I _F =1.6mA, R _L =2.2 KΩ, I _F =5mA, R _L =680 Ω		
						V _{CC} = 5v, T _A = 25°C		
Common Mode Transient Immunity (each channel)	C _{MH} C _{ML}	5 5	≥10 ≥10		KV/μS KV/μS	I _F =0mA I _F =1.6mA V _{CC} = 5V, T _A = 25°C R _L =1.5KΩ V _{CM} = 300 V _{P-P}		

ALL TYPICAL @ T_A = 25°C

TYPICAL PERFORMANCE CURVES

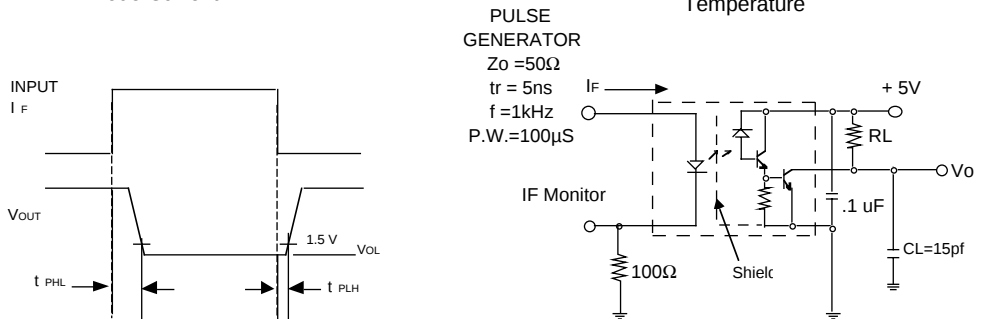
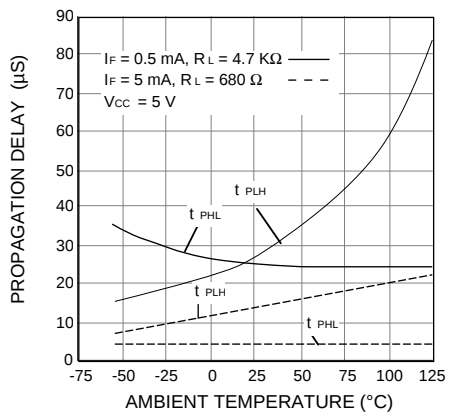
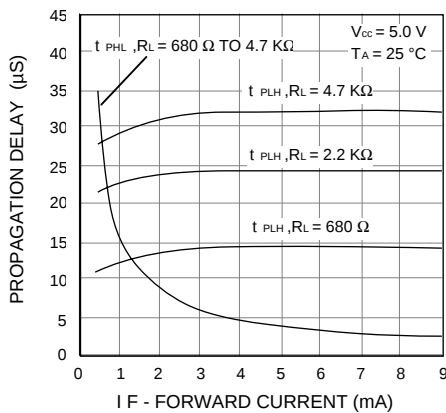
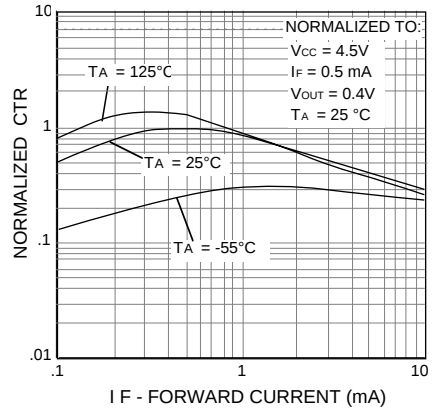
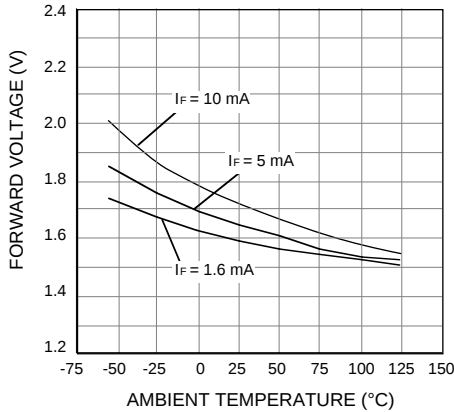


Fig. 5 - Switching Test Circuit