

4. CMOS Analog Clock

Device	Feature	V _{DD} (V)	I _{DD} (μ A)		OSC. (Hz)	Package	Remark
			Typ	Max			
KS5223	M: 1000 E,Q: 46.9ms, F,R:31.2ms N: 23.4ms	1.5	1.0	2.0	32,768	8 DIP CHIP	Multi Option type
† KS5224	M: 62.5ms	1.5	1.0	2.0	32,768	8 DIP CHIP	Multi Option type
†† KS5225	Stepping Motor or Rolling Motor Control Bonding Option	1.5	1.0	2.0	32,768	CHIP	<ul style="list-style-type: none"> • Snooze Time 256sec, 512sec Switch Option • Change Alarm or Normal Alarm Control Switch Option
†† KS5127	Automotive Digital Clock	Max 7	-	0.7mA	4,194304 MHz	40 DIP CHIP	<ul style="list-style-type: none"> • 1024Hz External Clock Drive • 1024Hz Signal Output • 4 Ways Display Brightness Control

1

5. Analog Melody + Clock (ONE CHIP)

Device	Standard Type	Song List	Feature	V _{DD} (V)	I _{DD} (μ A)		OSC Hz	Package	Remark
					Typ	Melody Operating			
KS5213	KS5213A	Minute (Bach)	<ul style="list-style-type: none"> •Selectable output pulse width by binary option (15.6 msec, 23.4 msec, 31.2 msec, 46.8 msec) •Output pulse periode: 2 sec (0.5 Hz) •Snooze function: 256 sec •Melody or ALARM selection 	1.5	1.0	500 (Typ)	32,768	Chip 16 DIP	Alarm sound: 886 x 8 x 0.5Hz Dual tone melody Digital ENV built in