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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Quad. 2-input Exclusive-NOR Gates

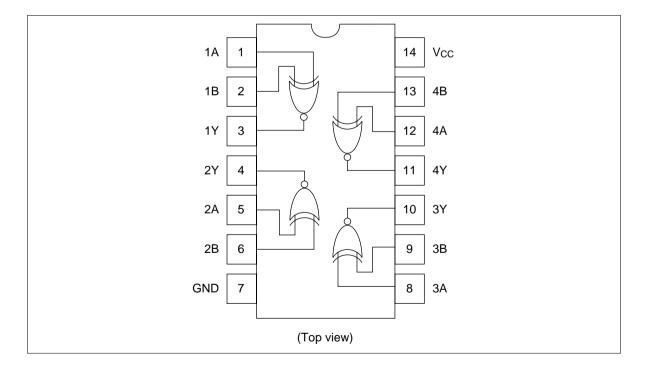
RENESAS

ADE-205-481 (Z) 1st. Edition Sep. 2000

Features

- High Speed Operation: $t_{pd} = 12.5$ ns typ ($C_L = 50$ pF)
- High Output Current: Fanout of 10 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$
- Low Input Current: 1 µA max
- Low Quiescent Supply Current: I_{CC} (static) = 1 μ A max (Ta = 25°C)

Pin Arrangement



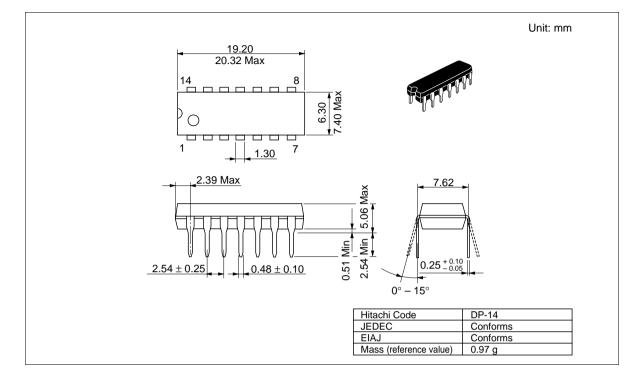
DC Characteristics

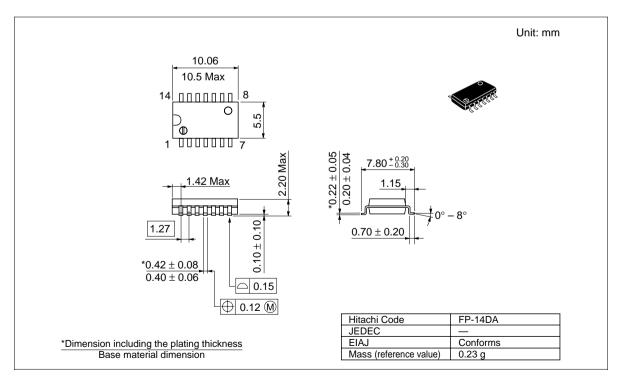
			Ta =	: 25°C	;	Ta = - +85°0	-40 to C			
ltem	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Condition	าร
Input voltage	V _{IH}	2.0	1.5	—	—	1.5	—	V		
		4.5	3.15	—	—	3.15	—			
		6.0	4.2	—	—	4.2	—	_		
	VIL	2.0	_	_	0.5	—	0.5	V		
		4.5	—	—	1.35		1.35	_		
		6.0	-	_	1.8	—	1.8	_		
Output voltage	V _{OH}	2.0	1.9	2.0	_	1.9	—	V	$Vin = V_{IH} \text{ or } V_{IL}$	I _{OH} = -20 μA
		4.5	4.4	4.5	—	4.4	—			
		6.0	5.9	6.0	_	5.9	—	_		
		4.5	4.18	_	—	4.13	—	_		I _{он} = -4 mА
		6.0	5.68	_	—	5.63	—	_		I _{он} = -5.2 mА
	V _{OL}	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} \text{ or } V_{IL}$	$I_{OL} = 20 \ \mu A$
		4.5	_	0.0	0.1	—	0.1	_		
		6.0	—	0.0	0.1	—	0.1	_		
		4.5	_	_	0.26		0.33			I _{oL} = 4 mA
		6.0		—	0.26		0.33	_		I _{oL} = 5.2 mA
Input current	lin	6.0	_	_	±0.1	—	±1.0	μΑ	$Vin = V_{cc} \text{ or } GND$	
Quiescent supply current	I _{cc}	6.0		—	1.0	—	10	μA	Vin = V _{cc} or GN	ND, lout = $0 \mu A$

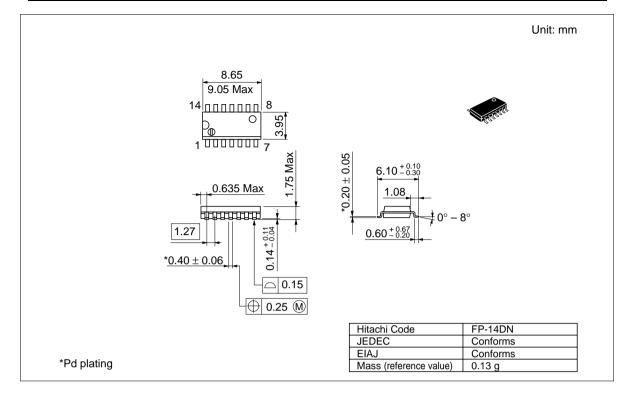
			Ta = 25°C		Ta = –40 to +85°C				
ltem	Symbol	V_{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	_	120	_	150	ns	
time		4.5		13	24	_	30	_	
		6.0	—	_	20	_	26		
	t _{PHL}	2.0	_	_	120	_	150	ns	
		4.5		12	24	—	30		
		6.0	_	_	20	_	26		
Output rise time	t _{TLH}	2.0	_	_	75	_	95	ns	
		4.5		7	15	—	19		
		6.0	_	_	13	_	16		
Output fall time	t_{THL}	2.0	_	—	75	—	95	ns	
		4.5	—	7	15	—	19		
		6.0		_	13	—	16	_	
Input capacitance	Cin			5	10		10	pF	

AC Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

Package Dimensions







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