

Control integrated Power System (CiPoS™)

IKCS12F60AA

IKCS12F60AB

<http://www.infineon.com/cipos>

Power Management & Drives

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CiPoS™ Control integrated Power System

Single In-Line Intelligent Power Module

3Φ-bridge 600V / 12A @ 25°C

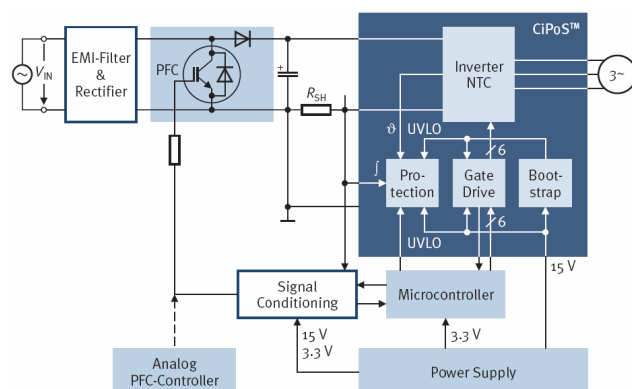


Features

- Fully isolated Single In-Line molded module
- Infineon TrenchStop® IGBTs with lowest $V_{CE(sat)}$
- Optimal adapted EmCon™ diode for low EMI
- SOI gate driver with bootstrap diode and capacitor
- Rugged SOI gate driver technology with stability against transient and negative voltage
- Temperature monitor and over temperature shutdown
- Overcurrent shutdown
- Undervoltage lockout at all channels
- Matched propagation delay for all channels
- Low side emitter pins accessible for all phase current monitoring
- Cross-conduction prevention
- Green mold compound, lead-free pins

Typical Applications

- Washing machines
- Air conditioners
- AC 3 phase motor drive



Description

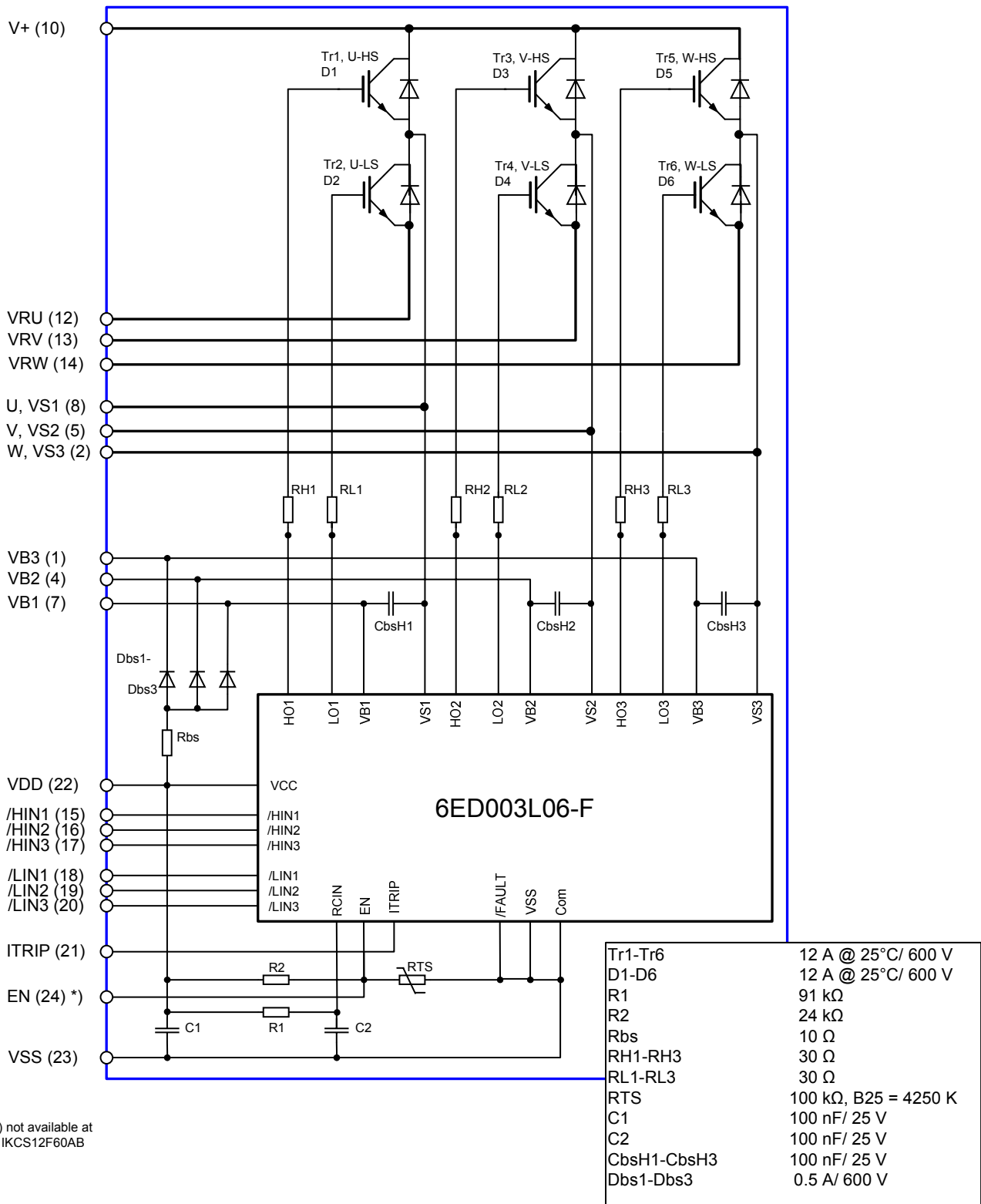
The CiPoS™ module family offers the chance for integrating various power and control components to increase reliability, optimize PCB size and system costs.

This SIL-IPM is designed to control AC motors in variable speed drives for applications like air conditioning, compressors and washing machines. The package concept is specially adapted to power applications, which need extremely good thermal conduction and electrical isolation, but also EMI-save control and overload protection. The features of Infineon TrenchStop® IGBTs and EmCon™ diodes are combined with a new optimized Infineon SOI gate driver for excellent electrical performance.

System Configuration

- 3 halfbridges with TrenchStop® IGBT & FW-EmCon™ diodes
- 3Φ SOI gate driver
- Bootstrap diodes for high side supply
- Integrated 100nF bootstrap capacitance
- Temperature sensor, passive components for adaptations
- Isolated heatsink
- Creepage distance 2.8mm

Internal Electrical Schematic



*) not available at IKCS12F60AB

Pin Assignment

Pin Number	Pin Name	Pin Description
1	VB3	high side floating IC supply voltage
2	W,VS3	motor output W, high side floating IC supply offset voltage
3	na	none
4	VB2	high side floating IC supply voltage
5	V,VS2	motor output V, high side floating IC supply offset voltage
6	na	none
7	VB1	high side floating IC supply voltage
8	U,VS1	motor output U, high side floating IC supply offset voltage
9	na	none
10	V+	positive bus input voltage
11	na	none
12	VRU	low side emitter
13	VRV	low side emitter
14	VRW	low side emitter
15	/HIN1	input gate driver high side 1/U
16	/HIN2	input gate driver high side 2/V
17	/HIN3	input gate driver high side 3/W
18	/LIN1	input gate driver low side 1/U
19	/LIN2	input gate driver low side 2/V
20	/LIN3	input gate driver low side 3/W
21	ITRIP	input overcurrent shutdown
22	VDD	IC main supply +15V
23	VSS	IC negative supply
24*	EN*	input logic enable, output temperature monitoring *) not available at IKCS12F60AB

Absolute Maximum Ratings

($T_c = 25^\circ\text{C}$, if not stated otherwise)

Module Section

Description	Condition	Symbol	Value			Unit
			min	typ	max	
Storage temperature range		T_{stg}	-40	-	125	$^\circ\text{C}$
Operating temperature control PCB ¹		T_{PCB}	-	125	-	$^\circ\text{C}$
Solder temperature	Wave soldering, 1.6mm (0.063in.) from case for 10s	T_{sol}	-	260	-	$^\circ\text{C}$
Insulation test voltage	RMS, $f = 50\text{Hz}$, $t = 1\text{min}$	V_{ISOL}	-	2500	-	V
Mounting torque	M3 screw, using a washer	M_S	-	0.5	0.6	Nm
Mounting pressure on surface	Package flat on mounting surface	N_{MC}	-	-	150	N/mm^2
Weight		m_P	-	17	-	g
Creepage distance		d_S	2.8	-	-	mm

IGBT and Diode Section

Description	Condition	Symbol	Value			Unit
			min	typ	max	
Collector-Emitter breakdown voltage ²	$V_{\text{GE}} = 0\text{V}$, $I_C = 0.25\text{mA}$ $t_p < 250\text{ns}$	$V_{(\text{BR})\text{CES}}$	600	-	-	V
DC output current	$T_c = 25^\circ\text{C}$, $T_{\text{vj}} < 150^\circ\text{C}$ $T_c = 80^\circ\text{C}$, $T_{\text{vj}} < 150^\circ\text{C}$	I_u, I_v, I_w	-12 -6	-	12 6	A
Repetitive peak collector current	T_p limited by T_{vjmax}	I_u, I_v, I_w	-18	-	18	A
Short circuit withstand time ³	$V_{\text{CC}} = 15\text{V}$, $V_{\text{DC}} \leq 400\text{V}$, $T_j \leq 150^\circ\text{C}$	t_{sc}	-	5	-	μs
Power dissipation per IGBT	$T_c = 25^\circ\text{C}$	P_{tot}	-	35	-	W
Operating junction temperature range	IGBT Diode	T_{vjI} T_{vjD}	-40 -40	-	150 150	$^\circ\text{C}$

¹ Monitored by pin 24

² For static operation with biased High-Side VS is reduced by 50V.

³ Allowed number of short circuits: <1000; time between short circuits: >1s.

Description	Condition	Symbol	Value			Unit
			min	typ	max	
Single IGBT thermal resistance, junction-case		R_{thJC}	-	3.6	-	K/W
Single diode thermal resistance, junction-case		R_{thJCD}	-	4.9	-	

Control Section

Description	Condition	Symbol	Value		Unit
			min	max	
Module supply voltage		V_{DD}	-1	20	V
High side floating supply voltage (V_B vs. V_S)		V_{BS}	-1	20	
High side floating IC supply offset voltage	$T_p < 250ns$ $t_p < 500ns$ $t_p \geq 500ns$	$V_{S1,2,3}$	- VDD-VBS-50 VDD-VBS-6	600 - 550	V
Input voltage	LIN, HIN, EN, ITRIP	V_{in}	-1	10	V
Operating junction temperature ¹		$T_{J,IC}$	-	125	
Max. switching frequency		f_{PWM}	-	20	kHz

Recommended Operation Conditions

All voltages are absolute voltages referenced to V_{SS} -Potential unless otherwise specified.

Description	Symbol	Value		Unit
		min	max	
High side floating supply offset voltage	V_S	-3	500	V
High side floating supply voltage (V_B vs. V_S)	V_{BS}	12.5	17.5	
High side output voltage (V_{HO} vs. V_S)	V_{HO}	0	V_{BS}	
Low side power supply	V_{DD}	12.5	17.5	
Logic input voltages LIN,HIN,EN,ITRIP	V_{IN}	0	5	

¹ Monitored by pin 24

Static Parameters

 (T_c = 25°C, if not stated otherwise)

Description	Condition	Symbol	Value			Unit
			min	typ	max	
Collector-Emitter saturation voltage	V _{DD} = 15V, I _{out} = +/- 6A 25°C 150°C	V _{CE(sat)}	- -	1.6 1.85	2.15	V
Diode forward voltage	V _{GE} = 0V, I _{out} = +/- 6A 25°C 150°C	V _F	- -	1.55 1.5	2.05	V
Zero gate voltage collector current ¹	V _{CE} = 600V, V _{GE} = 0V t _p < 250ns T _j = 25°C T _j = 150°C	I _{CES}	- -	- -	40 1000	μA
Short circuit collector current ¹	V _{DD} = 15V, t _{SC} ≤ 5μs V _{CC} = 400V, T _j ≤ 150°C	I _{C(SC)}	-	55	-	A
Logic "0" input voltage (LIN,HIN)		V _{IH}	1.7	2.1	2.4	V
Logic "1" input voltage (LIN,HIN)		V _{IL}	0.7	0.9	1.1	V
EN positive going threshold		V _{EN,TH+}	1.9	2.1	2.3	V
EN negative going threshold		V _{EN,TH-}	1.1	1.32	1.5	V
ITRIP positive going threshold		V _{IT,TH+}	360	450	540	mV
ITRIP input hysteresis		V _{IT,HYS}	60	85	-	mV
V _{DD} and V _{BS} supply undervoltage positive going threshold		V _{DDUV+} V _{BSUV+}	11.0	12.0	12.8	V
V _{DD} and V _{BS} supply undervoltage negative going threshold		V _{DDUV-} V _{BSUV-}	9.5	10.3	11.0	V
V _{CC} and V _{BS} supply undervoltage lockout hysteresis		V _{DDUVH} V _{BSUVH}	1.2	1.6	-	V
Input clamp voltage (/HIN, /LIN, EN, ITRIP)		V _{INCLAMP}	9.0	10.1	13.0	V
Quiescent V _{Bx} supply current (V _{Bx} only)	V _{IN} = low	I _{QB}	-	300	550	μA
Quiescent V _{DD} supply current (V _{DD} only)	V _{IN} = float	I _{QDD}	-	2.1	3.0	mA
Input bias current	V _{IN} = 5V	I _{IN+}	-	43	100	μA
Input bias current	V _{IN} = 0V	I _{IN-}	-	100	200	μA
ITRIP Input bias current	V _{ITRIP} = 5V	I _{ITRIP+}	-	62	120	μA
EN Input bias current	V _{EN} = 5V	I _{EN+}	-	62	120	μA
Leakage current of high side	V _S = 600V, T _{j,IC} = 125°C	I _{LVS}	-	30	-	μA

¹ Allowed number of short circuits: <1000; time between short circuits: >1s.

Dynamic Parameters

 (T_c = 25°C, if not stated otherwise)

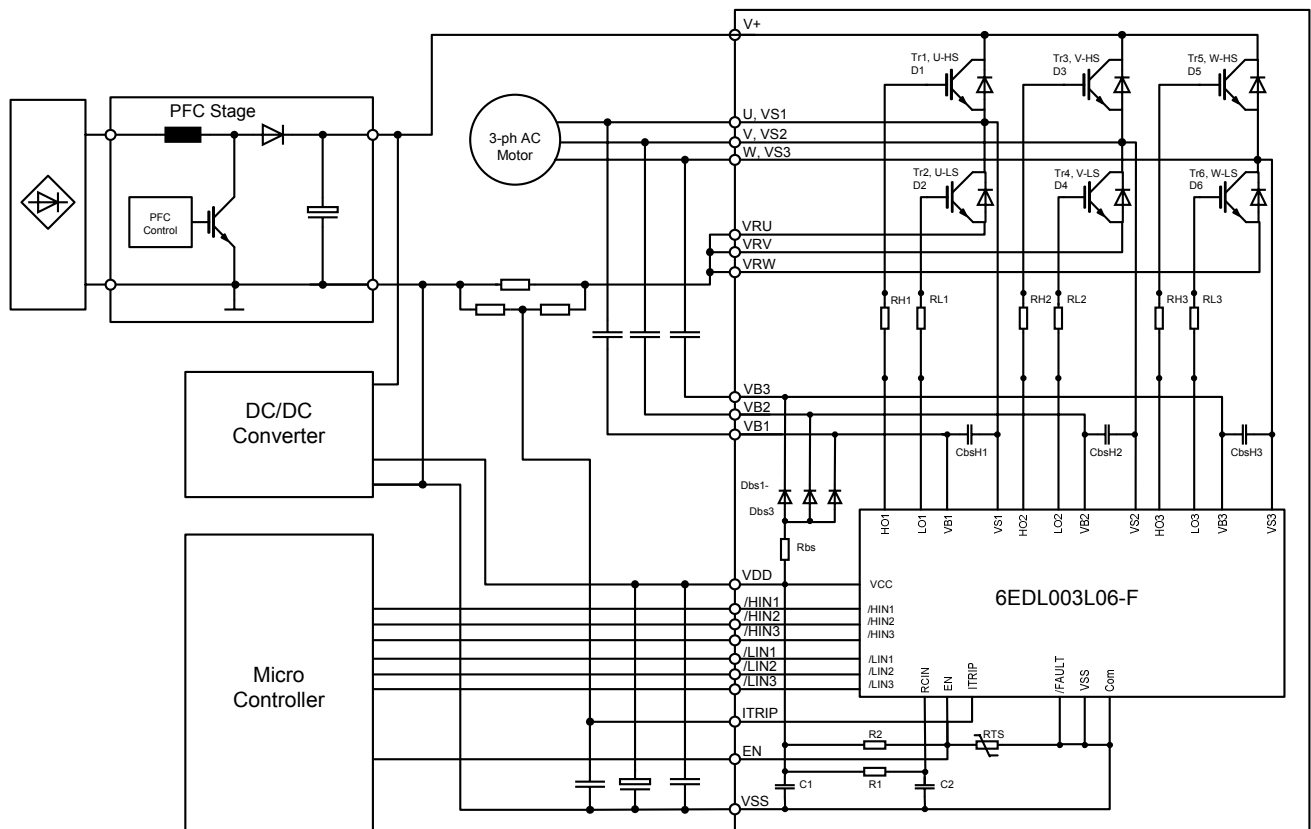
Description	Condition	Symbol	Value			Unit
			min	typ	max	
Turn-on propagation delay	V _{LIN,HIN} = 0V	t _{d(on)}	-	1684	-	ns
Turn-on rise time	I _{out} = 6A, V _{DC} = 400V V _{LIN,HIN} = 5V	t _r	-	102	-	ns
Turn-off propagation delay	V _{LIN,HIN} = 5V	t _{d(off)}	-	1217	-	ns
Turn-on fall time	I _{out} = 6A, V _{DC} = 400V V _{LIN,HIN} = 0V	t _f	-	18	-	ns
Shutdown propagation delay ENABLE	V _{EN} = 0V	t _{EN}	-	700	1000	ns
Shutdown propagation delay ITRIP	V _{ITRIP} = 1V	t _{ITRIP}	-	690	1000	ns
Input filter time ITRIP	V _{ITRIP} = 1V	t _{ITRIPmin}	155	225	380	ns
Input filter time at LIN for turn on and off and input filter time at HIN for turn on only	V _{LIN,HIN} = 0 V & 5V	t _{FILIN}	120	270	-	ns
Input filter time at HIN for turn off	V _{HIN} = 5V	t _{FILIN1}	-	200	-	ns
Input filter time at HIN for turn off	V _{HIN} = 5 V	t _{FILIN2}	-	350	-	ns
Input filter time EN		t _{FILEN}	300	430	-	ns
Fault clear time at RCIN after ITRIP-fault	V _{LIN,HIN} = 0 V & 5V V _{ITRIP} = 0 V	t _{FLTCLR}	-	5	-	ms
Min. deadtime between low side and high side		DT _{min}	1.5	-	-	µs
IGBT Turn-on Energy	I _{out} = 6A, V _{DC} = 400V T _{vj} = 25°C T _{vj} = 150°C	E _{on}	- -	tbd 0.27	- -	mJ
IGBT Turn-off Energy	I _{out} = 6A, V _{DC} = 400V T _{vj} = 25°C T _{vj} = 150°C	E _{off}	- -	tbd 0.2	- -	mJ
Diode recovery Energy	I _{out} = 6A, V _{DC} = 400V T _{vj} = 25°C T _{vj} = 150°C	E _{rec}	- -	tbd 0.12	- -	mJ

Integrated Components

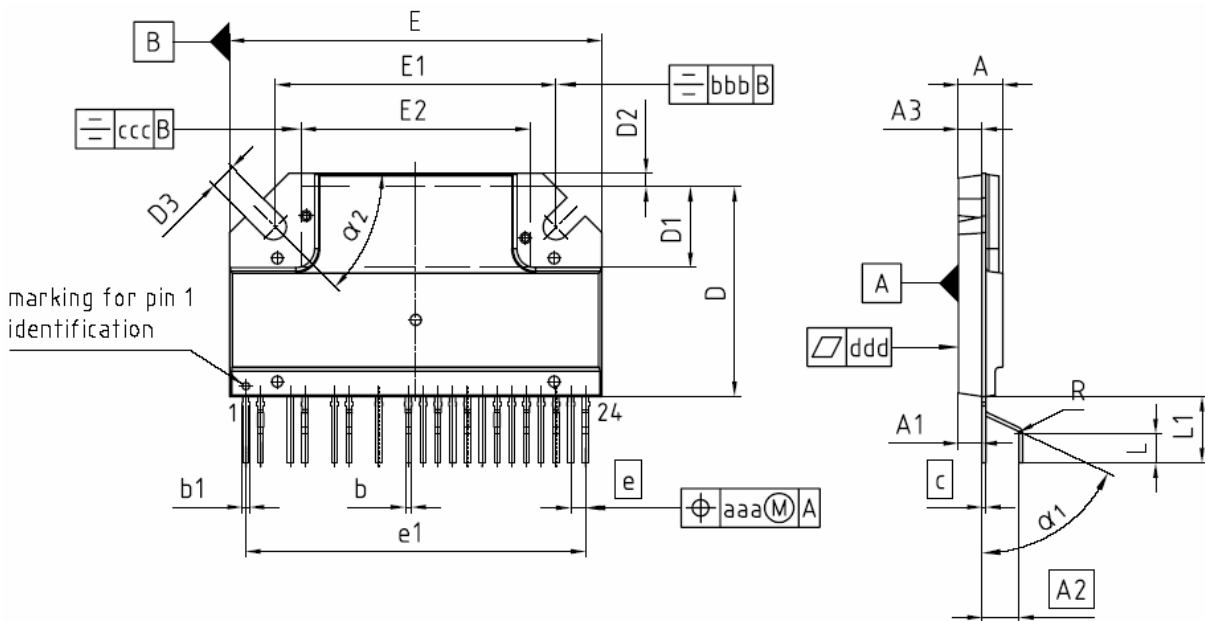
Description	Condition	Symbol	Value			Unit
			min	typ	max	
Integrated Resistor		R1	-	91	-	kΩ
Integrated Resistor		R2	-	24	-	Ω
Integrated Resistor		Rbs	-	10	-	Ω
Integrated Resistor	$T_{NTC} = 25^{\circ}\text{C}$	RTS	-	100	-	kΩ
Bootstrap diode forward voltage	$I_{FDBS} = 100\text{mA}$	V_{FDBS}	-	1.9	2.05	V
B-Constant of NTC (Negative Temperature Coefficient)	$T_{NTC} = 25^{\circ}\text{C}$	B25	-	4250	-	K
Integrated Capacitor		C1, C2	-	100	-	nF
Integrated Bootstrap Capacitor		$CbsH_x$	-	100	-	nF
Integrated Gate Resistor		RL_x	-	30	-	Ω

Circuit of a Typical Application

(here: IKCS12F60AA)



Package Outline IKCS12F60AA



DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	5,900	6,100	0,232	0,240
A1	3,200		0,126	
A2	5,000		0,197	
A3	3,100	3,300	0,122	0,130
b	0,760	0,880	0,030	0,035
b1	-	1,120	-	0,044
c	0,460	0,580	0,018	0,023
D	30,100	30,300	1,185	1,193
D1	10,600	11,200	0,417	0,441
D2	1,300	2,300	0,051	0,091
D3	3,400	3,600	0,134	0,142
E	50,300	50,500	1,980	1,988
E1	37,900	38,100	1,492	1,500
E2	30,700	31,300	1,209	1,232
e	2,000		0,079	
e1	46,000		1,811	
N	20		20	
L	3,630	4,230	0,143	0,167
L1	8,700	9,300	0,343	0,366
R	0,300	0,900	0,012	0,035
aaa	0,250		0,010	
bbb	0,250		0,010	
ccc	0,500		0,020	
ddd	0,150		0,006	
α1	64,000	66,000	64,000	66,000
α2	44,000	46,000	44,000	46,000

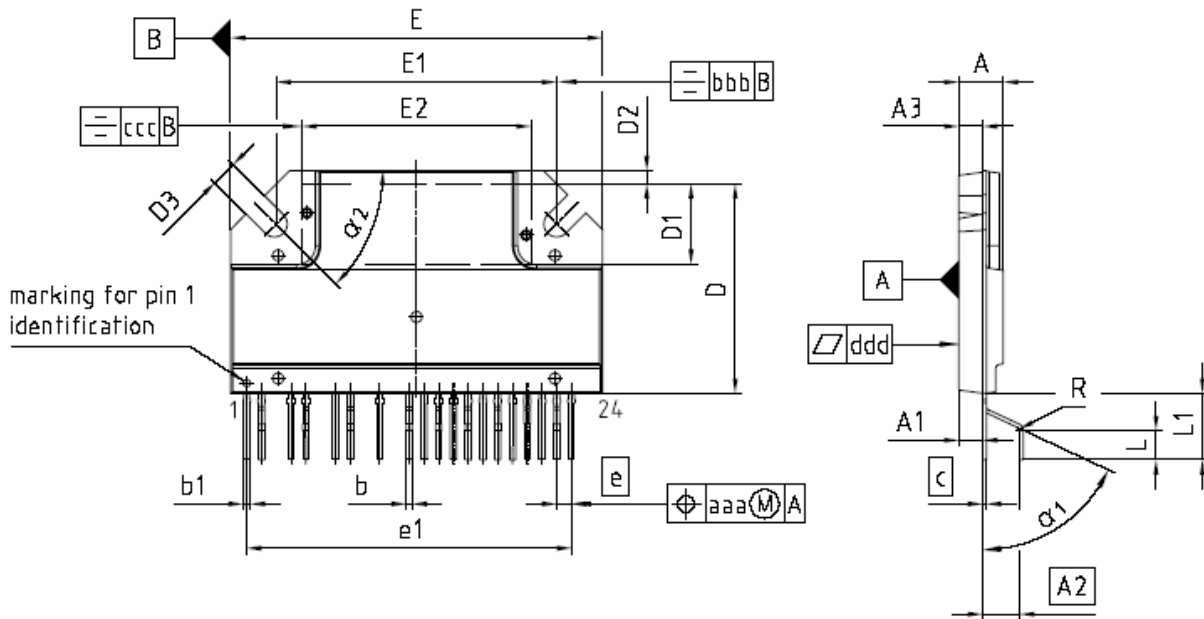
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Package Outline IKCS12F60AB


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	5,900	6,100	0,232	0,240
A1	3,200		0,126	
A2	5,000		0,197	
A3	3,100	3,300	0,122	0,130
b	0,760	0,880	0,030	0,035
b1	-	1,120	-	0,044
c	0,460	0,580	0,018	0,023
D	30,100	30,300	1,185	1,193
D1	10,600	11,200	0,417	0,441
D2	1,300	2,300	0,051	0,091
D3	3,400	3,600	0,134	0,142
E	50,300	50,500	1,980	1,988
E1	37,900	38,100	1,492	1,500
E2	30,700	31,300	1,209	1,232
e	2,000		0,079	
e1	44,000		1,732	
N	19		19	
L	3,630	4,230	0,143	0,167
L1	8,700	9,300	0,343	0,366
R	0,300	0,900	0,012	0,035
aaa	0,250		0,010	
bbb	0,250		0,010	
ccc	0,500		0,020	
ddd	0,150		0,006	
alpha1	64,000	66,000	64,000	66,000
alpha2	44,000	46,000	44,000	46,000

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