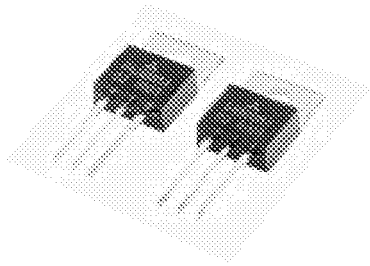


MITSUBISHI Nch POWER MOSFET

FS50UMJ-03

HIGH-SPEED SWITCHING USE

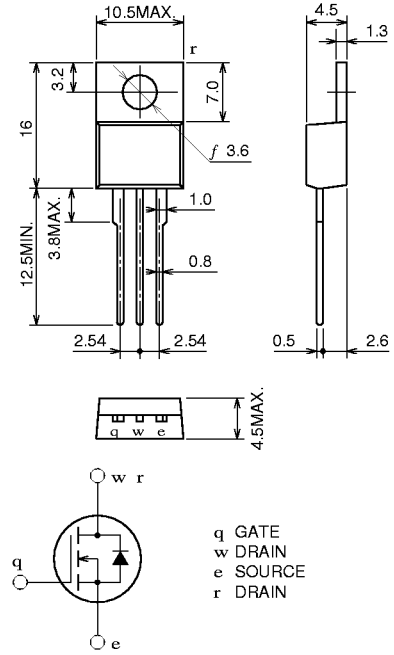
FS50UMJ-03



- ∨ 4V DRIVE
- ∨ V_{DSS} 30V
- ∨ r_{DS (ON)} (MAX) 19mΩ
- ∨ I_D 50A
- ∨ Integrated Fast Recovery Diode (TYP.) 60ns

OUTLINE DRAWING

Dimensions in mm



TO-220

APPLICATION

Motor control, Lamp control, Solenoid control
DC-DC converter, etc.

MAXIMUM RATINGS (T_c = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit
V _{DSS}	Drain-source voltage	V _{GS} = 0V	30	V
V _{GSS}	Gate-source voltage	V _{DS} = 0V	±20	V
I _D	Drain current		50	A
I _{DM}	Drain current (Pulsed)		200	A
I _{DA}	Avalanche drain current (Pulsed)	L = 30μH	50	A
I _S	Source current		50	A
I _{SM}	Source current (Pulsed)		200	A
P _D	Maximum power dissipation		45	W
T _{ch}	Channel temperature		-55 ~ +150	°C
T _{stg}	Storage temperature		-55 ~ +150	°C
—	Weight	Typical value	2.0	g

Feb.1999

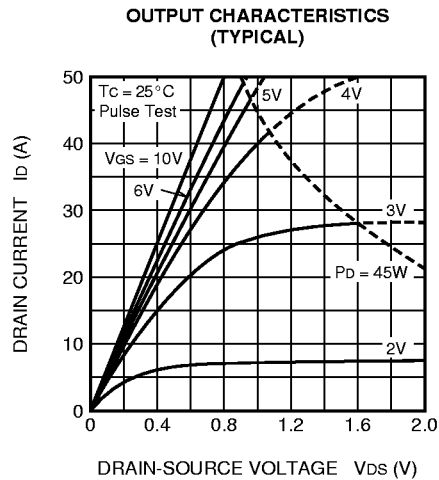
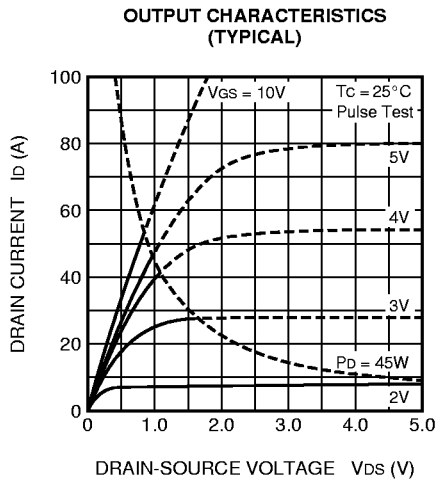
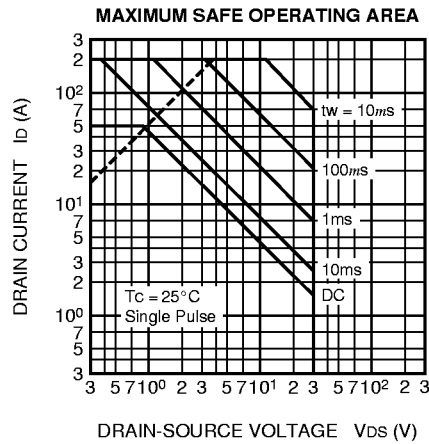
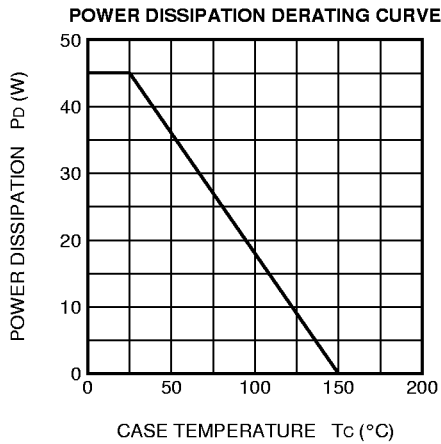
FS50UMJ-03

HIGH-SPEED SWITCHING USE

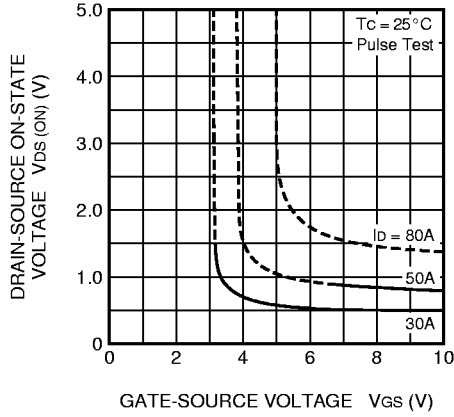
ELECTRICAL CHARACTERISTICS (T_{ch} = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
V _{(BR)DSS}	Drain-source breakdown voltage	I _D = 1mA, V _{GS} = 0V	30	—	—	V
I _{GSS}	Gate-source leakage current	V _{GS} = ±20V, V _{DS} = 0V	—	—	±0.1	μA
I _{DSS}	Drain-source leakage current	V _{DS} = 30V, V _{GS} = 0V	—	—	0.1	mA
V _{GS(th)}	Gate-source threshold voltage	I _D = 1mA, V _{DS} = 10V	1.0	1.5	2.0	V
r _{DS(ON)}	Drain-source on-state resistance	I _D = 25A, V _{GS} = 10V	—	15	19	mΩ
r _{DS(ON)}	Drain-source on-state resistance	I _D = 25A, V _{GS} = 4V	—	21	35	mΩ
V _{DS(ON)}	Drain-source on-state voltage	I _D = 25A, V _{GS} = 10V	—	0.375	0.475	V
y _{fs}	Forward transfer admittance	I _D = 25A, V _{DS} = 10V	—	28	—	S
C _{iss}	Input capacitance	V _{DS} = 10V, V _{GS} = 0V, f = 1MHz	—	1600	—	pF
C _{oss}	Output capacitance		—	500	—	pF
C _{rss}	Reverse transfer capacitance		—	260	—	pF
t _{d(on)}	Turn-on delay time	V _{DD} = 15V, I _D = 25A, V _{GS} = 10V, R _{GEN} = R _{GS} = 50Ω	—	17	—	ns
t _r	Rise time		—	90	—	ns
t _{d(off)}	Turn-off delay time		—	130	—	ns
t _f	Fall time		—	125	—	ns
V _{SD}	Source-drain voltage	I _S = 25A, V _{GS} = 0V	—	1.0	1.5	V
R _{th(ch-c)}	Thermal resistance	Channel to case	—	—	2.78	°C/W
t _{rr}	Reverse recovery time	I _S = 25A, di _s /dt = -50A/μs	—	60	—	ns

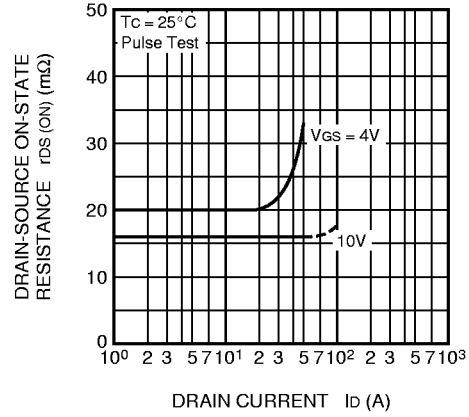
PERFORMANCE CURVES



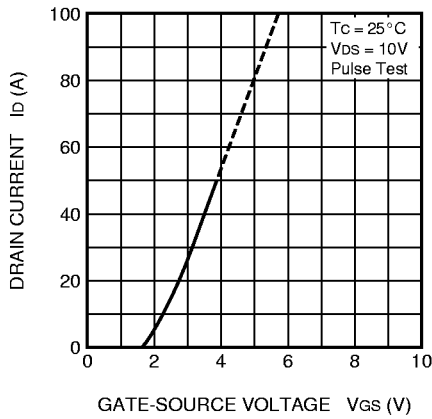
ON-STATE VOLTAGE VS. GATE-SOURCE VOLTAGE (TYPICAL)



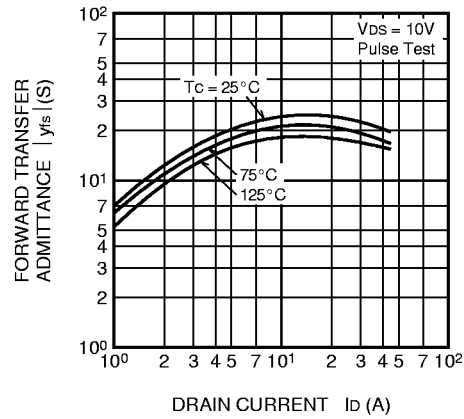
ON-STATE RESISTANCE VS. DRAIN CURRENT (TYPICAL)



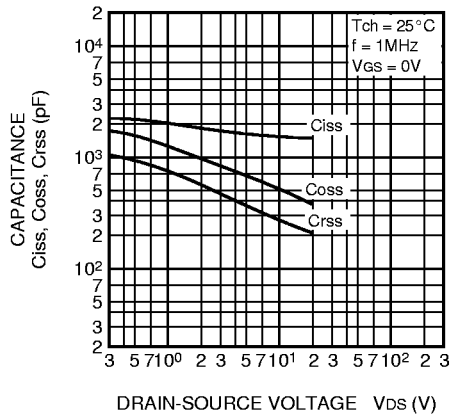
TRANSFER CHARACTERISTICS (TYPICAL)



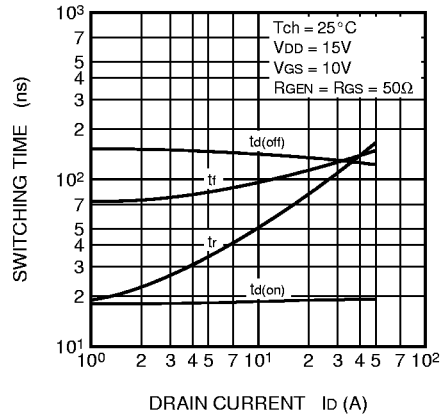
FORWARD TRANSFER ADMITTANCE VS. DRAIN CURRENT (TYPICAL)



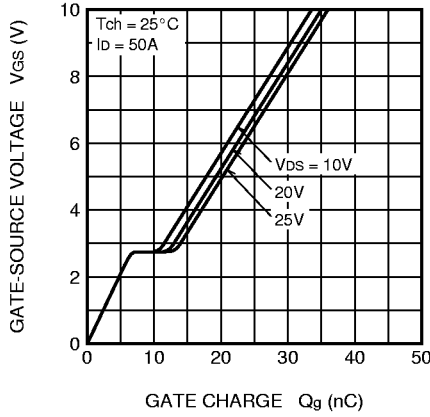
CAPACITANCE VS. DRAIN-SOURCE VOLTAGE (TYPICAL)



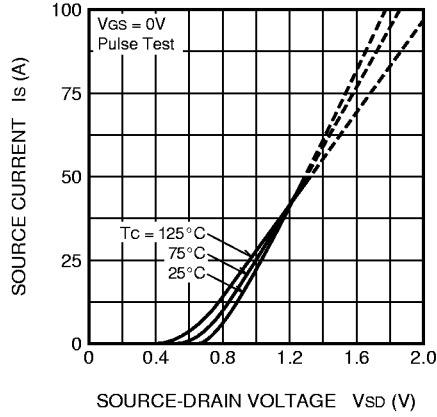
SWITCHING CHARACTERISTICS (TYPICAL)



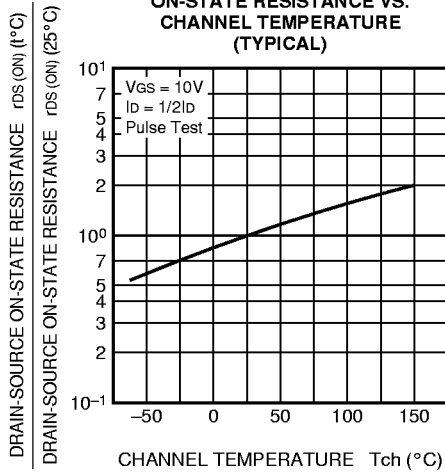
GATE-SOURCE VOLTAGE VS. GATE CHARGE (TYPICAL)



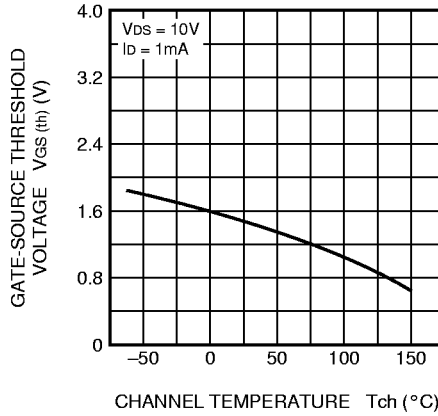
SOURCE-DRAIN DIODE FORWARD CHARACTERISTICS (TYPICAL)



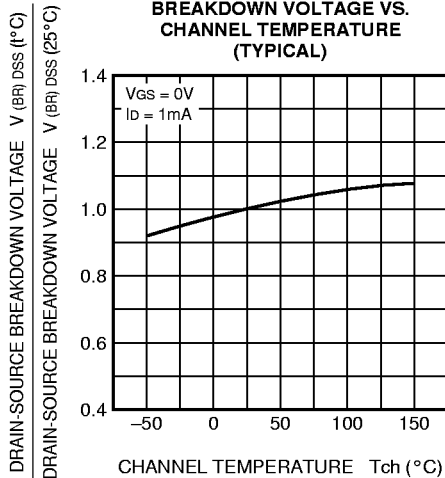
ON-STATE RESISTANCE VS. CHANNEL TEMPERATURE (TYPICAL)



THRESHOLD VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)



BREAKDOWN VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS

