

SP1674

TRIPLE 2-INPUT EXCLUSIVE-NOR GATE

This three gate ECL array is designed to provide the positive logic Exclusive-NOR function in high speed applications. These devices contain a temperature compensated internal bias which ensures that the threshold point remains in the centre of the transition region over the temperature range (0°C to +75°C). Input pull-down resistors eliminate the need to tie unused inputs to V_{EE}.

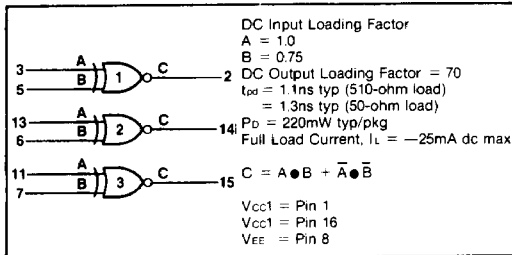


Fig.1 Logic diagram of SP1674

ORDERING INFORMATION

SP1674DG (Commercial-ceramic package)

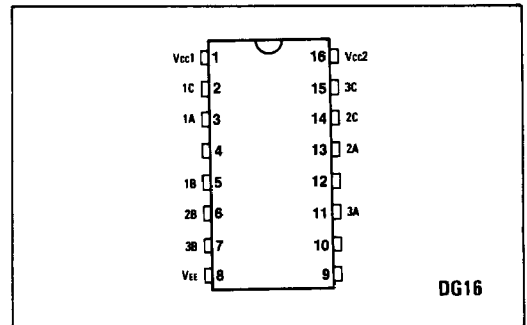


Fig.2 Pin connections (top view)

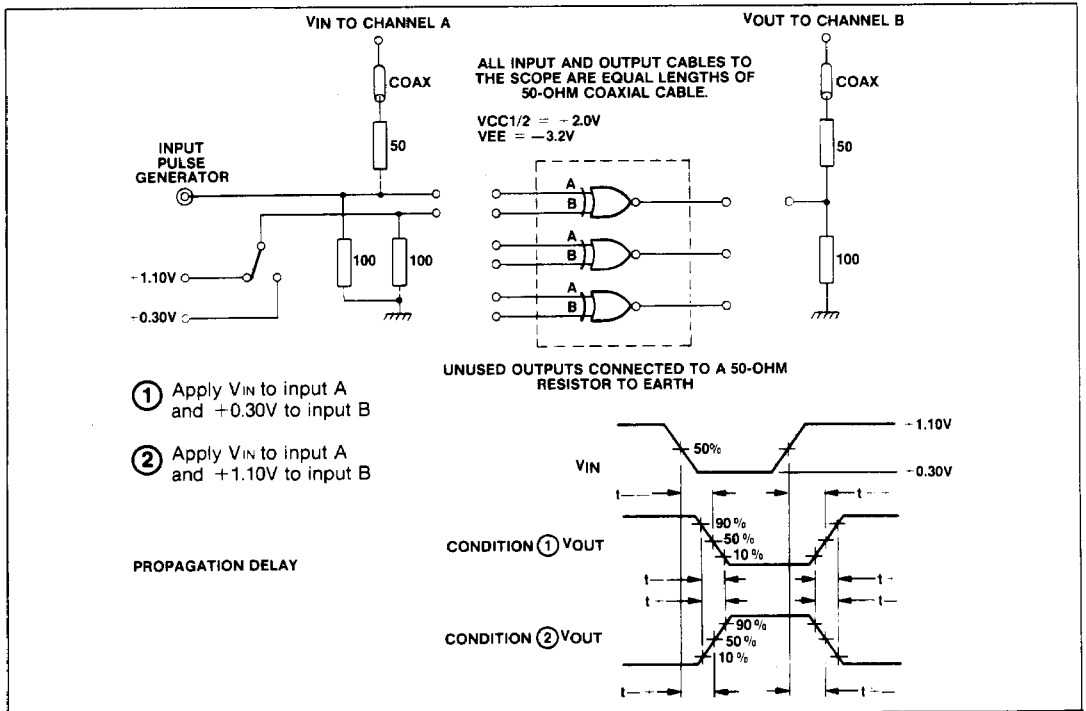


Fig.3 Switching time test circuit and waveforms at +25°C

