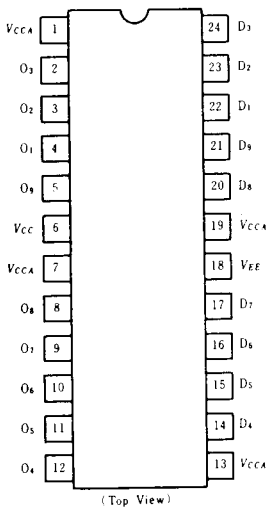
HD100126

9-bit Backplane Driver

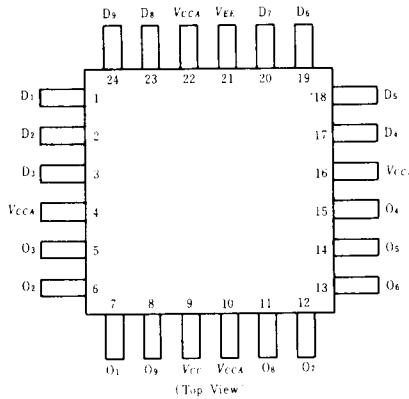
The HD100126 contains nine independent, high speed, buffer gates each with a single input and a single output.
The gates are non-inverting. These buffers are useful in bus-oriented systems where minimal output loading or bus isolation is desired. The output transition times are longer to minimize noise when used as a backplane driver.

■PIN ARRANGEMENT

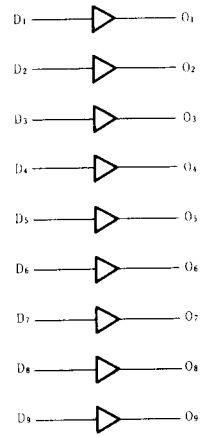
● HD100126



● HD100126F



■LOGIC SYMBOL



■DC CHARACTERISTICS (V_{EE} = −4.2 to −4.8V, V_{CC} = V_{CCA} = GND, Ta = 0 to +85℃)

Item	Symbol	Test Condition	min	typ	max	Unit
Supply Current	I_{EE}	All inputs open	46	70	96	mA
Input Current	I_{IH}	$V_{IN} = V_{IH\ max}$	—	—	350	μA

Note) As for other items, refer to the "Common DC Characteriatics".

■AC CHARACTERISTICS (V_{EE} = −2.2 to −2.8V, V_{CC} = V_{CCA} = 2.0V)

● HD100126

Item	Symbol	Test Condition	0℃		25℃			85℃		Unit
			min	max	min	typ	max	min	max	
Propagation Delay Time	t_{PLH} t_{PHL}	See test circuit and waveform	1.05	2.75	1.05	1.90	2.75	1.05	2.75	ns
Transition Time	t_{TLH} t_{THL}		1.15	3.40	1.15	2.55	3.40	1.05	3.40	ns

● HD100126F

Item	Symbol	Test Condition	0℃		25℃			85℃		Unit
			min	max	min	typ	max	min	max	
Propagation Delay Time	t_{PLH} t_{PHL}	See test circuit and waveform	1.05	2.55	1.05	1.85	2.55	1.05	2.55	ns
Transition Time	t_{TLH} t_{THL}		1.15	3.30	1.15	2.50	3.30	1.05	3.30	ns

Note) The circuit in a test socket or mounted on a printed circuit board and transverse air flow greater than 2.5m/s (500 linear fpm) is maintained.