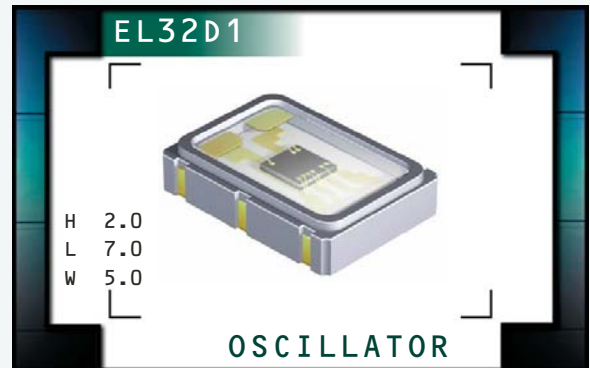


EL32D1 Series



- Voltage Controlled Crystal Oscillators (VCXO)
- LVDS Output
- +3.3V Supply Voltage
- Complementary Output
- Tri-State Output Function (Pad 2)
- External Voltage Control Function
- 6 Pad Ceramic SMD Package
- RoHS Compliant (Pb-Free)



ELECTRICAL SPECIFICATIONS

Frequency Range (MHz)	50, 51.84, 52, 52.08, 54, 57.1429, 61.44, 63.36, 65.536, 76.8, 77.76, 100, 104, 106.25, 122.88, 125, 126.72, 128, 153.6, 155.52, 156.25, 159.375, 161.1328, 166.6286, 167.3315, 184.32, 186.666, 200MHz
Operating Temperature Range	0°C to +70°C -40°C to +85°C
Storage Temperature Range	-55°C to +125°C
Supply Voltage (V _{CC})	3.3V _{DC} ±5%
Differential Output Voltage (V _{OD})	247mV Minimum, 355mV Typical, 454mV Maximum
Input Current	80mA Maximum
Frequency Tolerance / Stability	Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, 1st Year Aging, Shock, and Vibration ±50ppm or ±25ppm Maximum
Output Voltage Logic High (V _{OH})	1.43V _{DC} Typical, 1.6V _{DC} Maximum
Output Voltage Logic Low (V _{OL})	1.1V _{DC} Typical, 0.9V _{DC} Minimum
V _{DD} Magnitude Change (ΔV _{DD})	-50mV Minimum, +50mV Maximum
Offset Voltage (V _{OS})	1.125V Minimum, 1.20V Typical, 1.375V Maximum
Rise Time / Fall Time	20% to 80% of waveform 1 nSeconds Maximum
Duty Cycle	at 50% of waveform 50 ±10(%) 50 ±5(%)
Offset Voltage Magnitude Change (ΔV _{OS})	25mV Maximum
Load Drive Capability	Between Output and Complementary Output 100 Ohms and 10pF
Control Voltage (V _C)	Test Conditions for Frequency Deviation 1.65V _{DC} ±1.65V _{DC}
Control Voltage Range (V _{CR})	0.0V _{DC} to V _{CC} +0.5V _{DC}
Frequency Deviation	Inclusive of Operating Temperature Range, Supply Voltage Change, and Output Load Change ±75ppm Minimum
Linearity	20%, 15%, or 10% Maximum
Transfer Function	Positive Transfer Characteristic
Modulation Bandwidth (MBW)	Measured at -3dB with Control Voltage of +1.65V _{DC} 10kHz Minimum
Input Impedance (Z _I)	50kOhms Typical
Typical Phase Noise (Fo = 155.520MHz)	at 10Hz Offset -55dBc/Hz at 100Hz Offset -90dBc/Hz at 1kHz Offset -120dBc/Hz at 10kHz Offset -140dBc/Hz at 100kHz Offset -145dBc/Hz at 1MHz Offset -148dBc/Hz
Logic Control/Additional Output	Tri-State Enable High / Complementary Output
Tri-State Input Voltage	V _{IH} of 70% of V _{CC} Minimum Enables Output No Connection Enables Output V _{IL} of 30% of V _{CC} Maximum Disables Outputs: High Impedance
RMS Phase Jitter	FJ = 12kHz to 20MHz 0.4pSec Typical, 1 pSec Maximum
Accumulated Period Jitter (t _{acc})	Sigma of Total Jitter Distribution 4pSec Typical, 5pSec Maximum
Period Jitter (t _{rj})	Sigma of Random Jitter 3pSec Typical, 5pSec Maximum
Period Jitter (t _{rms})	Sigma of Total Jitter Distribution 3pSec Typical, 5pSec Maximum
Period Jitter (t _{dj})	Deterministic Jitter 4pSec Typical, 10pSec Maximum
Period Jitter (t _{p-p})	Peak to Peak of Jitter Distribution 27pSec Typical, 40pSec Maximum
Start Up Time	10mSec Maximum

MANUFACTURER	CATEGORY	SERIES	PACKAGE	VOLTAGE	CLASS	REV. DATE
ECLIPTEK CORP.	OSCILLATOR	EL32D1	CERAMIC	3.3V	OS3Z	01/11

PART NUMBERING GUIDE

EL32D1 E E A 2 K - 155.520M TR

**FREQUENCY TOLERANCE & STABILITY/
OPERATING TEMPERATURE RANGE**

D=±50ppm Maximum over 0°C to +70°C
E=±25ppm Maximum over 0°C to +70°C
H=±50ppm Maximum over -40°C to +85°C

FREQUENCY DEVIATION

E=±75ppm Minimum

LINEARITY

A=20% Maximum
B=15% Maximum
C=10% Maximum

AVAILABLE OPTIONS

Blank=Bulk
TR=Tape & Reel

FREQUENCY

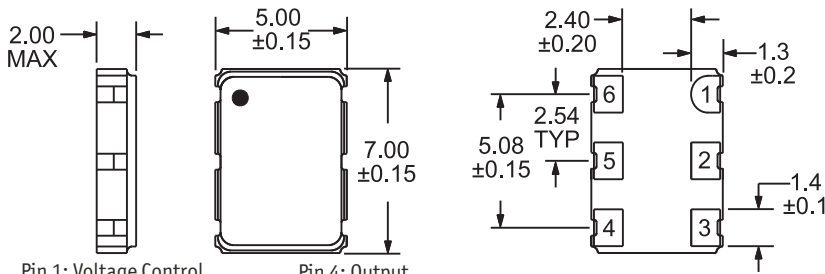
LOGIC CONTROL / ADDITIONAL OUTPUT

K=Tri-State (Enable High) / Complementary Output

DUTY CYCLE

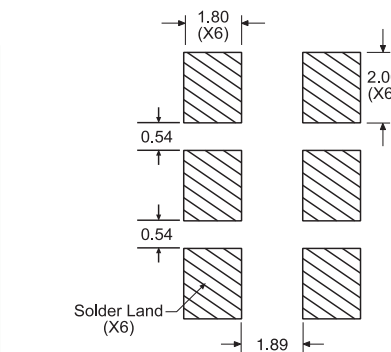
1=50% ±10%
2=50% ±5%

MECHANICAL DIMENSIONS
ALL DIMENSIONS IN MILLIMETERS



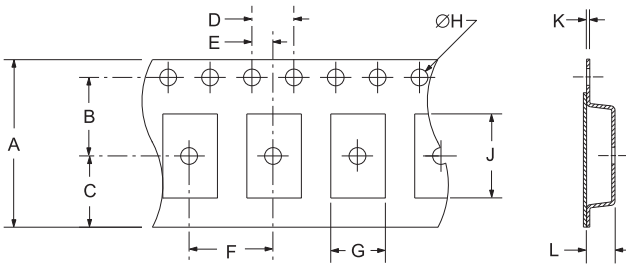
Pin 1: Voltage Control
Pin 2: Tri-State
Pin 3: Case Ground
Pin 4: Output
Pin 5: Complementary Output
Pin 6: Supply Voltage

SUGGESTED SOLDER PAD LAYOUT
ALL DIMENSIONS IN MILLIMETERS

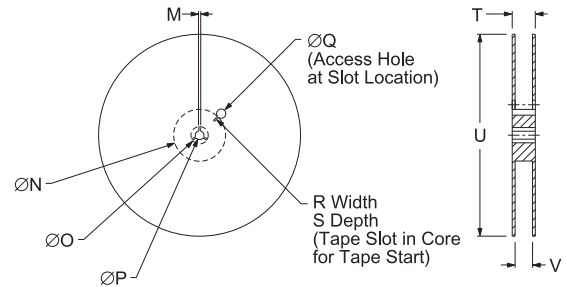


Tolerances=±0.1

TAPE AND REEL DIMENSIONS
ALL DIMENSIONS IN MILLIMETERS



TAPE	A	B	C	D	E
	16±.3-1	7.5±.1	6.75±.1	4 ±.1	2±.1
F	G	H	J	K	L
8±.1	B0*	1.5 +.1-0	A0*	.3±.05	K0*



REEL	M	N	O	P	Q
	1.5 MIN	50 MIN	20.2 MIN	13±.2	40 MIN
R	S	T	U	V	QTY/REEL
2.5 MIN	10 MIN	22.4 MAX	360 MAX	16.4+2-0	1,000

*Compliant to EIA 481A

ENVIRONMENTAL/MECHANICAL SPECIFICATIONS

Characteristic	Specification
Fine Leak Test	MIL-STD-883, Method 1014, Condition A
Gross Leak Test	MIL-STD-883, Method 1014, Condition C
Mechanical Shock	MIL-STD-202, Method 213, Condition C
Vibration	MIL-STD-883, Method 2007, Condition A
Solderability	MIL-STD-883, Method 2002
Temperature Cycling	MIL-STD-883, Method 1010
Resistance to Soldering Heat	MIL-STD-202, Method 210
Resistance to Solvents	MIL-STD-202, Method 215

MARKING SPECIFICATIONS

Line 1: ECLIPTEK
Line 2: XX.XXX M
Line 3: XX Y ZZ

Frequency in MHz (5 Digits Maximum + Decimal)
Week of Year
Last Digit of Year
Eclipse Manufacturing Identifier

MANUFACTURER	CATEGORY	SERIES	PACKAGE	VOLTAGE	CLASS	REV. DATE
ECLIPTEK CORP.	OSCILLATOR	EL32D1	CERAMIC	3.3V	OS3Z	01/11