

FEATURES

- Sampling Rates from 1 kHz to 15 MHz (MSPS)
- DNL better than 1/2 LSB from 1 kHz to 10 MHz
- Interface to any Input Range between GND and V_{DD}
- Monotonic; No Missing Codes
- Single Power Supply (4 to 6 volt)
- Low Power CMOS (300 mW)
- Latch-Up Free
- 2000 Volt ESD Protection

BENEFITS

- Reduced Board Space (small package)
- Excellent Accuracy Without High System Power
- Reduced External Parts - No Sample/Hold Needed
- Designer Can Adapt Input Range and Scaling
- Use MP7690A for New Designs

GENERAL DESCRIPTION

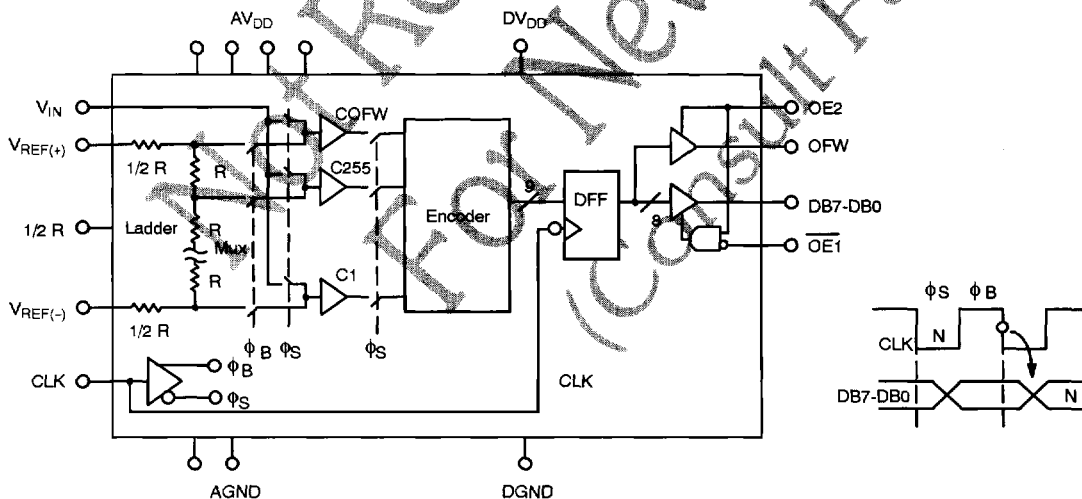
The MP7690 is an 8-bit monolithic CMOS single step high speed Analog-to-Digital Converter designed for precision applications in video and data acquisition requiring conversion rates to 10 MHz with differential linearity error less than 1/2 LSB and low power consumption. The input architecture of the MP7690 allows direct interface to any analog input range between AGND and AV_{DD} (0 to 2V, 1

to 4 V, 0 to 5 V, etc.). The user simply sets $V_{REF(+)}$ and $V_{REF(-)}$ to encompass the desired input range.

The MP7690 includes 256 clocked comparators, encoders, 3-state output buffers, a reference ladder resistor and associated timing circuitry. An overflow bit (or flag) is provided.

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SIMPLIFIED BLOCK AND TIMING DIAGRAM

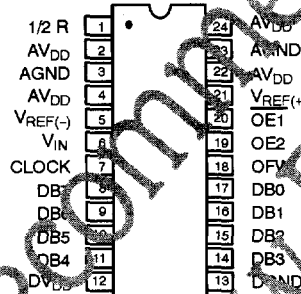


ORDERING INFORMATION

Package Type	Temperature Range	Part No.	DNL (LSB)	INL (LSB)
Ceramic Dip	-40 to +85°C	MP7690JD	±1 1/2	
Ceramic Dip	-40 to +85°C	MP7690KD	±1	1 1/2
Ceramic Dip	-55 to +125°C	MP7690SD	±1 1/2	
Ceramic Dip	-55 to +125°C	MP7690TD	±1	1/2

PIN CONFIGURATION

See Packaging Section for Package Dimensions



24 Pin DIP (0.300")

PIN OUT DEFINITIONS

PIN NO.	NAME	DESCRIPTION
1	1/2R	Center of Reference Resistance Ladder
2	AV _{DD}	Analog Power Supply Voltage
3	AGND	Analog Ground Return
4	AV _{DD}	Analog Power Supply Voltage
5	V _{REF(-)}	Lower Reference Voltage Input
6	V _{IN}	Analog Input Voltage
7	CLK	Sampling Clock Input
8	DB7	Data Bit 7 (MSB)
9	DB6	Data Output Bit 6
10	DB5	Data Output Bit 5
11	DB4	Data Output Bit 4
12	DV _{DD}	Digital Power Supply Voltage

PIN NO.	NAME	DESCRIPTION
13	DGND	Digital Ground Return
14	DB3	Data Output Bit 3
15	DB2	Data Output Bit 2
16	DB1	Data Output Bit 1
17	DB0	Data Output Bit 0 (LSB)
18	OFW	Overflow flag
19	OE2	Output Enable Control Pin
20	OE1	Output Enable Control Pin
21	V _{REF(+)}	Upper Reference Voltage Input
22	AV _{DD}	Analog Power Supply Voltage
23	AGND	Analog Ground Return
24	AV _{DD}	Analog Power Supply Voltage