

LM293/A, LM393/A, LM2903

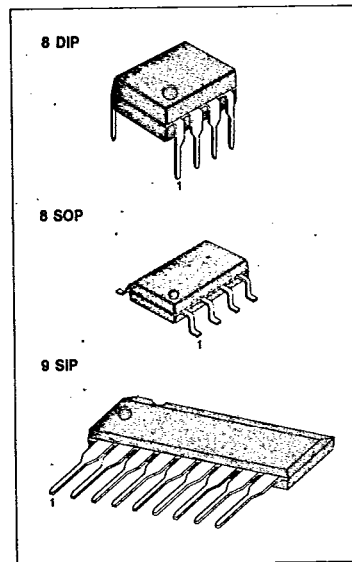
LINEAR INTEGRATED CIRCUIT

DUAL DIFFERENTIAL COMPARATOR

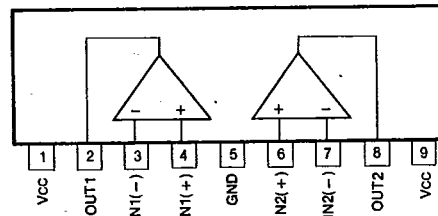
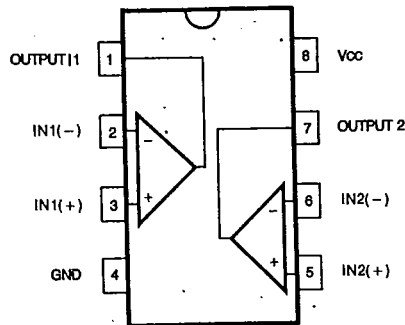
The LM293 series consists of two independent voltage comparators that one designed to operate from a single power supply over a wide range of voltage.

FEATURES

- Single Supply Operation: 2V to 36V
- Dual Supply Operation:  $\pm 1V$  to  $\pm 18V$
- Allow Comparison of Voltages Near Ground Potential
- Low Current Drain 800 $\mu A$  Typ
- Compatible with all Forms of Logic
- Low Input Bias Current 25nA Typ
- Low Input Offset Current  $\pm 5nA$  Typ
- Low Offset Voltage  $\pm 2mV$  Typ



BLOCK DIAGRAM



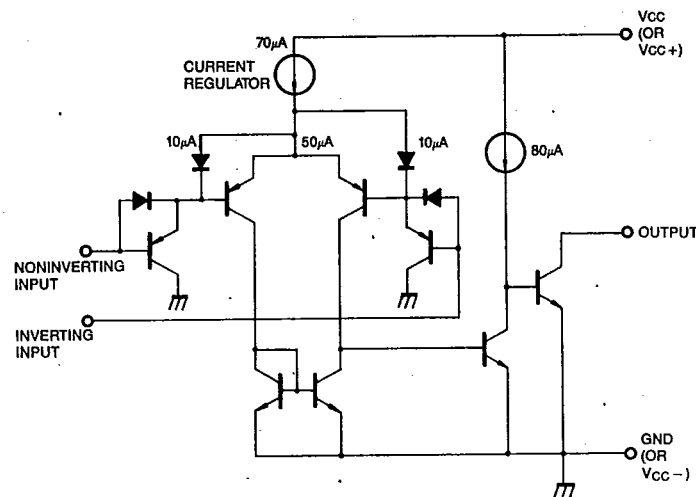
ORDERING INFORMATION

Device	Package	Operating Temperatur
LM293N LM293AN	8 DIP	- 25 ~ + 85°C
LM293S	9 SIP	
LM293D LM293AD	8 SOP	
LM393N LM393AN	8 DIP	0 ~ + 75°C
LM393S	9 SIP	
LM393D LM393AD	8 SOP	
LM2903N	8 DIP	- 40 ~ + 85°C
LM2903D	8 SOP	

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## SCHEMATIC DIAGRAM



## ABSOLUTE MAXIMUM RATINGS

Characteristic	Symbol	Value	Unit
Power Supply Voltage	$V_s$	$\pm 18$ or 36	V
Differential Input Voltage	$V_{ID}$	36	V
Input Voltage	$V_I$	-0.3 to +36	V
Output Short Circuit to GND		Continuous	
Power Dissipation	$P_D$	570	mW
Operating Temperature	$T_{opr}$	-25 ~ +85	°C
LM293/LM293A		0 ~ +70	°C
LM393/LM393A		-40 ~ +85	°C
Storage Temperature	$T_{stg}$	-65 ~ +150	°C



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ELECTRICAL CHARACTERISTICS ( $V_{CC} = 5V$ ,  $T_a = 25^\circ C$ , unless otherwise specified)

Characteristic	Symbol	Test Conditions	LM293A/LM393A			LM293/LM393		Unit	
			Min	Typ	Max	Min	Typ		Max
Input Offset Voltage	$V_{IO}$	$V_{CM} = 0V$ to $V_{CC} - 1.5V$ $V_o = 1.4V$ , $R_s = 0$ $T_{amin} \leq T_a \leq T_{amax}$		$\pm 1$	$\pm 2$		$\pm 1$	$\pm 5$	mV
								$\pm 9.0$	
Input Offset Current	$I_{IO}$	$T_{amin} \leq T_a \leq T_{amax}$		$\pm 5$	$\pm 50$		$\pm 5$	$\pm 50$	nA
					$\pm 150$			$\pm 150$	
Input Bias Current	$I_B$	$T_{amin} \leq T_a \leq T_{amax}$		25	250		25	250	nA
					400			400	
Input Common Mode Voltage Range	$V_{ICR}$	$T_{amin} \leq T_a \leq T_{amax}$		0	$V_{CC} - 1.5$	0	$V_{CC} - 1.5$		V
				0	$V_{CC} - 2$	0	$V_{CC} - 2$		
Supply Current	$I_{CC}$	$R_L = \infty$ $R_L = \infty$ $V_{CC} = 30V$		0.4	1		0.4	1	mA
					1	2.5		1	
Voltage Gain	$A_V$	$V_{CC} = 15V$ , $R_L \geq 15K\Omega$ (for large $V_o$ swing)	50	200		50	200	V/mV	
Large Signal Response Time	$t_{RES1}$	$V_{IN} =$ TTL Logic Swing $V_{ref} = 1.4V$ , $V_{RL} = 5V$ , $R_L = 5.1K\Omega$		300		300		nS	
Response Time	$t_{RES2}$	$V_{RL} = 5V$ , $R_L = 5.1K\Omega$		1.3		1.3		$\mu S$	
Output Sink Current	$I_{sink}$	$V_{IN^-} \geq 1V$ , $V_{IN^+} = 0V$ , $V_o \leq 1.5V$	6	16		6	16	mA	
Output Saturation Voltage	$V_{sat}$	$V_{IN^-} \geq 1V$ , $V_{IN^+} = 0V$ $I_{sink} = 4mA$ $T_{amin} \leq T_a \leq T_{amax}$		250	400		250	400	mV
					700			700	
Output Leakage Current	$I_{leak}$	$V_{IN^-} = 0$ , $V_{IN^+} = 1V$		0.1			0.1		nA
					1.0			1.0	$\mu A$

\*  $T_{amin} \leq T_a \leq T_{amax}$ LM293/LM293A:  $T_{amin} = -25^\circ C$ ,  $T_{amax} = +85^\circ C$ LM393/LM393A:  $T_{amin} = 0^\circ C$ ,  $T_{amax} = +70^\circ C$ LM2903:  $T_{amin} = -45^\circ C$ ,  $T_{amax} = +85^\circ C$ 

## LM293/A, LM393/A, LM2903

## LINEAR INTEGRATED CIRCUIT

ELECTRICAL CHARACTERISTICS ( $V_{CC}=5V$ ,  $T_a=25^\circ C$ , unless otherwise specified)

Characteristic	Symbol	Test Conditions	LM2903			Unit
			Min	Typ	Max	
Input Offset Voltage	$V_{IO}$	$V_{CM} = 0V$ to $V_{CC} - 1.5V$ $V_o = 1.4V$ , $R_o = 0$ $T_{amin} \leq T_a \leq T_{amax}$		$\pm 2$	$\pm 7$	mV
				$\pm 9$	$\pm 15$	
Input Offset Current	$I_{IO}$	$T_{amin} \leq T_a \leq T_{amax}$		$\pm 5$	$\pm 50$	nA
				$\pm 50$	$\pm 200$	
Input Bias Current	$I_B$	$T_{amin} \leq T_a \leq T_{amax}$		25	250	nA
					500	
Input Common Mode Voltage Range	$V_{ICR}$	$T_{amin} \leq T_a \leq T_{amax}$	0		$V_{CC} - 1.5$	V
			0		$V_{CC} - 2$	
Supply Current	$I_{CC}$	$R_L = \infty$ $R_L = \infty$ $V_{CC} = 30V$		0.4	1	mA
				1	2.5	
Voltage Gain	$A_V$	$V_{CC} = 15V$ , $R_L \geq 15K\Omega$ (for large $V_o$ swing)	25	100		V/mV
Large Signal Response Time	$t_{RES1}$	$V_{IN} = \text{TTL Logic Swing}$ $V_{ref} = 1.4V$ , $V_{RL} = 5V$ , $R_L = 5.1K\Omega$		300		nS
Response Time	$t_{RES2}$	$V_{RL} = 5V$ , $R_L = 5.1K\Omega$		1.5		$\mu S$
Output Sink Current	$I_{sink}$	$V_{IN}^- \geq 1V$ , $V_{IN}^+ = 0V$ , $V_o \leq 1.5V$	6	16		mA
Output Saturation Voltage	$V_{sat}$	$V_{IN}^- \geq 1V$ , $V_{IN}^+ = 0V$ $I_{sink} = 4mA$ $T_{amin} \leq T_a \leq T_{amax}$		250	400	mV
					700	
Output Leakage Current	$I_{leak}$	$V_{IN}^- = 0$ , $V_{IN}^+ = 1V$ $V_o = 5V$ $V_o = 30V$		0.1		nA
					1.0	$\mu A$

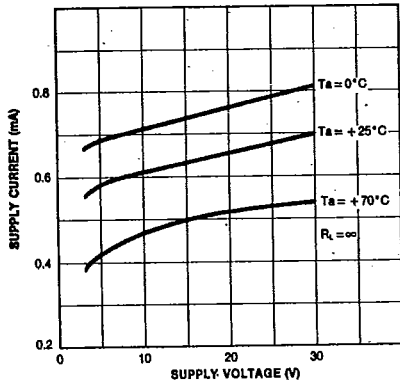
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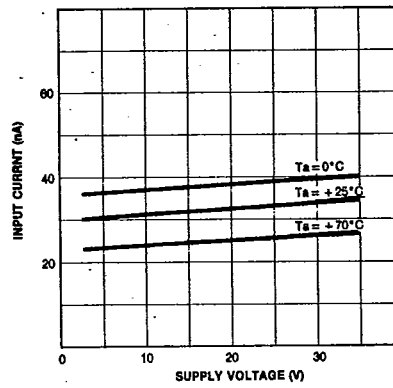
LINEAR INTEGRATED CIRCUIT

TYPICAL PERFORMANCE CHARACTERISTICS

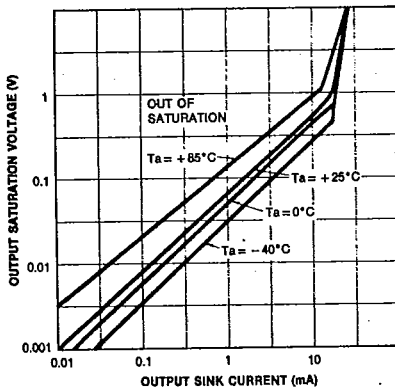
SUPPLY CURRENT  $I_s$  SUPPLY VOLTAGE



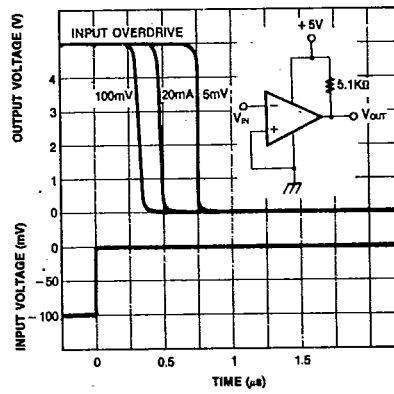
INPUT CURRENT  $I_i$  SUPPLY VOLTAGE



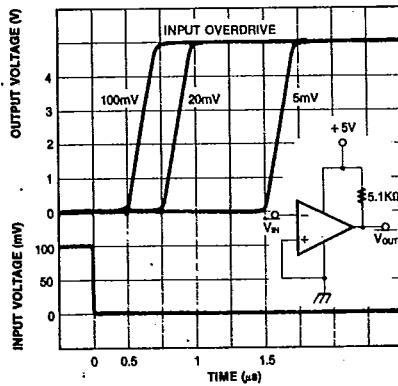
OUTPUT SATURATION VOLTAGE



RESPONSE TIME



RESPONSE TIME



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## LINEAR INTEGRATED CIRCUIT

## APPLICATION INFORMATION

The LM293 series are high gain, wide bandwidth devices which, like most comparators, can easily oscillate if the output is inadvertently allowed to capacitively couple to the inputs via stray capacitance. That occurs during the output voltage transitions, when the comparator changes state.

To minimize this problem, PC board layout should be designed to reduce stray input-output coupling, reducing the input resistors to less than 10K $\Omega$  reduces the feedback signal levels and finally, adding even a small amount (1 to 10mV) of positive feedback (hysteresis) causes such a rapid transition that oscillations due to stray feedback are not possible.

It is good design practice to ground all unused pins.

The differential input voltage may be larger than positive supply without damaging the device. Note that voltages more negative than -0.3V should not be used: an input clamping diode can be used as protection.

The output of the LM293 series is the uncommitted collector of a NPN transistor with grounded emitter. This allows the device to be used like any open-collector gate providing the OR-wide facility.

The output sink current capability is approximately 16mA; if this limit is exceeded, the output transistor will come out of saturation and the output voltage will rise very rapidly.

Under this limit, the output saturation voltage is limited by the approximately 60 $\Omega$   $r_{sat}$  of the output transistor.

TYPICAL APPLICATIONS ( $V_{CC} = +15V$ )

Basic comparator

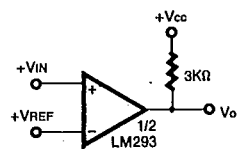


Fig. 3

Non-inverting comparator with Hysteresis

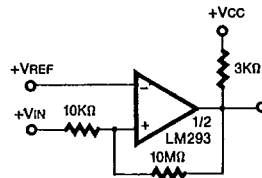


Fig. 4

Inverting comparator with Hysteresis

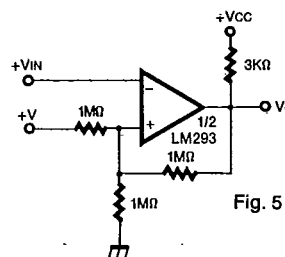


Fig. 5

Driving C-MOS

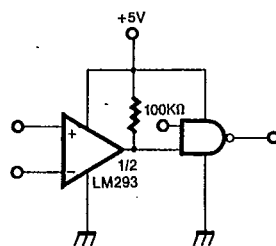


Fig. 6

Driving TTL

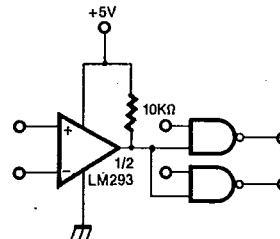


Fig. 7



**LM293/A, LM393/A, LM2903**      **LINEAR INTEGRATED CIRCUIT**

**APPLICATION INFORMATION** (continued).

AND gate

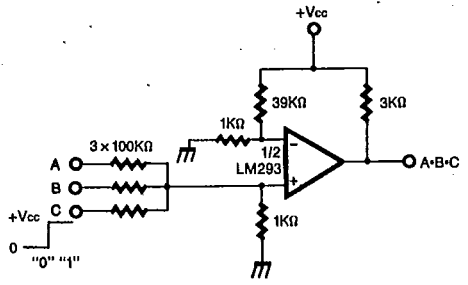


Fig. 8

OR gate

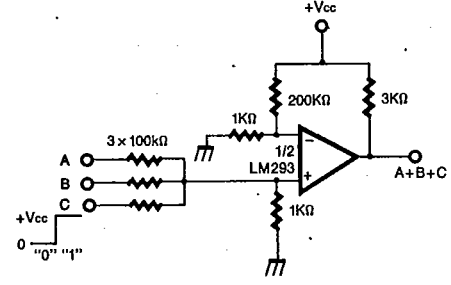


Fig. 9

Large fan-in AND gate

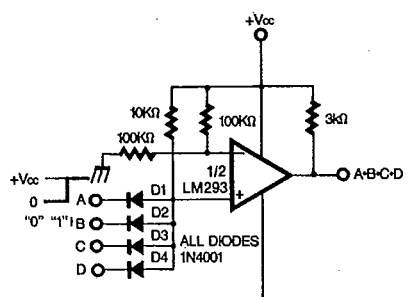


Fig. 10

Squarewave oscillator

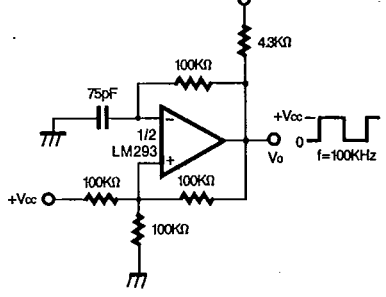


Fig. 11

Pulse generator

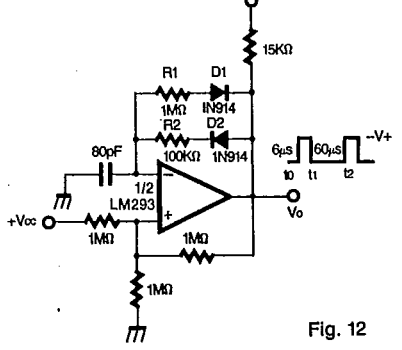


Fig. 12

One-shot multivibrator

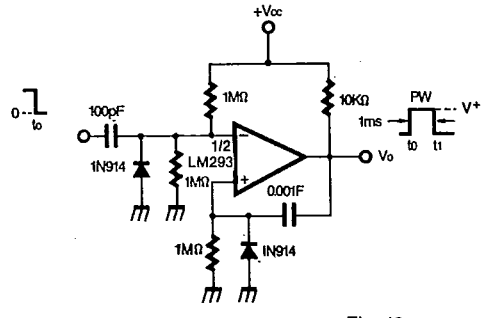


Fig. 13