

54AC/74AC1017 • 54ACT/74ACT1017

16 × 16 Parallel Multiplier

Description

The 'AC/ACT1017 is a high-speed, low power 16 × 16-bit parallel multiplier that is ideally suited for real-time digital signal processing applications. Fabricated using advanced FACT technology, the 'AC/ACT1017 offers a very low power alternative and exceptional performance.

The 'AC/ACT1017 is a pin and functional replacement for AMD's Am29517; the 'AC/ACT1017 operates from a single VCC supply and is compatible with standard TTL logic levels.

The 'AC/ACT1017 performs the same mathematical functions as the 'AC/ACT1016 but has a single clock, CLK, and three register enables making it ideal for microcoded applications.

The architecture of the 'AC/ACT1017 features one 16-bit port dedicated to the X input registers (enabled by \overline{ENX}), one 16-bit I/O port used to load the Y input registers (controlled by \overline{ENY}) and for displaying the Least Significant Product (LSP), and one 16-bit output port multiplexed between displaying the Least Significant Product (LSP) and the Most Significant Product (MSP).

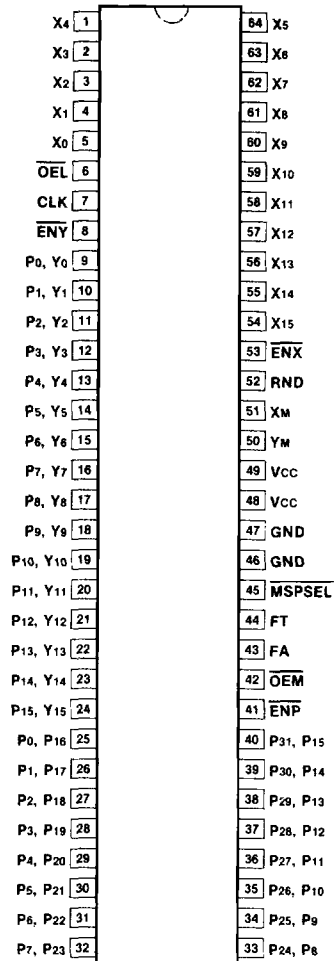
The I/O port direction is controlled by \overline{OEL} and the output port 3-state control is controlled by \overline{OEM} .

The result is registered if FT is LOW (all 32 register bits enabled by \overline{ENP}) and unregistered if FT is held HIGH.

Twos complement, unsigned magnitude and mixed mode multiplications are possible through the twos complement X and Y mode controls, X_M and Y_M , respectively. These mode controls are registered, controlled by the input clock (CLK).

Result rounding is controlled by the registered RND signal (controlled by CLK). Selection of one of the two rounding modes is determined by the FA signal.

Connection Diagram



Pin Assignment
for DIP

AC1017 • ACT1017

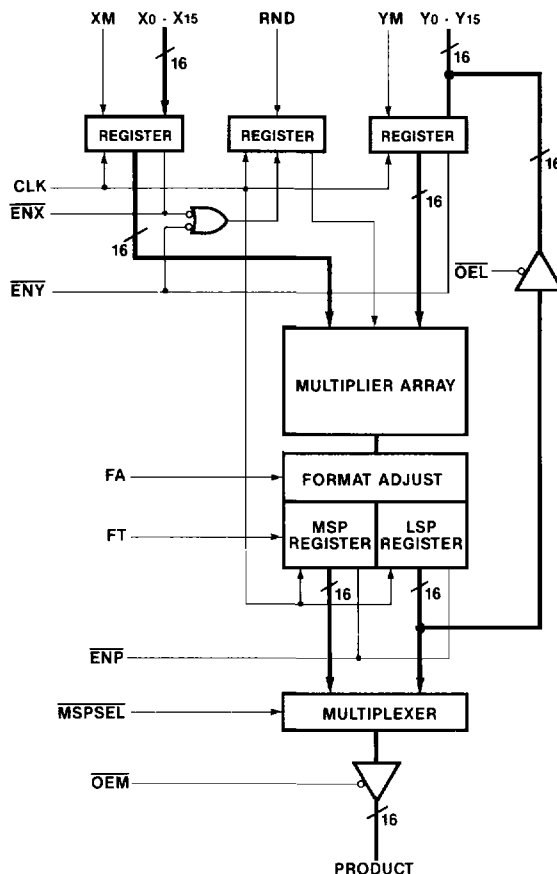
- 16 × 16 Parallel Multiplier
- Selectable Rounding Modes
- Twos Complement, Unsigned Magnitude and Mixed Mode Multiplication
- Pin and Functionally Compatible with Am29517
- ACT1017 Interfaces Directly to TTL
- AC1017 Provides Low Voltage, High-Speed Operation
- Single Vcc Supply
- ± 2000 V ESD Protection
- High Drive 8 mA Outputs
- Single Input Clock

Pin Names

X15 - X0	Multiplicand Data Inputs
Y15 - Y0	Multiplier Data Inputs
CLK	Input Clock
\overline{ENX}	Register Enable, X15 - X0
\overline{ENY}	Register Enable, Y15 - Y0
\overline{ENP}	Register Enable, MSP and LSP
X _M , Y _M	Mode Control Inputs
FA	Format Adjust Control
FT	Format Transparent Control
\overline{OEL}	3-State Enable, LSP Routing
\overline{OEM}	3-State Enable, MSP Routing
RND	Round Control, MSP
\overline{MSPSEL}	MSP Select
P31 - P16	MSP Outputs
P15 - P0	LSP Outputs

Ordering Code: See Section 6

Logic Diagram



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.