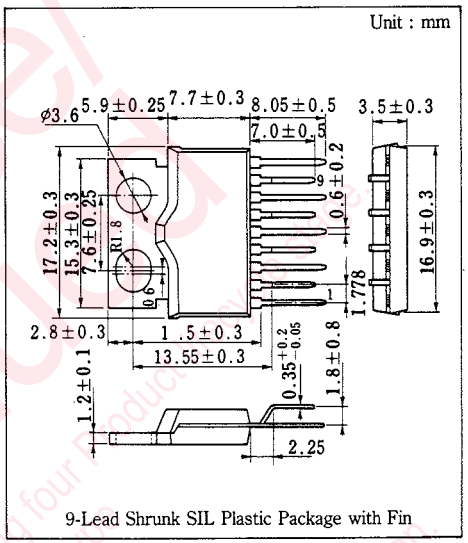
AN5530K

TV Vertical Deflection Output Circuit

■ Outline

The AN5530K is an integrated circuit designed for TV vertical deflection output circuit. Combining with the deflection signal processing IC can facilitate the vertical output circuit design.



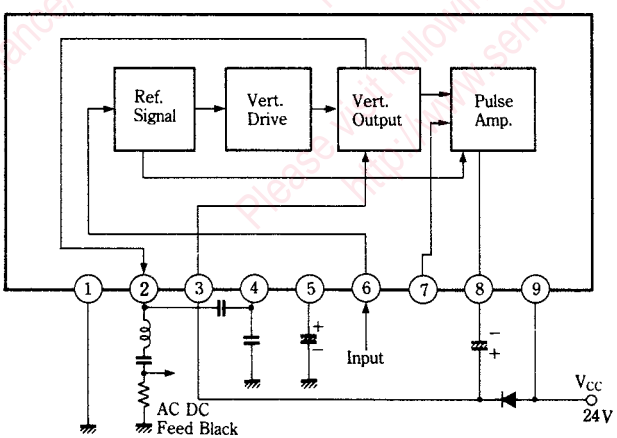
■ Features

- Low power consumption, direct deflection coil driving capability (Flyback voltage two times as high as supply voltage is supplied during flyback period only)
- Internal comparator

■ Pin

Pin No.	Pin Name
1	GND
2	Output
3	Supply Voltage for Output
4	Filter
5	Ripple Filter
6	Input
7	Trigger Pulse Input
8	Pulse Amp. Output
9	V_{CC}

■ Block Diagram



■ Absolute Maximum Ratings (Ta=25℃)

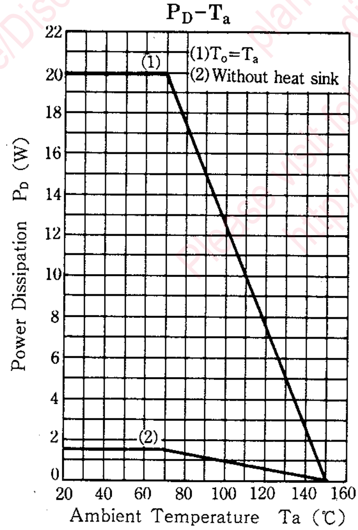
Item		Symbol	Rating		Unit
Voltage	Supply Voltage	V_{CC}	30		V
	Circuit Voltage	$V_{2\text{-}1}$	0	60	V
		$V_{3\text{-}1}$	0	60	V
		$V_{7\text{-}1}$	−1	3	V
Current	Supply Current	I_{CC}	360		mA
	Circuit Current	I_2	−1800	1800	$mA_{O\text{-}P}$
		I_8	−1800	1800	$mA_{O\text{-}P}$
Power Dissipation (T_a=70℃)		P_D	8		W
Temperature	Operating Ambient Temperature	T_{opr}	−20~+70		℃
	Storage Temperature	T_{stg}	−55~+150		℃

■ Electrical Characteristics (Ta=25℃)

Item	Symbol	Test Circuit	Condition	min.	typ.	max.	Unit
Deflection Current	$I_{y(P\text{-}P)}$	1		1700	1800	1900	$mA_{p\text{-}p}$
Deflection Current Linearity	$\Delta I_{y(+)}$	1		59		175	$mA_{p\text{-}p}$
Deflection Current Linearity	$\Delta I_{y(-)}$	1		54		162	$mA_{p\text{-}p}$
Deflection Current Change with Ambient Temperature*	$\Delta I_y/T_a$	1	T_a=−20~+70°C	−1.5		1.5	%
Center Voltage	V_{MID}	1	$I_{y(p\text{-}p)}$=1.7A	14.0	14.6	15.2	V
Flyback Pulse Amplitude	$V_{(FBP)}$	1	$I_{y(p\text{-}p)}$=1.7A	47			V
Static Circuit Current	I_{CQ}		$V_{3\text{-}1}$=24V, $V_{9\text{-}1}$=24V, $V_{7\text{-}1}$=0; ②–2.4kΩ–⑥, 2.4kΩ to GND	8	14	24	mA
Output Tr Saturation Voltage	$V_{3\text{-}2}$		$V_{3\text{-}1}$=$V_{9\text{-}1}$=24V, Pin②-③=33Ω, $V_{6\text{-}1}$=3V, $V_{7\text{-}1}$=0		3.0	4.0	V
Output Tr Saturation Voltage	$V_{2\text{-}1}$		$V_{3\text{-}1}$=$V_{9\text{-}1}$=24V, Pin②-①=33Ω, $V_{6\text{-}1}$=3V, $V_{7\text{-}1}$=0		1	2.0	V
Q_{21} Saturation Voltage	$V_{8\text{-}1}$		$V_{9\text{-}1}$=24V, Pin⑧-⑨=1.2kΩ, $V_{7\text{-}1}$=0, $V_{6\text{-}1}$=6.9V			0.5	V
Thermal Resistance	$R_{th(j\text{-}c)}$					4	℃/W

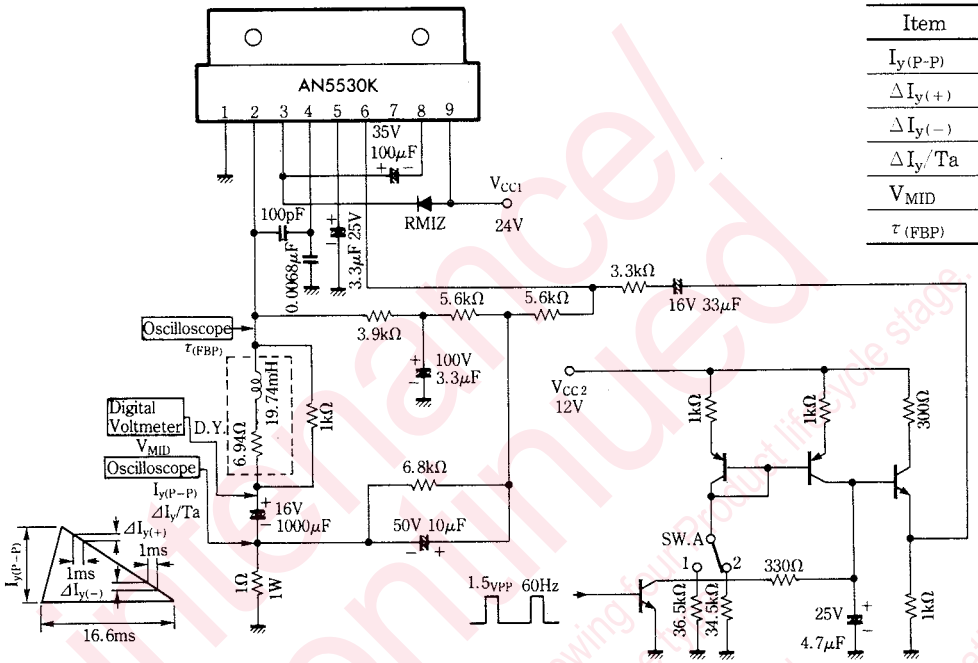
* Design reference value

$P_D - T_a$

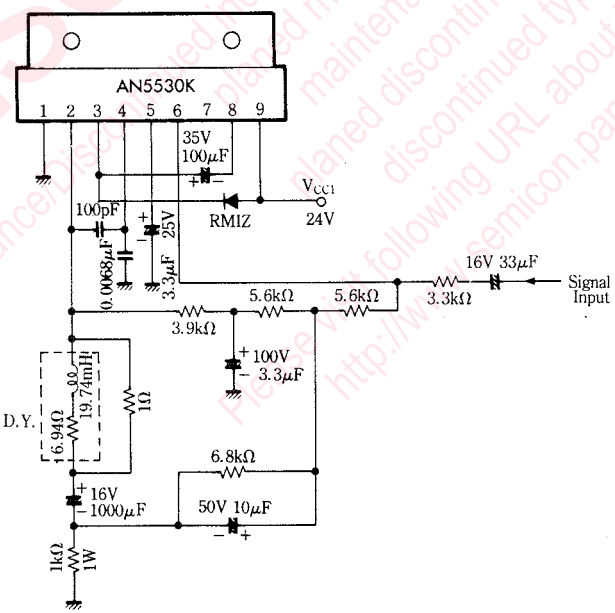


Test Circuit 1

Item	SW-A
$I_{y(P-P)}$	②
$\Delta I_{y(+)}$	①
$\Delta I_{y(-)}$	①
$\Delta I_y/Ta$	①
V_{MID}	①
$\tau_{(FBP)}$	①



■ Application Circuit



Request for your special attention and precautions in using the technical information and semiconductors described in this book

(1) If any of the products or technical information described in this book is to be exported or provided to non-residents, the laws and regulations of the exporting country, especially, those with regard to security export control, must be observed.

(2) The technical information described in this book is intended only to show the main characteristics and application circuit examples of the products. No license is granted in and to any intellectual property right or other right owned by Panasonic Corporation or any other company. Therefore, no responsibility is assumed by our company as to the infringement upon any such right owned by any other company which may arise as a result of the use of technical information described in this book.

(3) The products described in this book are intended to be used for standard applications or general electronic equipment (such as office equipment, communications equipment, measuring instruments and household appliances).
Consult our sales staff in advance for information on the following applications:
- Special applications (such as for airplanes, aerospace, automobiles, traffic control equipment, combustion equipment, life support systems and safety devices) in which exceptional quality and reliability are required, or if the failure or malfunction of the products may directly jeopardize life or harm the human body.
- Any applications other than the standard applications intended.

(4) The products and product specifications described in this book are subject to change without notice for modification and/or improvement. At the final stage of your design, purchasing, or use of the products, therefore, ask for the most up-to-date Product Standards in advance to make sure that the latest specifications satisfy your requirements.

(5) When designing your equipment, comply with the range of absolute maximum rating and the guaranteed operating conditions (operating power supply voltage and operating environment etc.). Especially, please be careful not to exceed the range of absolute maximum rating on the transient state, such as power-on, power-off and mode-switching. Otherwise, we will not be liable for any defect which may arise later in your equipment.
 Even when the products are used within the guaranteed values, take into the consideration of incidence of break down and failure mode, possible to occur to semiconductor products. Measures on the systems such as redundant design, arresting the spread of fire or preventing glitch are recommended in order to prevent physical injury, fire, social damages, for example, by using the products.

(6) Comply with the instructions for use in order to prevent breakdown and characteristics change due to external factors (ESD, EOS, thermal stress and mechanical stress) at the time of handling, mounting or at customer's process. When using products for which damp-proof packing is required, satisfy the conditions, such as shelf life and the elapsed time since first opening the packages.

20080805