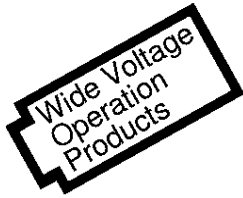


SED1336F_{0A}

Graphic LCD/TV Controller



- For Medium-scale LCD
- Simplified External Circuits
- Virtual Screen Display RAM
- Enhanced Control Function

■ DESCRIPTION

The SED1336F_{0A} is a controller for displaying such things as characters and figures on TV monitors and dot matrix liquid crystal displays and it has functions that are software compatible with the SED1330F series.

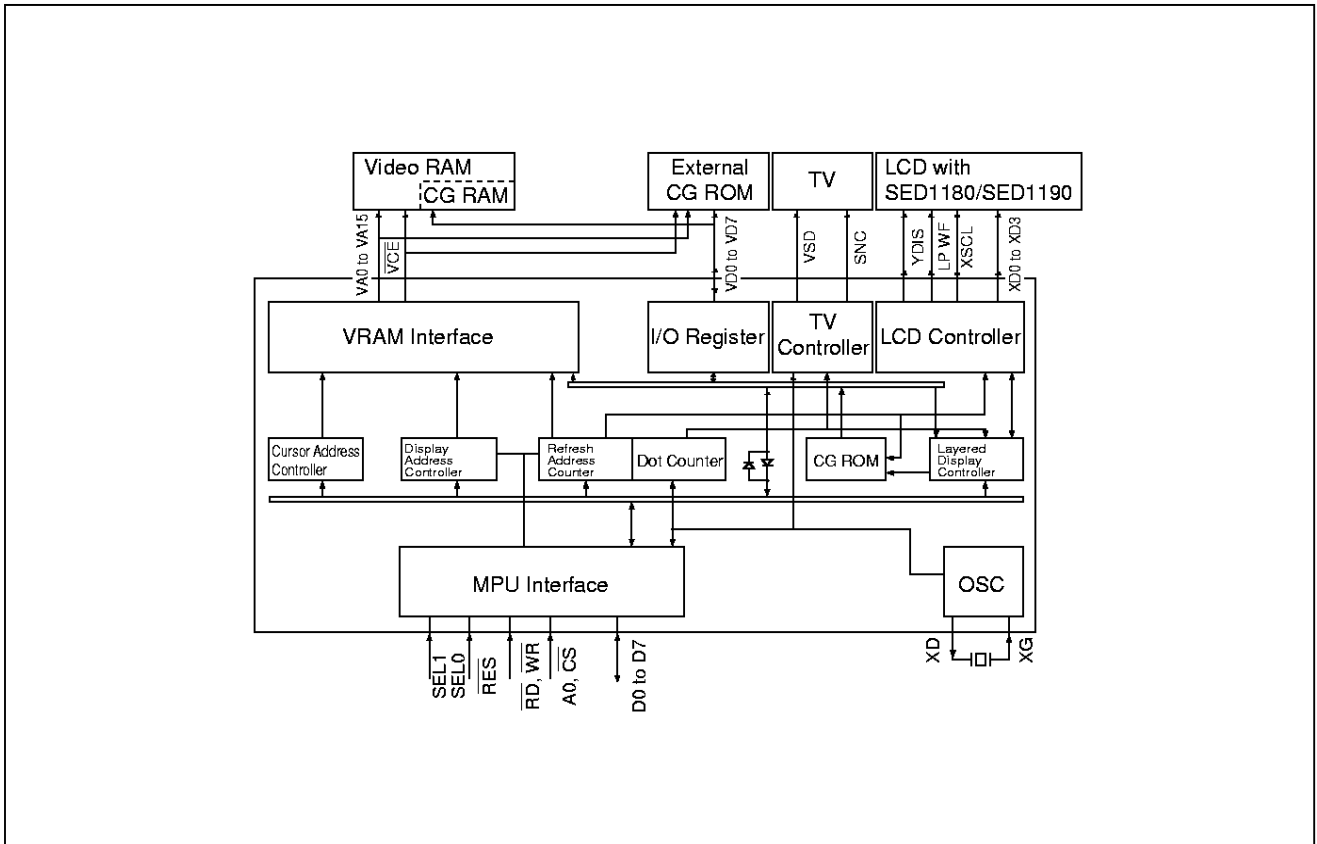
The built-in TV control circuit generates an NTSC and PAL system synchronous signal and can display the same screen as an LCD display on a TV as well.

In addition, it has built-in a simple MPU interface using a data bus, a VRAM timing generation circuit, an LCD module control circuit and a high speed character generator, permitting the configuration of a display control block with little externally installed circuitry.

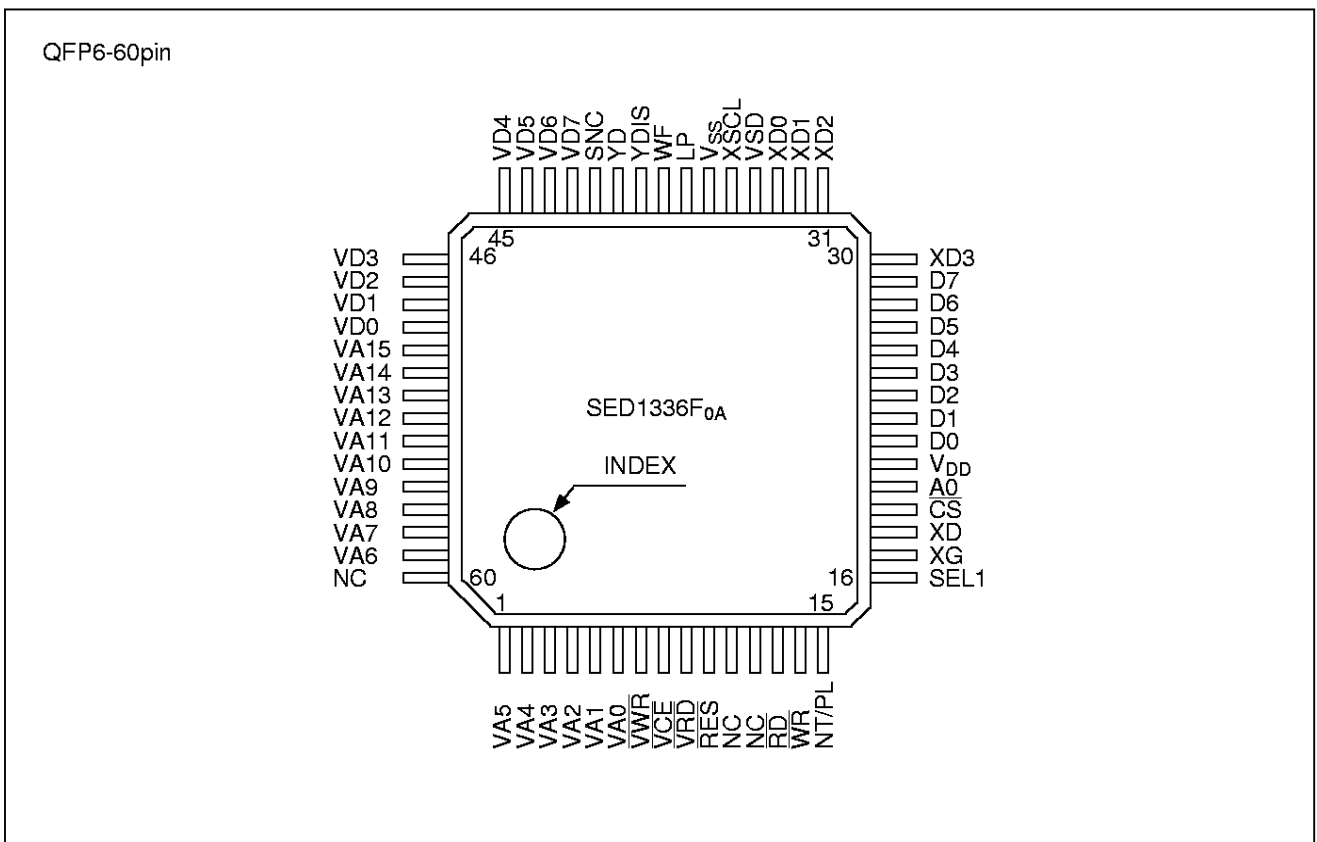
■ FEATURES

- 6800 and 8080 family compatibility, 2-pin programmable
- Display size LCD: 640×256 dots (Max)
TV: 256×200 dots (Fixed)
- TV mode NTSC/PAL
- Multimode display Up to 2 layers of mixed character and graphics
Up to 3 layers of graphics
- Selectable display synthesis Display attributes (reverse video, flashing, etc.)
for selected areas of the display
Simple animation
- Supports 64K bytes of memory 4K bytes of user definable characters
60K bytes of display memory
- 160 JIS 5×7 pixel characters internal
- Supports external character ROM or RAM 8×8 or 8×16 pixel characters
Allows mixing of ROM RAM character sets
- Variable LCD duty cycle, from 1/2 to 1/256
- Power supply 3.0 to 5.5V
- Package QFP6-60pin (plastic)

■ BLOCK DIAGRAM



■ PIN CONFIGURATION



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Electric Device Information of EPSON WWW server

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