

P-CHANNEL JFETs



TO-92/TO-226AA

ELECTRICAL CHARACTERISTICS at $T_A = 25^\circ\text{C}$

Device Type	V _{(BR)GSS}		I _{GSS}		V _{GS(off)}				I _{DSS}			g _{fs}			C _{iss} ¹		C _{rss} ¹		r _{DS}	Pin- ning 1, 2, 3
					Limits		Conditions													
	Min.	@ I _G	Max.	@ V _{DS}	Min.	Max.	V _{DS}	I _D	Min.	Max.	@ V _{DS}	Min.	Max.	@ V _{DS}	Max.	@ V _{DS}	Max.	@ V _{DS}	(Ω)	
(V)	(μA)	(nA)	(V)	(V)	(V)	(V)	(nA)	(mA)	(mA)	(V)	(mS)	(mS)	(V)	(pF)	(V)	(pF)	(V)			
2N3820	20	10	20	10	—	8.0	-10	-10 ²	-0.3	-15	-10	0.8	5.0	-10	32	-10	16	-10	—	DGS†
TP3993	25	1.0	1.0	15	4.0	9.5	-10	-1 ²	-10	—	-10	6.0	12	-10	16	-10	5.5	10 ³	150	DSG‡
TP3994	25	1.0	1.0	15	1.0	5.5	-10	-1 ²	-2.0	—	-10	4.0	10	-10	16	-10	5.5	10 ³	300	DSG‡
TP4381	25	1.0	1.0	15	1.0	5.0	-15	-1.0 ²	-3.0	-12	-15	2.0	6.0	-15	20	-15	5.0	-15	—	DSG‡
2N5460	40	10	5.0	20	0.75	6.0	-15	-1.0	-1.0	-5.0	-15	1.0	5.0	-15	7.0	-15	3.0	-15	—	DSG‡
2N5461	40	10	5.0	20	1.0	7.5	-15	-1.0	-2.0	-9.0	-15	1.5	5.5	-15	7.0	-15	3.0	-15	—	DSG‡
2N5462	40	10	5.0	20	1.8	9.0	-15	-1.0	-4.0	-16	-15	2.0	6.0	-15	7.0	-15	3.0	-15	—	DSG‡
J174	30	1.0	1.0	20	5.0	10	-15	-10	-20	-135	-15	—	—	—	—	—	—	—	85	DSG†
J175	30	1.0	1.0	20	3.0	6.0	-15	-10	-7.0	-70	-15	—	—	—	—	—	—	—	125	DSG†
J176	30	1.0	1.0	20	1.0	4.0	-15	-10	-2.0	-35	-15	—	—	—	—	—	—	—	250	DSG†
J177	30	1.0	1.0	20	0.8	2.25	-15	-10	-1.5	-20	-15	—	—	—	—	—	—	—	300	DSG†
TPU304	30	1.0	1.0	20	5.0	10	-15	-1 ²	-30	-90	-15	—	—	—	27	-15	7.0	12 ³	85	DSG‡
TPU305	30	1.0	1.0	20	3.0	6.0	-15	-1 ²	-15	-60	-15	—	—	—	27	-15	7.0	7.0 ³	110	DSG‡
TPU306	30	1.0	1.0	20	1.0	4.0	-15	-1 ²	-5.0	-25	-15	—	—	—	27	-15	7	5.0 ³	175	DSG‡

NOTES: † Reversed pinning (S-G-D) available on special order—add suffix letter 'R' to part number.

‡ Reversed pinning (S-D-G) available on special order—add suffix letter 'R' to part number.

1) $V_{GS} = 0\text{ V}$.

2) I_D in μA .

3) $V_{DS} = 0\text{ V}$, V_{GS} in volts.

4) $V_{GS} = 1.0\text{ V}$.