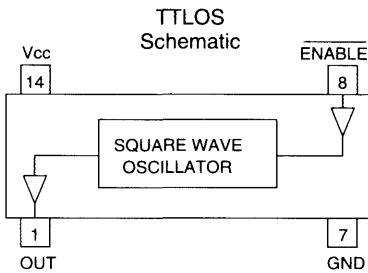
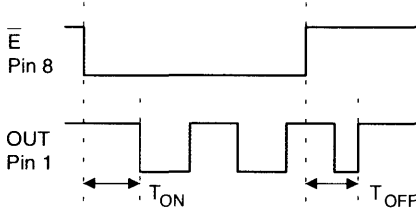


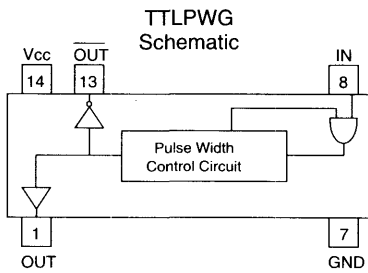
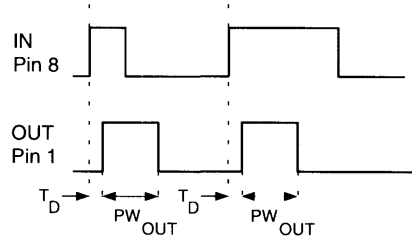
TTL Gated Oscillators

These gated oscillators permit synchronization of the output square wave with the high-to-low transition of the enable input. When the enable is high, the output is held high. The output will start with a high-to-low transition one half-cycle after the input trigger. The output frequency tolerance is $\pm 3\%$.



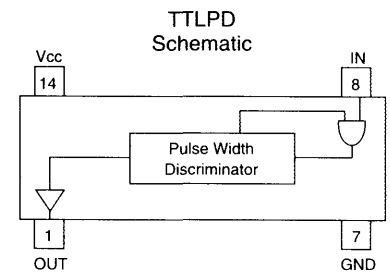
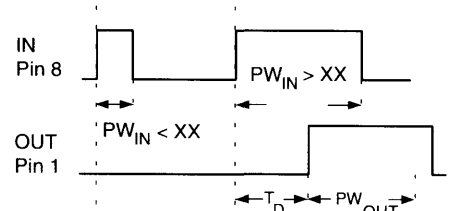
TTL Pulse Width Generator Modules

Triggered by the input's rising edge (input pulse width 10 ns, min.), a pulse of specified width will be generated at the output with a propagation delay of 5 ± 2 ns (7 ± 2 ns, for inverted output). High-to-low transitions will not trigger the unit. Designed for output duty-cycle less than 50%.



TTL Pulse Width Discriminators

Input pulse widths greater than the Nominal value (XX in ns from P/N TTLPD-XX) of the module, will propagate with delay ($XX + 5$ ns) $\pm 5\%$ or 2 ns, whichever is greater. Output pulse width equals input width $\pm 7\%$ or 4 ns, whichever is greater. Input pulse widths less than Nominal value are suppressed.



Electrical Specifications at 25°C

TTL Gated Oscillators	
Part Number	Output Frequency
TTLOS-5	5 MHz
TTLOS-10	10 MHz
TTLOS-15	15 MHz
TTLOS-20	20 MHz
TTLOS-25	25 MHz
TTLOS-30	30 MHz
TTLOS-33	33 MHz
TTLOS-35	35 MHz
TTLOS-40	40 MHz
TTLOS-45	45 MHz
TTLOS-50	50 MHz
TTLOS-66	66 MHz
TTLOS-75	75 MHz
TTLOS-80	80 MHz

Electrical Specifications at 25°C

TTL Buffered Pulse Width Generator Modules		
Part Number	Output Pulse Width (ns)	Maximum Freq. (MHz)
TTLPWG-5	5 ± 1.0	63
TTLPWG-7	7 ± 1.0	53
TTLPWG-10	10 ± 1.5	42
TTLPWG-15	15 ± 2.0	32
TTLPWG-20	20 ± 2.0	22
TTLPWG-25	25 ± 2.0	19
TTLPWG-30	30 ± 2.0	15
TTLPWG-35	35 ± 2.0	13
TTLPWG-40	40 ± 2.0	11
TTLPWG-45	45 ± 2.25	10
TTLPWG-50	50 ± 2.5	9
TTLPWG-60	60 ± 3.0	8
TTLPWG-80	80 ± 4.0	6
TTLPWG-100	100 ± 5.0	5

Electrical Specifications at 25°C

TTL Pulse Width Discriminator Modules		
Part Number	Suppressed Pulse Width, Max. (ns)	Passed Pulse Width, Min. (ns)
TTLPD-10	< 8.5	> 11.5
TTLPD-15	< 13.5	> 16.5
TTLPD-20	< 18.5	> 21.5
TTLPD-25	< 23.5	> 26.5
TTLPD-30	< 28.5	> 31.5
TTLPD-40	< 38.0	> 42.0
TTLPD-50	< 47.5	> 52.5
TTLPD-60	< 57.0	> 63.0
TTLPD-75	< 71.0	> 79.0
TTLPD-100	< 95.0	> 105.0
TTLPD-120	< 114.0	> 126.0
TTLPD-125	< 118.7	> 131.3
TTLPD-150	< 142.5	> 157.5
TTLPD-200	< 190.0	> 210.0

OPERATING SPECIFICATIONS

- V_{CC} Supply Voltage 5.00 \pm 0.25 VDC
- Supply Current, I_{CC}
 - TTLPWG 35 mA typ., 55 mA max.
 - TTLPD 42 mA typ., 60 mA max.
 - TTLOS 15 mA typ., 30 mA max.
- Logic "1" Input: V_{IH} 2.00 V min., 5.50 V max.
- Logic "0" Input: V_{IL} 0.80 V max.
- V_{OH} Logic "1" Voltage Out 2.40 V min.
- V_{OL} Logic "0" Voltage Out 0.50 V max.
- Operating Temperature Range 0° to 70°C
- Storage Temperature Range -65° to +150°C

MIL-GRADE: Add "M" suffix. Integrated circuits screened to MIL-STD-883B with -55 to +125°C operating temperature range. These devices have a package height of .335"

Dimensions in Inches (mm)

14-Pin Package with Unused Leads Removed Per Schematic

