

# Microcomputer Peripheral LSIs

## Microcomputer Peripheral LSIs

Category	Type No.	Process	Functions	Supply Voltage (V)	Package	
						No.
FLP driver	MN12510	CMOS	FLP drive control, Key scan control	--30, +5	SDIP042-P-0600 QFP044-P-1010	L22 L49
LCD driver	MN1256	CMOS	8 digits 7 segments, FEM type liquid crystal display	--5, --2.5	QFP040-P-1010	L45
	MN1258		180 segments driver for dot matrix LCD panel	+5, --20	Chip	--
	MN1259		16 common driver-controller for dot matrix LCD panel	+5	Chip	--
LCD/CRT interface	MN5502	CMOS	Single chip LCD/PDP/CRT control LSI. Max. 720 × 512 dot.	+5	QFP124-P-2828	L75
Voltage detector	MN1380	CMOS	Voltage detection (CMOS output type)	+1.5 ~ +6.0	M type	L26
	MN13801		Voltage detection (N-ch open drain type)	+1.5 ~ +6.0	M type	L26
	MN13802		Voltage detection (CMOS output inversion type)	+1.5 ~ +6.0	M type	L26
	MN1381		Voltage detection (CMOS output type)	+1.5 ~ +6.0	TO-92 type	L28
	MN13811		Voltage detection (N-ch open drain type)	+1.5 ~ +6.0	TO-92 type	L28
	MN13812		Voltage detection (CMOS output inversion type)	+1.5 ~ +6.0	TO-92 type	L28
	MN1382		Voltage detection (CMOS output type)	+1.5 ~ +6.0	Mini 3P	L27
	MN13821		Voltage detection (N-ch open drain type)	+1.5 ~ +6.0	Mini 3P	L27
	MN13822		Voltage detection (CMOS output inversion type)	+1.5 ~ +6.0	Mini 3P	L27
Clock (Calendar)	MN871501	CMOS	Real time clock (RTC)	+2.7 ~ +5.5	SSOP020-P-0225	L36

## 16-Bit Microprocessor Peripheral LSIs

Category	Type No.	Process	Functions	Package	
					No.
16-bit peripheral	MN12861	CMOS	Clock generator, Timer/Counter, Programmable I/O, Interrupt controller	QFP100-P-1818	L71
	MN12862		Clock generator, Timer/Counter, DMAC, Interrupt controller	QFP100-P-1818	L71
	MN5501		Timer/Counter × 2, Interrupt controller × 2, USART	QFP100-P-1818	L71

(Package Symbol) DIP = Dual-In-Line Plastic Package, SDIP = Shrunk Dual-In-Line Plastic Package, QFP = Quad Flat Package,  
 SO = Small Outline (PANAFAT PACKAGE),  
 SSOP = Shrunk Small Outline L-Leaded Package