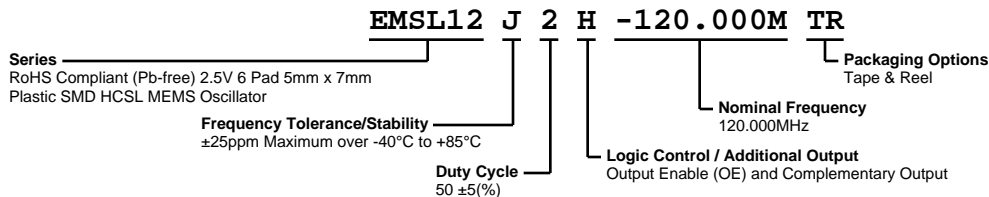


# EMSL12J2H-120.000M TR



## ELECTRICAL SPECIFICATIONS

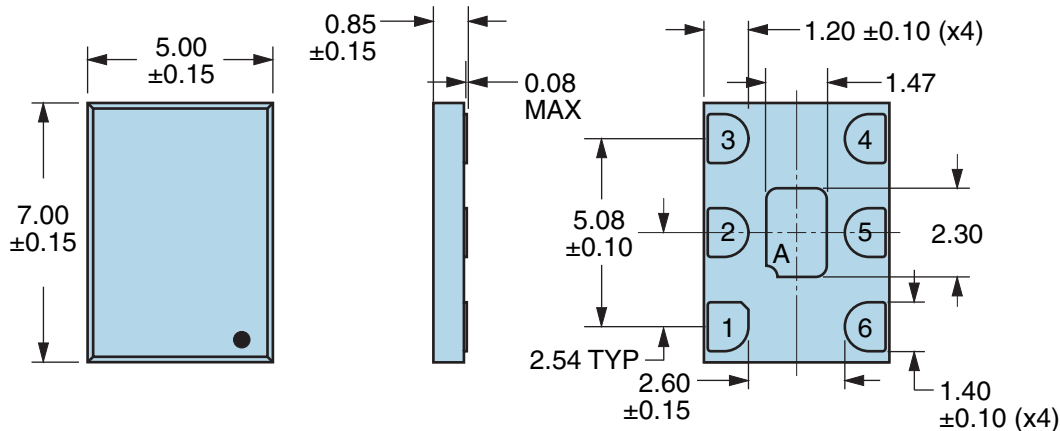
<b>Nominal Frequency</b>	120.000MHz
<b>Frequency Tolerance/Stability</b>	$\pm 25$ ppm Maximum over $-40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ (Inclusive of all conditions: Calibration Tolerance at $25^{\circ}\text{C}$ , Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, 1st Year Aging at $25^{\circ}\text{C}$ , Reflow, Shock, and Vibration)
<b>Aging at <math>25^{\circ}\text{C}</math></b>	$\pm 1$ ppm First Year Maximum
<b>Supply Voltage</b>	$+2.5\text{Vdc} \pm 0.125\text{Vdc}$
<b>Input Current</b>	65mA Maximum (Excluding Load Termination Current)
<b>Output Voltage Logic High (Voh)</b>	750mVdc Typical, 600mVdc Minimum
<b>Output Voltage Logic Low (Vol)</b>	25mVdc Typical, 50mVdc Maximum
<b>Rise/Fall Time</b>	300pSec Typical, 350pSec Maximum (Measured over 20% to 80% of waveform)
<b>Duty Cycle</b>	50 $\pm 5$ (%) (Measured at 50% of waveform)
<b>Load Drive Capability</b>	50 Ohms to ground (Output and Complementary Output)
<b>Output Logic Type</b>	HCSSL
<b>Logic Control / Additional Output</b>	Output Enable (OE) and Complementary Output
<b>Output Control Input Voltage</b>	Vih of 70% of Vcc Minimum or No Connect to Enable Output and Complementary Output, Vil of 30% of Vcc Maximum to Disable Output and Complementary Output (High Impedance)
<b>Output Enable Current</b>	60mA Maximum (OE) Without Load
<b>Period Jitter (Deterministic)</b>	0.2pSec Typical
<b>Period Jitter (Random)</b>	2.0pSec Typical
<b>Period Jitter (RMS)</b>	1.5pSec Typical, 3.0pSec Maximum
<b>Period Jitter (pk-pk)</b>	20pSec Typical, 25pSec Maximum
<b>Period Jitter (Cycle to Cycle)</b>	10pSec Typical
<b>RMS Phase Jitter (Fj = 637kHz to 10MHz; Random)</b>	1.6pSec Typical
<b>RMS Phase Jitter (Fj = 1.5MHz to 22MHz; Random)</b>	0.6pSec Typical
<b>RMS Phase Jitter (Fj = 1.875MHz to 20MHz; Random)</b>	0.5pSec Typical
<b>Start Up Time</b>	10mSec Maximum
<b>Storage Temperature Range</b>	$-55^{\circ}\text{C}$ to $+125^{\circ}\text{C}$

## ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

<b>ESD Susceptibility</b>	MIL-STD-883, Method 3015, Class 2, HBM 2000V
<b>Flammability</b>	UL94-V0
<b>Mechanical Shock</b>	MIL-STD-883, Method 2002, Condition G, 30,000G
<b>Moisture Resistance</b>	MIL-STD-883, Method 1004
<b>Moisture Sensitivity Level</b>	J-STD-020, MSL 1
<b>Resistance to Soldering Heat</b>	MIL-STD-202, Method 210, Condition K
<b>Resistance to Solvents</b>	MIL-STD-202, Method 215
<b>Solderability</b>	MIL-STD-883, Method 2003 (Six I/O Pads on bottom of package only)
<b>Temperature Cycling</b>	MIL-STD-883, Method 1010, Condition B
<b>Thermal Shock</b>	MIL-STD-883, Method 1011, Condition B
<b>Vibration</b>	MIL-STD-883, Method 2007, Condition A, 20G

# EMSL12J2H-120.000M TR

## MECHANICAL DIMENSIONS (all dimensions in millimeters)



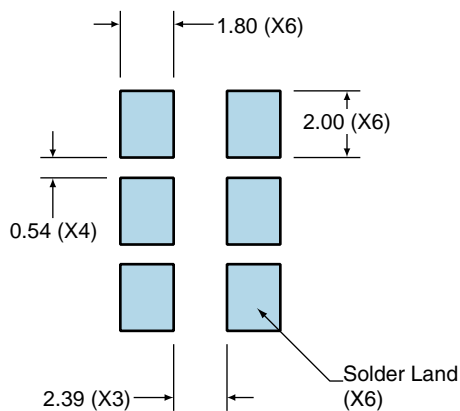
PIN	CONNECTION
1	Output Enable (OE)
2	No Connect
3	Case Ground
4	Output
5	Complementary Output
6	Supply Voltage

LINE	MARKING
1	XXXX or XXXXX XXXX or XXXXX=Ecliptek Manufacturing Lot Code

Note A: Center paddle is connected internally to oscillator ground (Pad 3).

## Suggested Solder Pad Layout

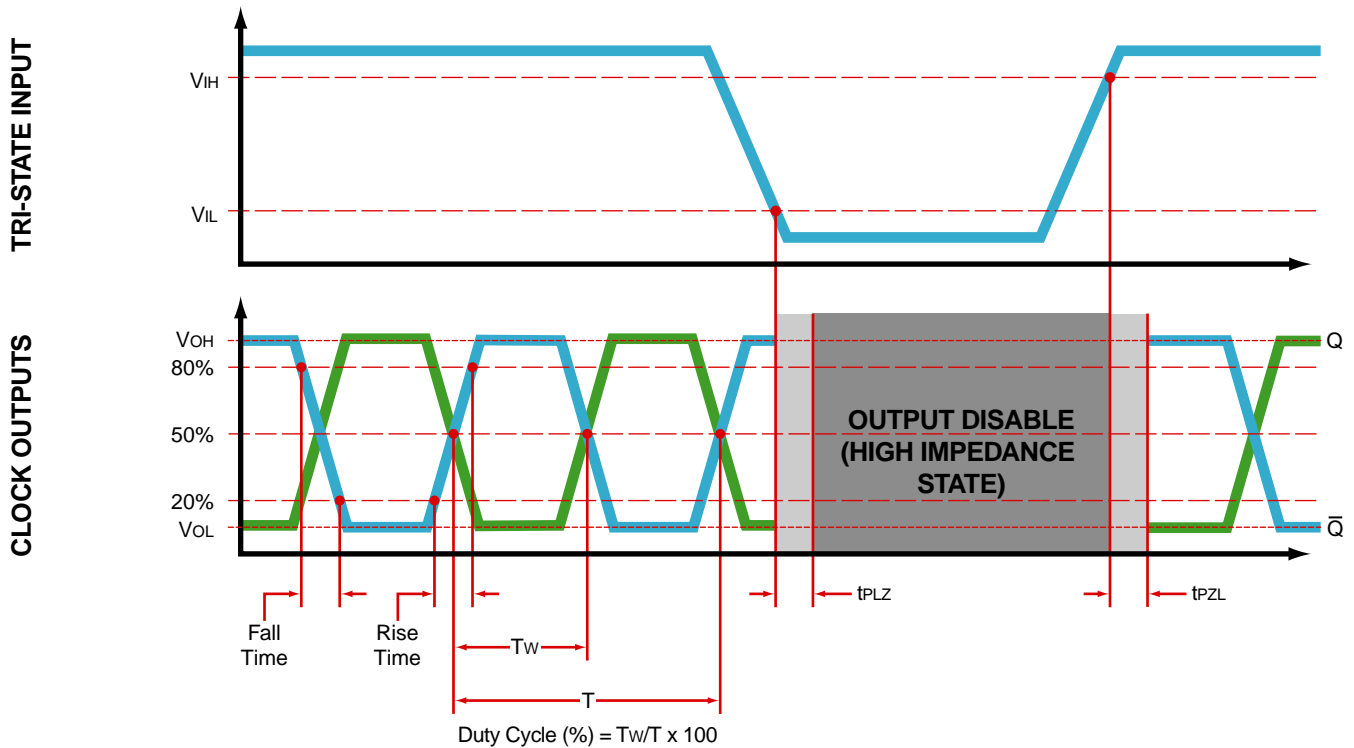
All Dimensions in Millimeters



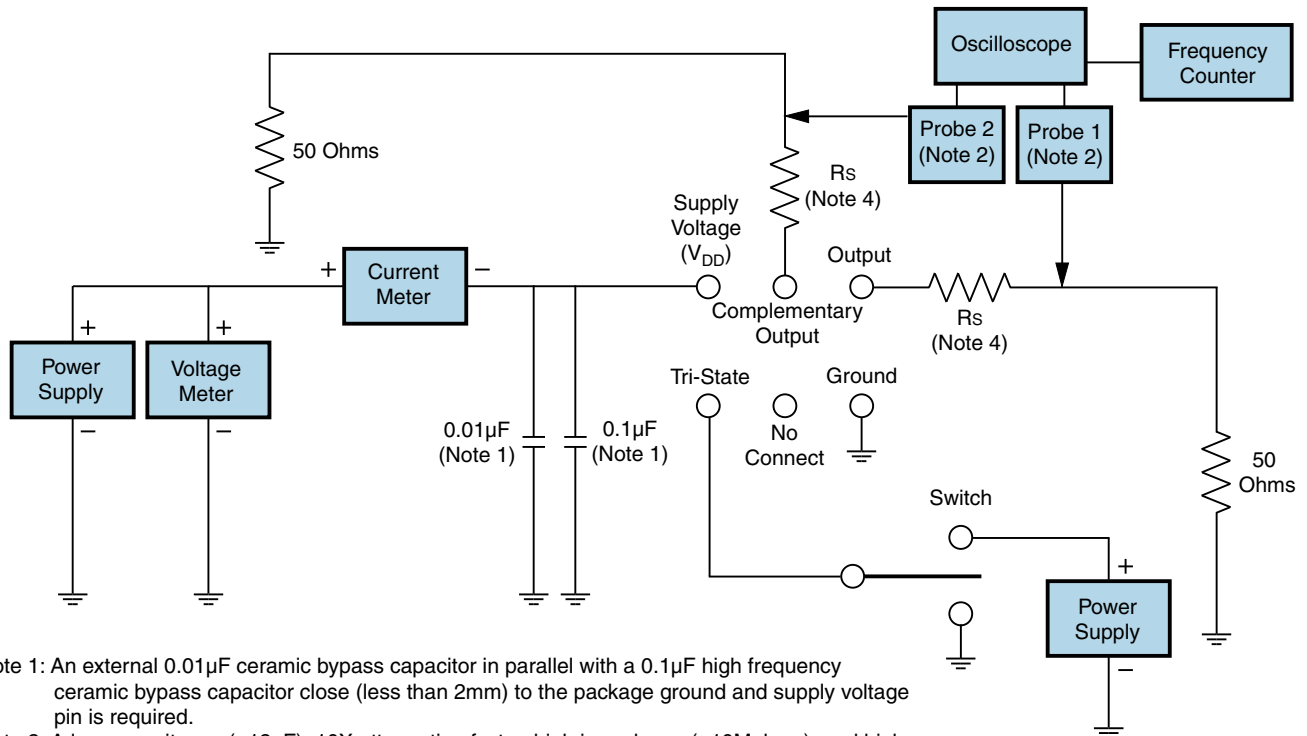
All Tolerances are  $\pm 0.1$

# EMSL12J2H-120.000M TR

## OUTPUT WAVEFORM & TIMING DIAGRAM



## Test Circuit for Tri-State and Complementary Output



Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less than 2mm) to the package ground and supply voltage pin is required.

Note 2: A low capacitance (<12pF), 10X attenuation factor, high impedance (>10Mohms), and high bandwidth (>500MHz) passive probe is recommended.

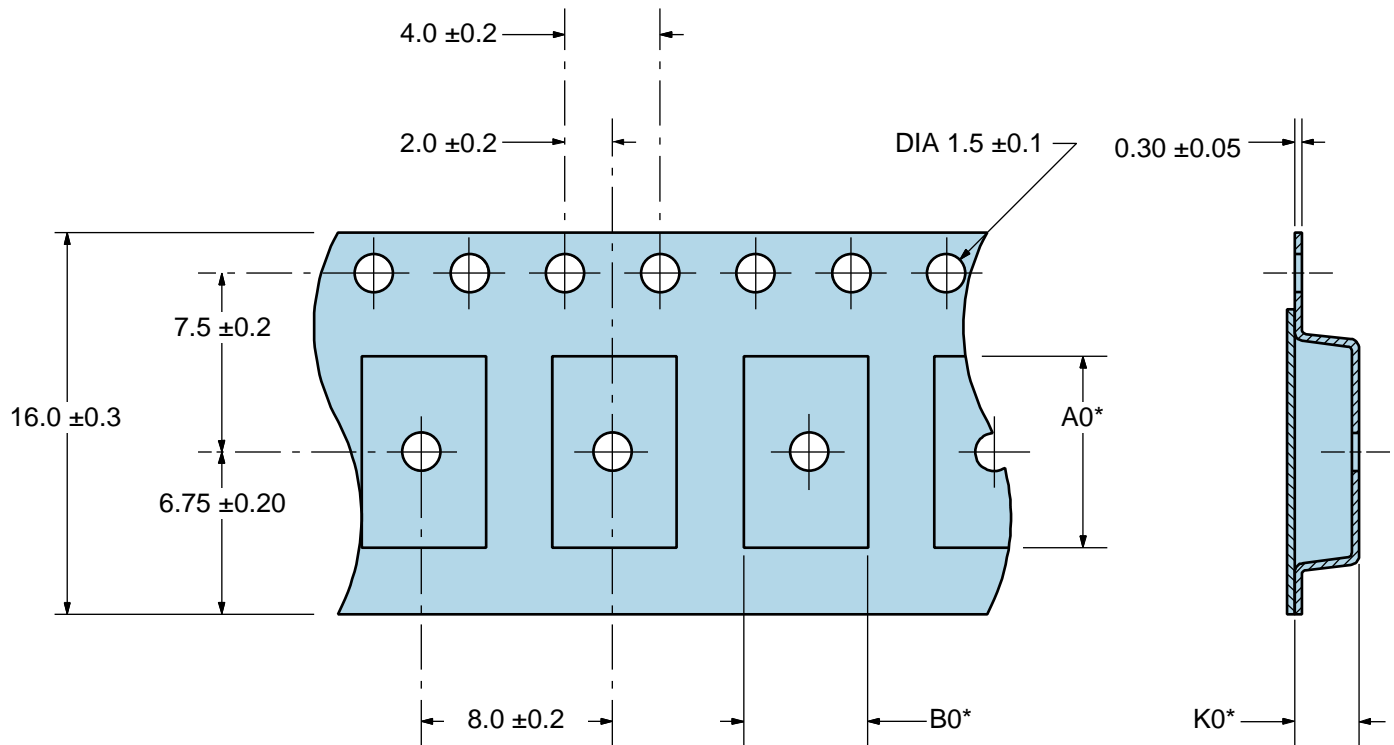
Note 3: Test circuit PCB traces need to be designed for a characteristic line impedance of 50 ohms.

Note 4: A 10 ohm to 33 ohm series resistor is required to limit overshoot. Rs value is circuit layout dependant.

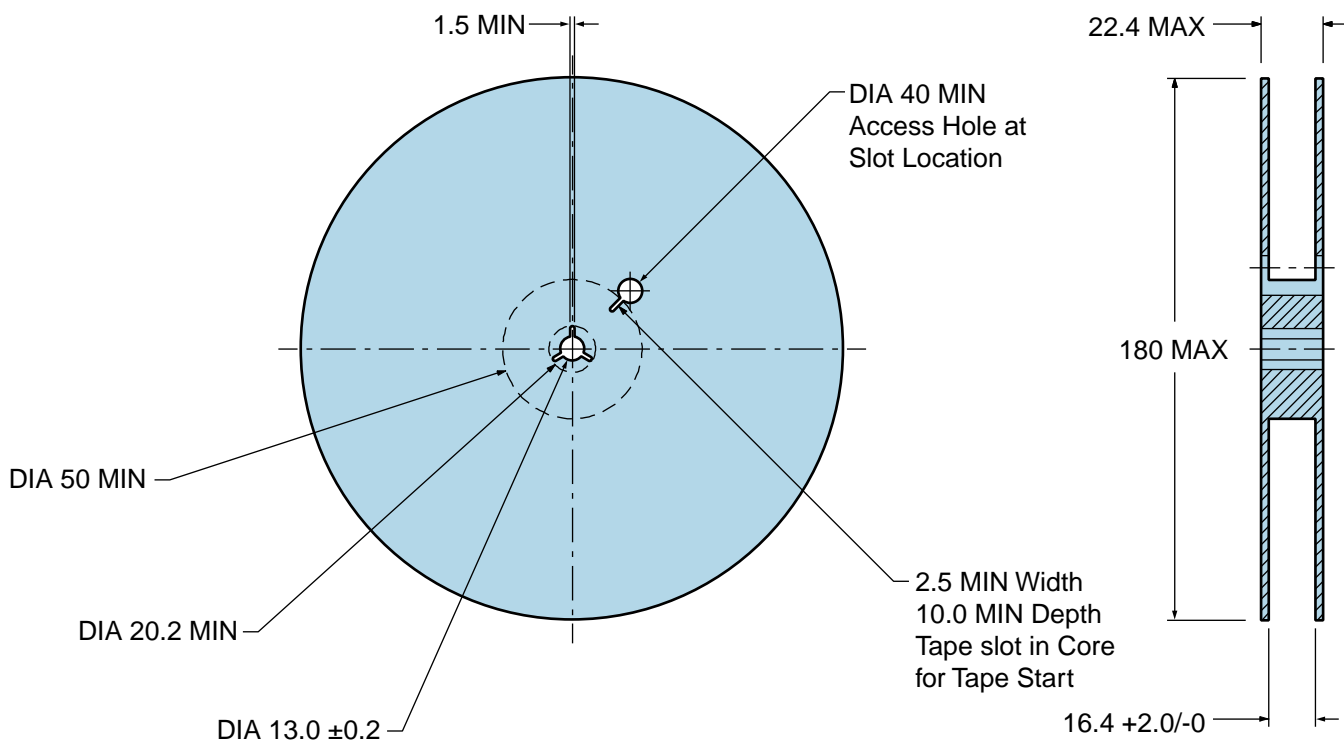
# EMSL12J2H-120.000M TR

## Tape & Reel Dimensions

Quantity Per Reel: 1,000 units



\*Compliant to EIA 481A



## Recommended Solder Reflow Methods



### High Temperature Infrared/Convection

**$T_S$  MAX to  $T_L$  (Ramp-up Rate)** 3°C/second Maximum

#### Preheat

- Temperature Minimum ( $T_S$  MIN) 150°C
- Temperature Typical ( $T_S$  TYP) 175°C
- Temperature Maximum ( $T_S$  MAX) 200°C
- Time ( $t_s$  MIN) 60 - 180 Seconds

**Ramp-up Rate ( $T_L$  to  $T_P$ )** 3°C/second Maximum

#### Time Maintained Above:

- Temperature ( $T_L$ ) 217°C
- Time ( $t_L$ ) 60 - 150 Seconds

**Peak Temperature ( $T_P$ )** 260°C Maximum for 10 Seconds Maximum

**Target Peak Temperature ( $T_P$  Target)** 250°C +0/-5°C

**Time within 5°C of actual peak ( $t_p$ )** 20 - 40 seconds

**Ramp-down Rate** 6°C/second Maximum

**Time 25°C to Peak Temperature (t)** 8 minutes Maximum

**Moisture Sensitivity Level** Level 1

## Recommended Solder Reflow Methods



### Low Temperature Infrared/Convection 240°C

<b><math>T_S</math> MAX to <math>T_L</math> (Ramp-up Rate)</b>	5°C/second Maximum
<b>Preheat</b>	
- Temperature Minimum ( $T_S$ MIN)	N/A
- Temperature Typical ( $T_S$ TYP)	150°C
- Temperature Maximum ( $T_S$ MAX)	N/A
- Time ( $t_S$ MIN)	60 - 120 Seconds
<b>Ramp-up Rate (<math>T_L</math> to <math>T_P</math>)</b>	5°C/second Maximum
<b>Time Maintained Above:</b>	
- Temperature ( $T_L$ )	150°C
- Time ( $t_L$ )	200 Seconds Maximum
<b>Peak Temperature (<math>T_P</math>)</b>	240°C Maximum
<b>Target Peak Temperature (<math>T_P</math> Target)</b>	240°C Maximum 1 Time / 230°C Maximum 2 Times
<b>Time within 5°C of actual peak (<math>t_p</math>)</b>	10 seconds Maximum 2 Times / 80 seconds Maximum 1 Time
<b>Ramp-down Rate</b>	5°C/second Maximum
<b>Time 25°C to Peak Temperature (t)</b>	N/A
<b>Moisture Sensitivity Level</b>	Level 1

### Low Temperature Manual Soldering

185°C Maximum for 10 seconds Maximum, 2 times Maximum.

### High Temperature Manual Soldering

260°C Maximum for 5 seconds Maximum, 2 times Maximum.