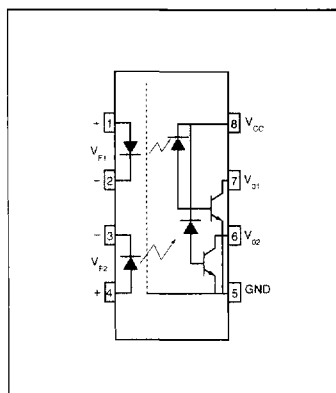
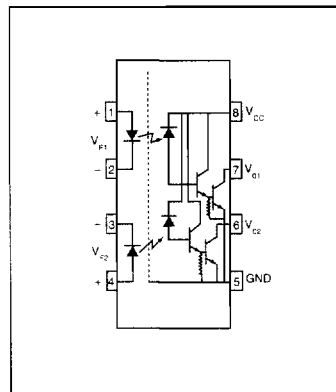


Dual Channel Phototransistor Output: GaAs Input										
Part Number	CTR @ $\leq 10 \text{ mA } I_F(\%)$		BV_{CEO} (V)	BV_{ECO} (V)	$V_{CE(sat)}$ (V)	t_{ON}/t_{OFF} (μs)	V_{ISO} AC[RMS]	Pkg dwg	Agency Approvals (see pg 23)	
	min	max	min	min	max	max				
MCT6	20	—	30	6	0.4	—	5.3 kV	D	A, U	
MCT61	50	—	30	6	0.4	—	5.3 kV	D	A, U	
MCT62	100	—	30	6	0.4	—	5.3 kV	D	A, U	
MCT9001	50	600	55	7	0.4	—	5.3 kV	D	A, U	



Dual Channel 1Mbit/s High-Speed Transistor Output: GaAsP Input										
Part Number	I_F (mA)	V_{OL} (V) max	V_{CC} (V) max	t_{PLH}/t_{PHL} (μs) max	I_{OH} (μA) max	CM_H/CM_L (kV/ μs) min	V_{ISO} AC[RMS]	Pkg dwg	Agency Approvals (see pg 23)	
HCPL-2530	16	0.5	30	1.5/1.5	0.5	1/-1	2.5 kV	D	A, U	
HCPL-2531	16	0.5	30	0.8/0.8	0.5	1/-1	2.5 kV	D	A, U	



Dual Channel Split Darlington Output: GaAsP Input										
Part Number	I_F (mA)	V_{OL} (V) max	V_{CC} (V) max	t_{PHL}/t_{PLH} (μs) max	I_{OH} (μA) max	CM_H/CM_L (kV/ μs) min	V_{ISO} AC[RMS]	Pkg dwg	Agency Approvals (see pg 23)	
HCPL-2730	1.6	0.4	7	20/35	100	1/-1	2.5 kV	D	A, U	
HCPL-2731	0.5	0.4	18	2/10	100	1/-1	2.5 kV	D	A, U	