

IMX3112 Preview

I3C Basic 1:2 Bus Multiplexer

The IMX3112 is a dual bidirectional 1:2 translating multiplexer for I3C/I2C bus. The input HSDA/H_SCL pair can fan out to two output pairs (or channels), either individual output channel can be selected.

Applications with slave devices that share the same address can use this multiplexer to prevent slave address conflicts. By selecting the proper output channel, the I3C/I2C master can access a large number of devices across both ports.

Features

- 1:2 bus multiplexer
- Two-wire programmable I2C or I3C bus serial interface
- Single device load on the Host bus
- Up to 12.5MHz transfer rate
- Single 1.8V power supply input
- 1.0V VLDO output at VIO pin
- Supports 1.0V, 1.1V, and 1.2V Push-Pull IO levels
- Supports 1.0V, 1.1V, 1.2V, 1.8V, 2.5V, and 3.3V open-drain IO levels
- Integrated Temperature Sensor; 0.5°C accuracy with 0.25°C resolution
- Packet Error Check (PEC) function
- Parity Error Check function
- Bus reset function
- In-band Interrupt (IBI)
- Up to 8 unique addresses
- Programmable I2C, I3C bus addressing scheme
- 9-contact thermally enhanced DFN package
- Temperature: Industrial range of -40°C to 125°C

Product Options

Orderable Part ID	Part Status	Pkg. Type	Lead Count (#)	Temp. Range	Carrier Type	Package	Buy Sample
IMX3112-YOB000NCG8	Preview	DFN	8	0 to 70°C	Reel	Package Info	Buy / Quote

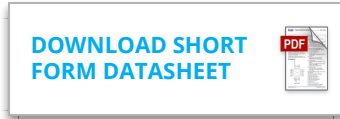
Documentation & Downloads

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Title	Type	Format	File Size	Date
Datasheets & Errata				
IMX3112 Short-Form Datasheet	Short Form Datasheet	PDF	41 KB	Feb 27, 2020
Other				
Memory Multiplexer Family Overview	Overview	PDF	807 KB	Aug 2, 2019

News & Additional Resources

	Date
IDT Introduces ONFI 4.1 1:4 Flash Memory Expander for High-Performance, High-Density Solid-State Drives	Aug 7, 2019



Quick Links

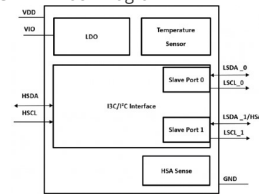
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- Tools & Support
- Images

Tools & Support

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Images

IMX3112 - Block Diagram



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