

66143

QUAD CHANNEL HERMETIC 20 PIN LCC, OPTICALLY COUPLED ISOLATOR



11-11-15

Features:

- High Reliability
- Base lead provided for conventional transistor biasing
- Very high gain, high voltage transistor
- Stability over wide temperature range.
- +1kVdc electrical isolation
- Screening available

Applications:

- Eliminate ground loops
- Level shifting
- Line receiver
- Switching power supplies
- Motor control

DESCRIPTION

The Mii 66143 -X02, -X03 and -X04 are optically coupled isolators, consisting of four GaAlAs LEDs and four silicon phototransistors mounted and coupled in a miniature surface mount hermetic leadless chip carrier. All electrical characteristics of each channel are identical to the JEDEC registered 4N47 (-X02), 4N48 (-X03) and 4N49 (-X04). Each unit contains four channels. These solid state couplers are ideal for designs where board space and device weight are important design considerations.

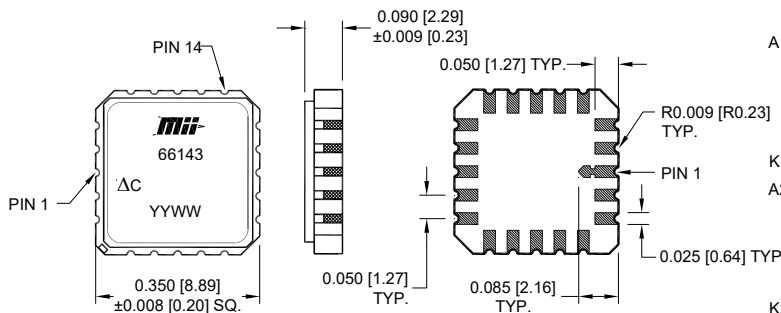
ABSOLUTE MAXIMUM RATINGS

Input-to-output Voltage (see Note 1)	±1kV
Collector-Base Voltage	45V
Collector-Emitter Voltage (Value applies to emitter-base open-circuited & the input-diode equal to zero)	40V
Emitter-Base Voltage	7V
Input Diode Reverse Voltage	3V
Input Diode Continuous Forward Current at (or below) 65°C Free-Air Temperature (see note 2)	40mA
Input Diode Power Dissipation	60mW
Continuous Collector Current	50mA
Continuous Transistor Power Dissipation at (or below) 25°C Free-Air Temperature (see Note 3)	300mW
Storage Temperature	-65°C to +150°C
Operating Free-Air Temperature Range	-55°C to +125°C
Lead Solder Temperature (10 seconds)	240°C

Notes:

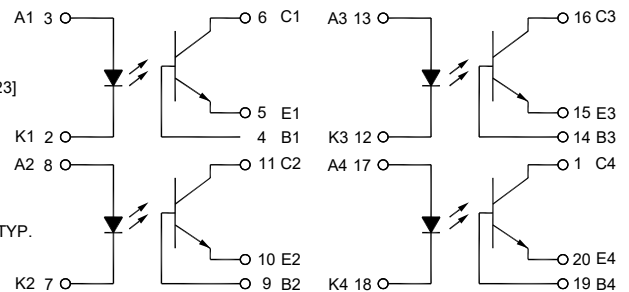
1. Measured with Inputs shorted together and outputs shorted together.
2. Derate linearly to 125°C free-air temperature at the rate of 0.67 mA/°C above 65°C.
3. Derate linearly to 125°C free-air temperature at the rate of 3 mW/°C.

Package Dimensions



ALL DIMENSIONS ARE IN INCHES [MILLIMETERS]

Schematic Diagram



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## ELECTRICAL CHARACTERISTICS

T<sub>A</sub> = 25°C unless otherwise specified.

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	TEST CONDITIONS	NOTE
Input Diode Static Reverse Current	IR			100	μA	V <sub>R</sub> = 3V	1
Input Diode Forward Voltage	V <sub>F</sub>	1.0		1.7	V	I <sub>F</sub> = 10mA	
		0.8		1.5			
		0.7		1.3			

## OUTPUT TRANSISTOR

T<sub>A</sub> = 25°C unless otherwise specified.

Collector-Base Breakdown Voltage	V <sub>(BR)CBO</sub>	45			V	I <sub>C</sub> = 100μA, I <sub>B</sub> = 0, I <sub>F</sub> = 0	
Collector-Emitter Breakdown Voltage	V <sub>(BR)CEO</sub>	40			V	I <sub>C</sub> = 1mA, I <sub>B</sub> = 0, I <sub>F</sub> = 0	
Emitter-Base Breakdown Voltage	V <sub>(BR)EBO</sub>	7			V	I <sub>C</sub> = 0mA, I <sub>E</sub> = 100μA, I <sub>F</sub> = 0	

## COUPLED CHARACTERISTICS

T<sub>A</sub> = 25°C unless otherwise specified.

On State Collector Current	-X02	I <sub>C(ON)</sub>	0.5		-	V <sub>CE</sub> = 5V, I <sub>B</sub> = 0, I <sub>F</sub> = 1mA	
T <sub>a</sub> = +25°C	-X03		1.0		5		
	-X04		2.0		10		
On State Collector Current	-X02	I <sub>C(ON)</sub>	0.7			V <sub>CE</sub> = 5V, I <sub>B</sub> = 0, I <sub>F</sub> = 2mA	
T <sub>a</sub> = -55°C	-X03		1.4				
	-X04		2.8				
On State Collector Current	-X02	I <sub>C(ON)</sub>	0.5			V <sub>CE</sub> = 5V, I <sub>B</sub> = 0, I <sub>F</sub> = 2mA	
T <sub>a</sub> = +100°C	-X03		1.0				
	-X04		2.0				
On-State Collector Base Current		I <sub>CB(ON)</sub>	30			V <sub>CB</sub> = 5V, I <sub>E</sub> = 0, I <sub>F</sub> = 10mA	
Off State Collector Current		I <sub>C(OFF)</sub>			100	V <sub>CE</sub> = 20V, I <sub>B</sub> = 0, I <sub>F</sub> = 0mA	1
Off State Collector Current, T <sub>a</sub> = +100°C		I <sub>C(OFF)</sub>			100	V <sub>CE</sub> = 20V, I <sub>B</sub> = 0, I <sub>F</sub> = 0mA	1
Collector-Base Dark Current		I <sub>CB(OFF)</sub>			10	V <sub>CB</sub> = 20V, I <sub>E</sub> = 0, I <sub>F</sub> = 0	
Collector-Emitter Saturation Voltage	-X02	V <sub>CE(SAT)</sub>			0.3	I <sub>F</sub> = 2mA, I <sub>C</sub> = 0.5mA, I <sub>B</sub> = 0 I <sub>F</sub> = 2mA, I <sub>C</sub> = 1mA, I <sub>B</sub> = 0 I <sub>F</sub> = 2mA, I <sub>C</sub> = 2mA, I <sub>B</sub> = 0	
	-X03				0.3		
	-X04				0.3		
Input to Output Resistance		R <sub>IO</sub>	10 <sup>11</sup>			V <sub>IN-OUT</sub> = 1kV, t <sub>w</sub> = 100μs, duty cycle ≤ 1%	2
Input to Output Capacitance		C <sub>IO</sub>			5	F = 1MHz, V <sub>IN-OUT</sub> = 0	
Rise Time (Phototransistor Operation)	-X02	t <sub>r</sub>		10	20	V <sub>CC</sub> = 10V, I <sub>F</sub> = 5mA, R <sub>L</sub> = 100Ω, I <sub>B</sub> = 0	
or	-X03	or		10	20		
Fall Time	-X04	t <sub>f</sub>		15	25		
Rise Time (Photodiode Operation)	-X02	t <sub>r</sub>		0.85	3	V <sub>CC</sub> = 10V, I <sub>F</sub> = 5mA, R <sub>L</sub> = 100Ω, I <sub>E</sub> = 0	1
or	-X03	or		0.85	3		
Fall Time	-X04	t <sub>f</sub>		0.85	3		

### NOTES:

- Parameter applies to all four channels.
- These parameters are measured between all phototransistor leads shorted together and with both input diode leads shorted together.

### RECOMMENDED OPERATING CONDITIONS:

PARAMETER.	SYMBOL	MIN	MAX	UNITS
Input Current, Low Level	I <sub>FL</sub>	0	100	μA
Input Current, High Level	I <sub>FH</sub>	1	10	mA
Supply Voltage	V <sub>CC</sub>	5.0	20	V

### SELECTION GUIDE

PART NUMBER	PART DESCRIPTION
66143-002	4N47 Commercial
66143-003	4N48 Commercial
66143-004	4N49 Commercial
66143-102	4N47 Screened
66143-103	4N48 Screened
66143-104	4N49 Screened