

T-39-11

Philips Components

Data sheet	
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date of issue	March 1991

BUK427-600 B

PowerMOS transistor

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GENERAL DESCRIPTION

N-channel enhancement mode field-effect power transistor in a plastic full pack envelope. The device is intended for use in Switched Mode Power Supplies (SMPS), motor control, welding, DC/DC and AC/DC converters, and in general purpose switching applications.

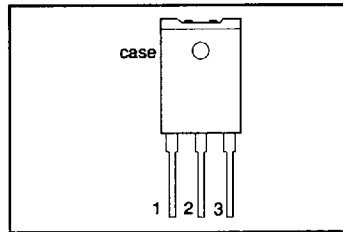
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	UNIT
V_{DS}	Drain-source voltage	600	V
I_D	Drain current (DC)	3.9	A
P_{tot}	Total power dissipation	45	W
$R_{DS(ON)}$	Drain-source on-state resistance	1.2	Ω

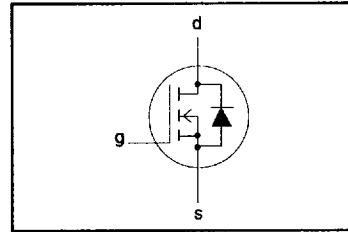
PINNING - SOT199

PIN	DESCRIPTION
1	gate
2	drain
3	source
case	isolated

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134)

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{DS}	Drain-source voltage	-	-	600	V
V_{DGR}	Drain-gate voltage	$R_{GS} = 20 \text{ k}\Omega$	-	600	V
$\pm V_{GS}$	Gate-source voltage	-	-	30	V
I_D	Drain current (DC)	$T_{hs} = 25 \text{ }^\circ\text{C}$	-	3.9	A
I_D	Drain current (DC)	$T_{hs} = 100 \text{ }^\circ\text{C}$	-	2.5	A
I_{DM}	Drain current (pulse peak value)	$T_{hs} = 25 \text{ }^\circ\text{C}$	-	15.6	A
P_{tot}	Total power dissipation	$T_{hs} = 25 \text{ }^\circ\text{C}$	-	45	W
T_{stg}	Storage temperature	-	- 55	150	$^\circ\text{C}$
T_J	Junction Temperature	-	-	150	$^\circ\text{C}$

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THERMAL RESISTANCES

From junction to heatsink	with heatsink compound	$R_{th\ j-hs} = 2.8 \text{ K/W}$
From junction to ambient	-	$R_{th\ j-a} = 35 \text{ K/W}$

STATIC CHARACTERISTICS $T_{hs} = 25 \text{ }^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{(BR)DSS}$	Drain-source breakdown voltage	$V_{GS} = 0 \text{ V}; I_D = 0.25 \text{ mA}$	600	-	-	V
$V_{GS(TO)}$	Gate threshold voltage	$V_{DS} = V_{GS}; I_D = 1 \text{ mA}$	2.1	3.0	4.0	V
I_{DSS}	Zero gate voltage drain current	$V_{DS} = 600 \text{ V}; V_{GS} = 0 \text{ V}; T_J = 25 \text{ }^\circ\text{C}$	-	2	20	μA
I_{DSS}	Zero gate voltage drain current	$V_{DS} = 600 \text{ V}; V_{GS} = 0 \text{ V}; T_J = 125 \text{ }^\circ\text{C}$	-	0.1	1.0	mA
I_{GSS}	Gate source leakage current	$V_{GS} = \pm 30 \text{ V}; V_{DS} = 0 \text{ V}$	-	10	100	nA
$R_{DS(ON)}$	Drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 6.5 \text{ A}$	-	1.0	1.2	Ω

DYNAMIC CHARACTERISTICS $T_{hs} = 25 \text{ }^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
g_{fs}	Forward transconductance	$V_{DS} = 25 \text{ V}; I_D = 6.5 \text{ A}$	5.0	8.0	-	S
C_{iss}	Input capacitance	$V_{GS} = 0 \text{ V}; V_{DS} = 25 \text{ V}; f = 1 \text{ MHz}$	-	1500	1800	pF
C_{oss}	Output capacitance		-	170	270	pF
C_{rss}	Feedback capacitance		-	70	120	pF
$t_{d\ on}$	Turn-on delay time	$V_{DD} = 30 \text{ V}; I_D = 2.8 \text{ A};$	-	20	40	ns
t_r	Turn-on rise time	$V_{GS} = 10 \text{ V}; R_{GS} = 50 \text{ } \Omega;$	-	60	90	ns
$t_{d\ off}$	Turn-off delay time	$R_{gen} = 50 \text{ } \Omega$	-	200	250	ns
t_f	Turn-off fall time		-	75	90	ns
L_d	Internal drain inductance	Measured from drain lead 6 mm from package to centre of die	-	5	-	nH
L_s	Internal source inductance	Measured from source lead 6 mm from package to source bond pad	-	12.5	-	nH

ISOLATION $T_{hs} = 25 \text{ }^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{isol}	Repetitive peak voltage from all three terminals to external heatsink	R.H. $\leq 65 \%$; clean and dustfree	-	-	2500	V
C_{isol}	Capacitance from T2 to external heatsink	$f = 1 \text{ MHz}$	-	22	-	pF

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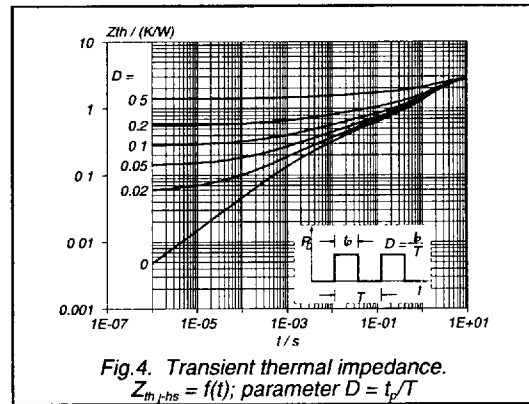
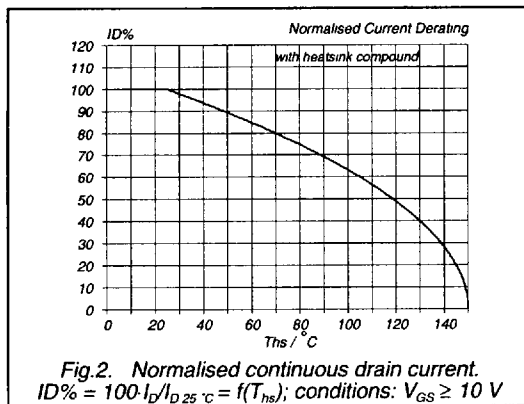
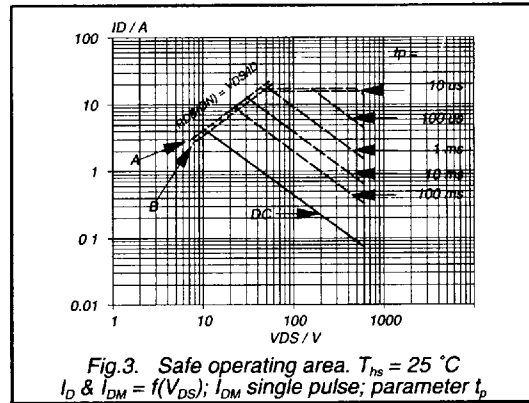
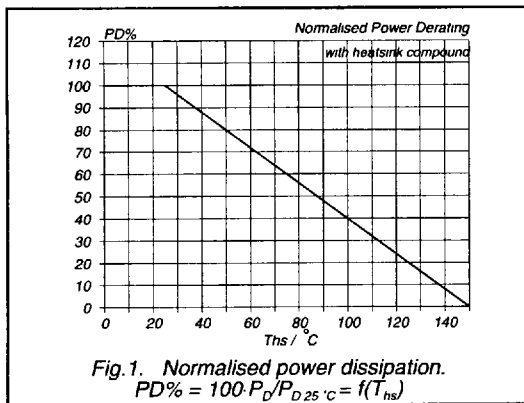
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REVERSE DIODE LIMITING VALUES AND CHARACTERISTICS

$T_{hs} = 25\text{ }^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{DR}	Continuous reverse drain current	-	-	-	4.3	A
I_{DRM}	Pulsed reverse drain current	-	-	-	17.2	A
V_{SD}	Diode forward voltage	$I_F = 4.3\text{ A}; V_{GS} = 0\text{ V}$	-	1.1	1.4	V
t_{rr}	Reverse recovery time	$I_F = 4.3\text{ A}; -di_F/dt = 100\text{ A}/\mu\text{s}; V_{GS} = 0\text{ V}; V_R = 100\text{ V}$	-	500	-	ns
Q_{rr}	Reverse recovery charge	$V_{GS} = 0\text{ V}; V_R = 100\text{ V}$	-	6.0	-	μC



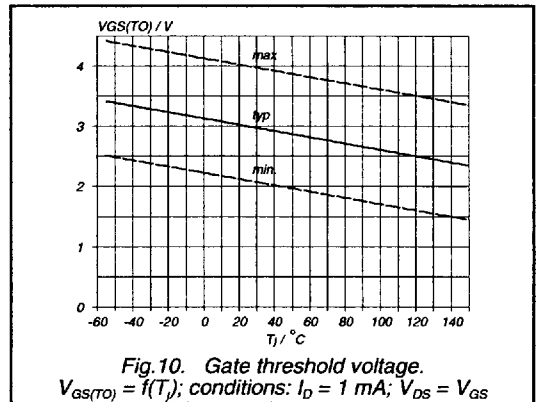
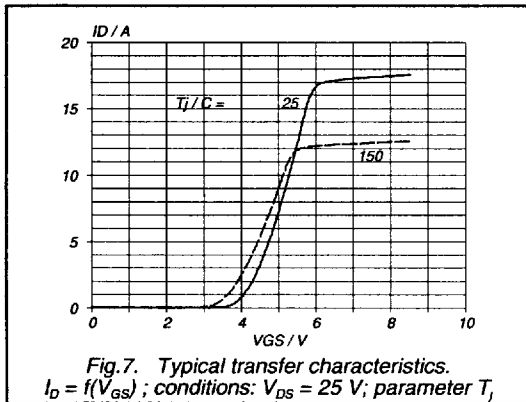
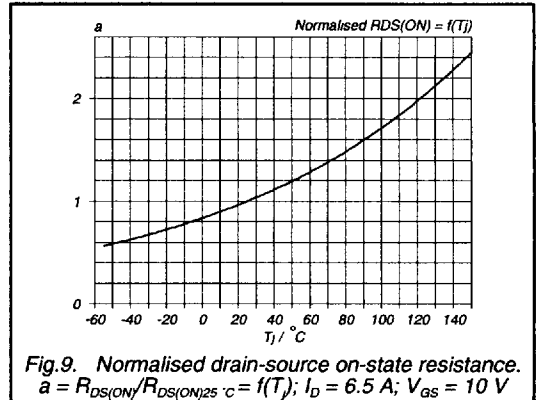
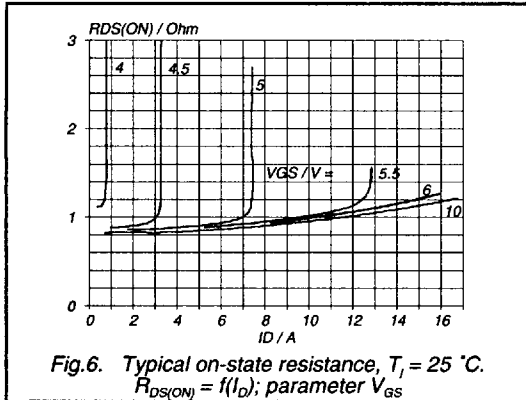
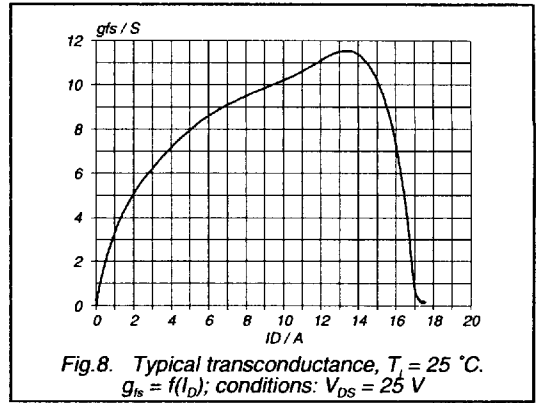
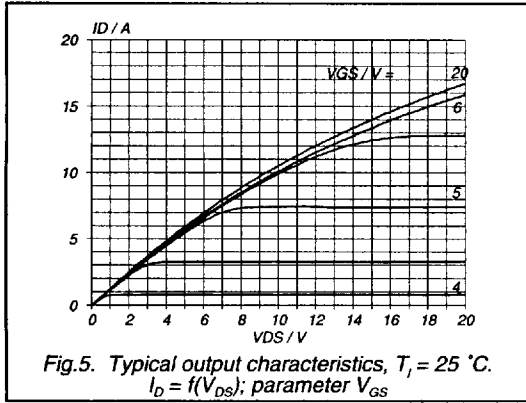
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