

## CLASS C, 800-960 MHz 12 VOLT POWER TRANSISTOR

NEM080481-12  
NEM081081-12  
NEM082081B-12  
NEM084081B-12

### FEATURES

- **LOW OPERATING VOLTAGE:** 13.5 V
- **TITANIUM/PLATINUM/GOLD METALLIZATION FOR HIGH RELIABILITY**
- **GAIN AS HIGH AS 7 dB**
- **RUGGED VSWR:**  $\infty$  at  $V_{CC} = 13.5$  V
- **IDEALLY SUITED FOR 800/960 MHz BAND MOBILE/CELLULAR RADIO APPLICATIONS**
- **LOW COST PACKAGES**
- **HIGH OUTPUT POWER:** 37 WATTS AT 860 MHz
- **COMMON BASE**

### DESCRIPTION

NEC's NEM0800 series of NPN epitaxial UHF power transistors are designed specifically for large volume mobile radio applications in the 800 & 960 MHz bands. The series is available in a low cost metal-ceramic stripline package offering power levels of 6, 12, 24 and 40 W. Internal matching is incorporated to simplify circuit design. The series provides high gain, high efficiency and a high resistance to burn-out with load mismatch. The NEM0800 series is complementary to NEC's range of power amplifier modules offering total discrete/modular design flexibility.

### ELECTRICAL CHARACTERISTICS (TA = 25°C)

PART NUMBER EIAJ <sup>1</sup> REGISTERED NUMBER PACKAGE OUTLINE		NEM080481E-12			NEM081081E-12			NEM082081B-12 2SC3282A			NEM084081B12 2SC3283A			
		81E			81E			81B			81B			
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX
P <sub>OUT</sub>	Output Power at V <sub>CC</sub> = 13.5 V f = 950 MHz, P <sub>IN</sub> = 1.5 W f = 950 MHz, P <sub>IN</sub> = 3.5 W f = 860 MHz, P <sub>IN</sub> = 4 W f = 860 MHz, P <sub>IN</sub> = 8 W	W	4	6		10	12.5		18	22		30	37	
η <sub>c</sub>	Collector Efficiency at V <sub>CC</sub> = 13.5 V f = 950 MHz, P <sub>IN</sub> = 1.5 W f = 950 MHz, P <sub>IN</sub> = 3.5 W f = 860 MHz, P <sub>IN</sub> = 4 W f = 860 MHz, P <sub>IN</sub> = 8 W	%	55	60		55	65		50	58		50	55	
VSWR	Voltage Standing Wave Ratio at V <sub>CC</sub> = 13.5 V P <sub>OUT</sub> = 4 W P <sub>OUT</sub> = 10 W P <sub>OUT</sub> = 20 W P <sub>OUT</sub> = 40 W				∞			∞			∞			∞
I <sub>CBO</sub>	Collector Cutoff Current at V <sub>CB</sub> = 20 V, I <sub>E</sub> = 0	mA		0.2			0.3				2			4
I <sub>EBO</sub>	Emitter Cutoff Current at V <sub>EB</sub> = 20 V, I <sub>E</sub> = 0	mA		0.2			0.3				2			4
h <sub>FE</sub>	DC Forward Current Gain at V <sub>CE</sub> = 10 V, I <sub>C</sub> = 0.3 A (pulsed) I <sub>C</sub> = 0.5 A (pulsed) I <sub>C</sub> = 1 A (pulsed) I <sub>C</sub> = 2 A (pulsed)		20	60	200	20	60	200	20	50	200	20	50	200
C <sub>OB</sub>	Output Capacitance at V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	pF		7	10		15	19.5		2.5	35		50	70

Note:

1. Electronic Industrial Association of Japan.

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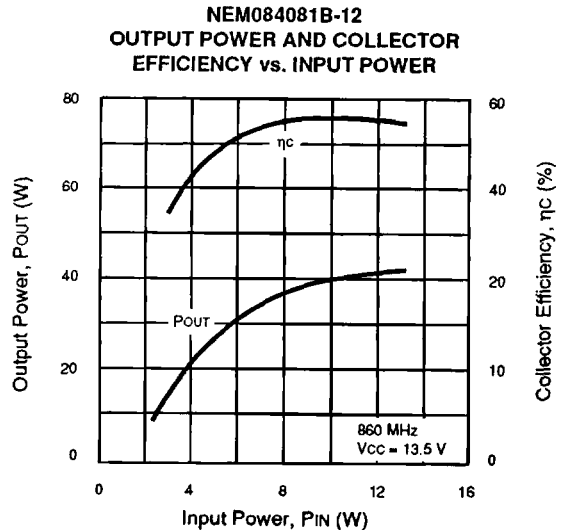
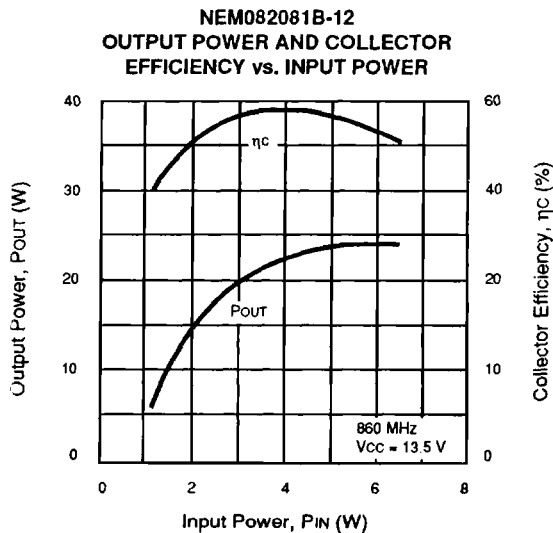
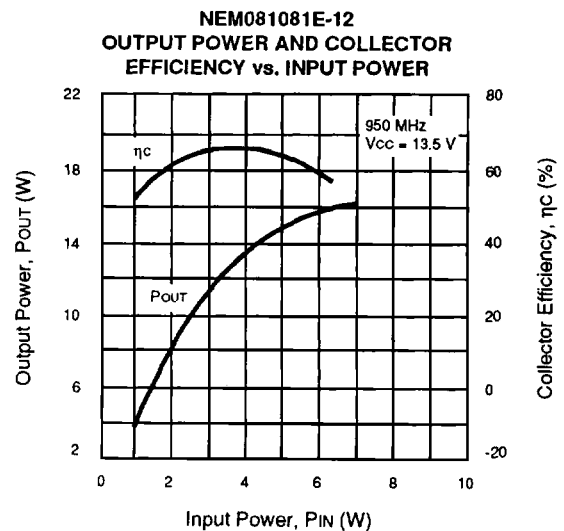
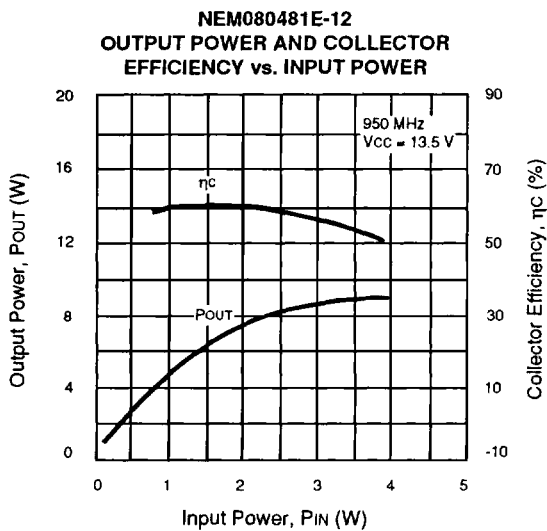
**ABSOLUTE MAXIMUM RATINGS<sup>1</sup>** (TA = 25°C)

PART NUMBER PACKAGE OUTLINE			NEM080481E-12 01	NEM081081E-12 01	NEM082081B-12 01	NEM084081B-1 01
SYMBOLS	PARAMETER	UNITS	RATINGS	RATINGS	RATINGS	RATINGS
V <sub>CB0</sub>	Collector to Base Voltage	V	35	35	35	35
V <sub>CE0</sub>	Collector to Emitter Voltage	V	18	18	16	16
V <sub>EB0</sub>	Emitter to Base Voltage	V	3	3	2.5	2.5
I <sub>C</sub>	Collector Current	A	1.5	3	7.5	15
R <sub>TH(J-C)</sub>	Thermal Resistance (Junction to Case)	°C/W	10	5	2.5	1.5
P <sub>T</sub>	Total Power Dissipation (T <sub>C</sub> = 25°C)	W	17.5	35	70	120
T <sub>J</sub>	Junction Temperature	°C	200	200	200	200
T <sub>STG</sub>	Storage Temperature	°C	-65 to +150	-65 to +150	-65 to +150	-65 to +150

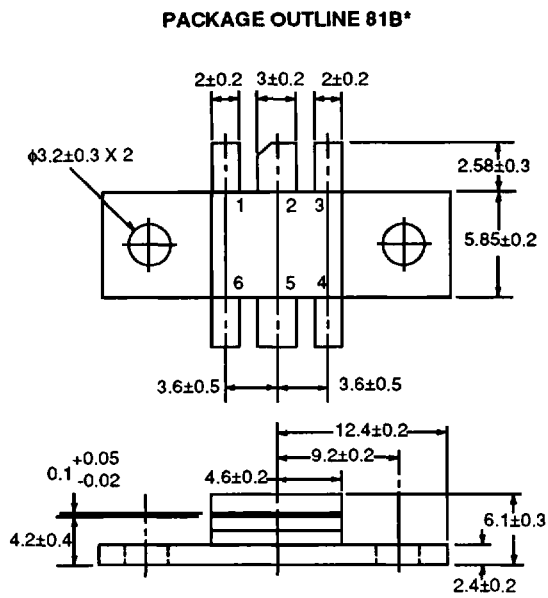
Notes:

1. Operation in excess of any one of these parameters may result in permanent damage.

**TYPICAL PERFORMANCE CURVES** (TA = 25°C)



**OUTLINE DIMENSIONS** (Units in mm)



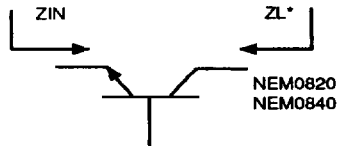
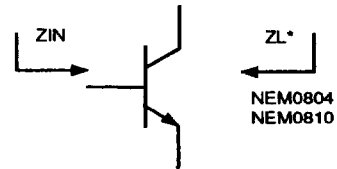
\*81E has emitter and base reversed.

**TYPICAL INPUT/OUTPUT IMPEDANCE LOAD<sup>1</sup>**

PART NUMBER	FREQUENCY (MHz)	Z <sub>IN</sub> (Ω)		Z <sub>L</sub> (Ω)	
		Real	Imaginary	Real	Imaginary
NEM080481E-12	950	3.22	+j6.6	6.36	-j4.7
NEM081081E-12	950	2.3	+j3.49	9.8	-j5.91
NEM082081B-12	860	2.5	+j0.5	2.23	+j0
NEM084081B-12	860	1.5	+j4.75	1.75	+j2.0

Note:

1. Typical Large Signal Impedances at V<sub>CC</sub> = 13.5 V, T<sub>A</sub> = 25°C.



\*Z<sub>L</sub> is optimum load impedance at rated output power.



**TEST CIRCUIT DIAGRAM**

